



ECE 4140/6240 Lab



VLSI Lab



Lab Material

- We will be using the ECE course website provided, blogs.gwu.edu/ece4140/
 - password: 41406240RF

Today's Objective

- Follow Lab #7

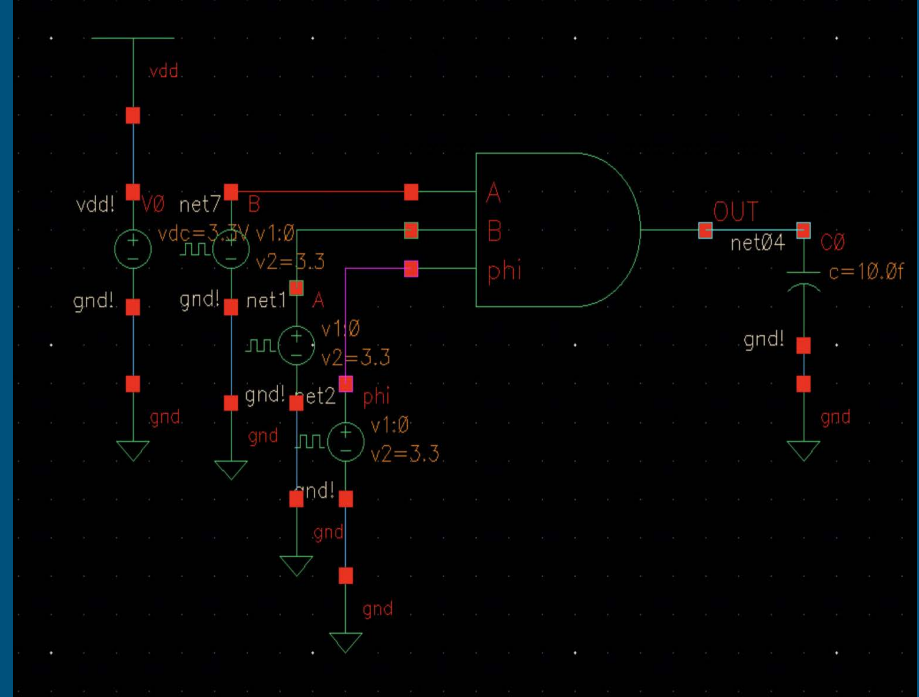
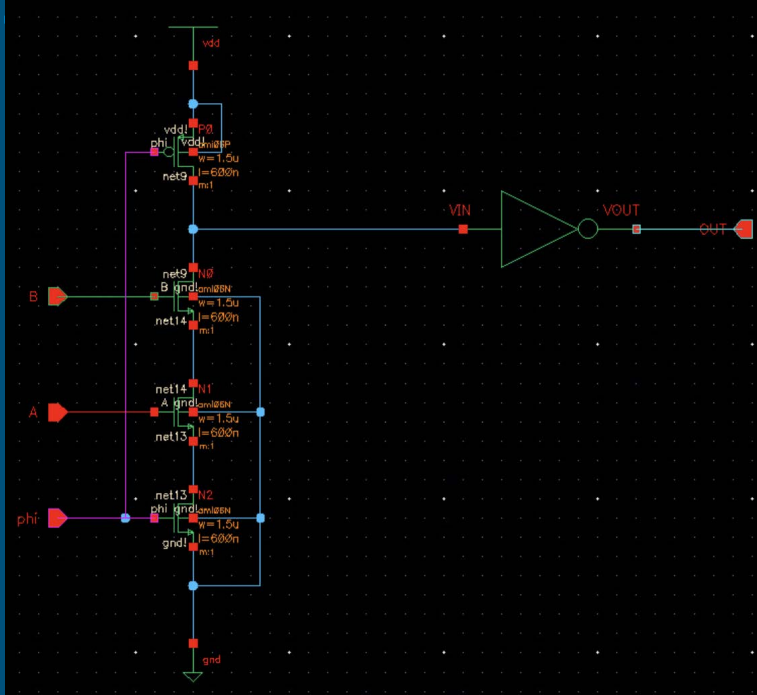
Things to Watch Out For

When making the schematic, consider adding pin ins/outs in place of testing equipment, and then making a separate testbench to test it all. A picture on the next slide better explains the way you can go about this

For the last step, simply use the ruler tool (Hotkey: k) to measure the area of your layout for the Domino AND Gate and compare it to your Static AND Gate. Do this just by taking the longest horizontal and vertical parts of each layout and multiplying

Things to Watch Out For

$v_{dc}=3.3V$, $c=10\text{ fF}$



Screenshots to include

Schematic

Symbol

Testbench

Layout

DRC 0 Errors

Extracted

LVS Match