

NVIDIA Research



Diving In



```
#include <thrust/host vector.h>
#include <thrust/device vector.h>
#include <thrust/sort.h>
int main(void)
   // generate 16M random numbers on the host
    thrust::host vector<int> h vec(1 << 24);</pre>
    thrust::generate(h vec.begin(), h vec.end(), rand);
   // transfer data to the device
    thrust::device vector<int> d vec = h vec;
   // sort data on the device
    thrust::sort(d vec.begin(), d vec.end());
    // transfer data back to host
    thrust::copy(d_vec.begin(), d_vec.end(), h_vec.begin());
   return 0;
```

Objectives



- Programmer productivity
 - Rapidly develop complex applications
 - Leverage parallel primitives
- Encourage generic programming
 - Don't reinvent the wheel
 - E.g. one reduction to rule them all
- High performance
 - With minimal programmer effort
- Interoperability
 - Integrates with CUDA C/C++ code

What is Thrust?



- C++ template library for CUDA
 - Mimics Standard Template Library (STL)
- Containers
 - thrust::host vector<T>
 - thrust::device_vector<T>
- Algorithms
 - thrust::sort()
 - thrust::reduce()
 - thrust::inclusive_scan()
 - Etc.

Containers



- Make common operations concise and readable
 - Hides cudaMalloc, cudaMemcpy and cudaFree

```
// allocate host vector with two elements
thrust::host_vector<int> h_vec(2);

// copy host vector to device
thrust::device_vector<int> d_vec = h_vec;

// manipulate device values from the host
d_vec[0] = 13;
d_vec[1] = 27;

std::cout << "sum: " << d_vec[0] + d_vec[1] << std::endl;

// vector memory automatically released w/ free() or cudaFree()</pre>
```

Containers



- Compatible with STL containers
 - Eases integration
 - vector, list, map,...

```
// list container on host
std::list<int> h_list;
h_list.push_back(13);
h_list.push_back(27);

// copy list to device vector
thrust::device_vector<int> d_vec(h_list.size());
thrust::copy(h_list.begin(), h_list.end(), d_vec.begin());

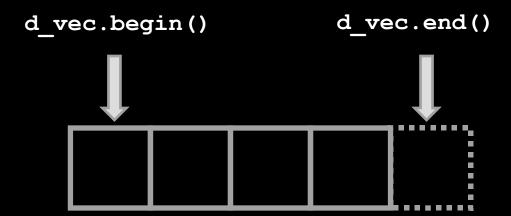
// alternative method
thrust::device_vector<int> d_vec(h_list.begin(), h_list.end());
```



Sequences defined by pair of iterators

```
// allocate device vector
thrust::device_vector<int> d_vec(4);

d_vec.begin(); // returns iterator at first element of d_vec
d_vec.end() // returns iterator one past the last element of d_vec
// [begin, end) pair defines a sequence of 4 elements
```





Iterators act like pointers

```
// allocate device vector
thrust::device vector<int> d vec(4);
thrust::device vector<int>::iterator begin = d vec.begin();
thrust::device vector<int>::iterator end = d vec.end();
int length = end - begin; // compute size of sequence [begin, end)
end = d_vec.begin() + 3; // define a sequence of 3 elements
               begin
                                   end
```



Use iterators like pointers

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- Track memory space (host/device)
 - Guides algorithm dispatch

```
// initialize random values on host
thrust::host_vector<int> h_vec(1000);
thrust::generate(h_vec.begin(), h_vec.end(), rand);

// copy values to device
thrust::device_vector<int> d_vec = h_vec;

// compute sum on host
int h_sum = thrust::reduce(h_vec.begin(), h_vec.end());

// compute sum on device
int d_sum = thrust::reduce(d_vec.begin(), d_vec.end());
```



Convertible to raw pointers

```
// allocate device vector
thrust::device_vector<int> d_vec(4);

// obtain raw pointer to device vector's memory
int * ptr = thrust::raw_pointer_cast(&d_vec[0]);

// use ptr in a CUDA C kernel
my_kernel<<<<N/256, 256>>>(N, ptr);

// Note: ptr cannot be dereferenced on the host!
```



Wrap raw pointers with device ptr

```
int N = 10;
// raw pointer to device memory
int * raw ptr;
cudaMalloc((void **) &raw ptr, N * sizeof(int));
// wrap raw pointer with a device ptr
thrust::device ptr<int> dev ptr(raw ptr);
// use device ptr in thrust algorithms
thrust::fill(dev ptr, dev ptr + N, (int) 0);
// access device memory through device ptr
dev ptr[0] = 1;
// free memory
cudaFree(raw ptr);
```

Namespaces



- C++ supports namespaces
 - Thrust uses thrust namespace

```
thrust::device_vector
thrust::copy
```

STL uses std namespace

```
std::vector
std::list
```

Avoids collisions

```
thrust::sort()
std::sort()
```

For brevity

using namespace thrust;

Recap



Containers

- Manage host & device memory
- Automatic allocation and deallocation
- Simplify data transfers

Iterators

- Behave like pointers
- Keep track of memory spaces
- Convertible to raw pointers

Namespaces

Avoids collisions

C++ Background



Function templates

```
// function template to add numbers (type of T is variable)
template< typename T >
T add(T a, T b)
{
    return a + b;
}

// add integers
int x = 10; int y = 20; int z;
z = add<int>(x,y); // type of T explicitly specified
z = add(x,y); // type of T determined automatically
// add floats
float x = 10.0f; float y = 20.0f; float z;
z = add<float>(x,y); // type of T explicitly specified
z = add(x,y); // type of T explicitly specified
z = add(x,y); // type of T determined automatically
```

C++ Background



16

Function objects (Functors)

```
// templated functor to add numbers
template< typename T >
class add
   public:
   T operator()(T a, T b)
       return a + b;
};
int x = 10; int y = 20; int z;
add<int> func; // create an add functor for T=int
z = func(x,y); // invoke functor on x and y
float x = 10; float y = 20; float z;
add<float> func; // create an add functor for T=float
z = func(x,y); // invoke functor on x and y
```

C++ Background



Generic Algorithms



- Thrust provides many standard algorithms
 - Transformations
 - Reductions
 - Prefix Sums
 - Sorting
- Generic definitions
 - General Types
 - Built-in types (int, float, ...)
 - User-defined structures
 - General Operators
 - reduce with plus operator
 - scan with maximum operator



General types and operators

```
#include <thrust/reduce.h>

// declare storage
device_vector<int> i_vec = ...
device_vector<float> f_vec = ...

// sum of integers (equivalent calls)
reduce(i_vec.begin(), i_vec.end());
reduce(i_vec.begin(), i_vec.end(), 0, plus<int>());

// sum of floats (equivalent calls)
reduce(f_vec.begin(), f_vec.end());
reduce(f_vec.begin(), f_vec.end(), 0.0f, plus<float>());

// maximum of integers
reduce(i_vec.begin(), i_vec.end(), 0, maximum<int>());
```



General types and operators

```
struct negate float2
    host device
   float2 operator()(float2 a)
       return make float2(-a.x, -a.y);
};
// declare storage
device vector<float2> input = ...
device vector<float2> output = ...
// create functor
negate float2 func;
// negate vectors
transform(input.begin(), input.end(), output.begin(), func);
```



General types and operators

```
// compare x component of two float2 structures
struct compare float2
     host device
   bool operator()(float2 a, float2 b)
       return a.x < b.x;</pre>
};
// declare storage
device vector<float2> vec = ...
// create comparison functor
compare float2 comp;
// sort elements by x component
sort(vec.begin(), vec.end(), comp);
```



Operators with State

```
// compare x component of two float2 structures
struct is greater than
   int threshold;
   is greater than(int t) { threshold = t; }
    host device
   bool operator()(int x) { return x > threshold; }
} ;
device vector<int> vec = ...
// create predicate functor (returns true for x > 10)
is greater than pred(10);
// count number of values > 10
int result = count if(vec.begin(), vec.end(), pred);
```

Recap



- Algorithms
 - Generic
 - Support general types and operators
 - Statically dispatched based on iterator type
 - Memory space is known at compile time
 - Have default arguments
 - reduce (begin, end)
 - reduce(begin, end, init, binary_op)



- Behave like "normal" iterators
 - Algorithms don't know the difference

• Examples

- constant iterator
- counting_iterator
- transform iterator
- permutation iterator
- zip_iterator



- constant_iterator
 - Mimics an infinite array filled with a constant value

```
// create iterators
constant_iterator<int> begin(10);
constant_iterator<int> end = begin + 3;

begin[0]  // returns 10
begin[1]  // returns 10
begin[100]  // returns 10

// sum of [begin, end)
reduce(begin, end);  // returns 30 (i.e. 3 * 10)
```





- counting_iterator
 - Mimics an infinite array with sequential values

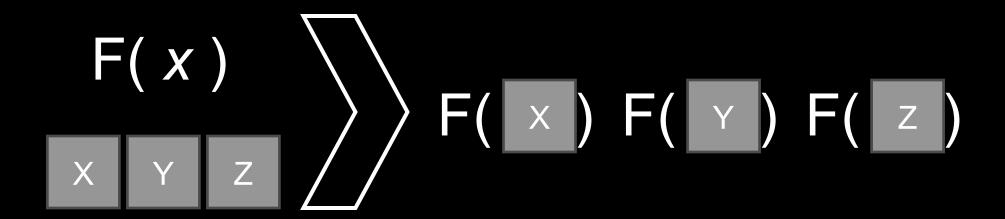
```
// create iterators
counting_iterator<int> begin(10);
counting_iterator<int> end = begin + 3;

begin[0]  // returns 10
begin[1]  // returns 11
begin[100]  // returns 110

// sum of [begin, end)
reduce(begin, end);  // returns 33 (i.e. 10 + 11 + 12)
```



- transform_iterator
 - Yields a transformed sequence
 - Facilitates kernel fusion





- transform_iterator
 - Conserves memory capacity and bandwidth

```
// initialize vector
device_vector<int> vec(3);
vec[0] = 10; vec[1] = 20; vec[2] = 30;

// create iterator (type omitted)
begin = make_transform_iterator(vec.begin(), negate<int>());
end = make_transform_iterator(vec.end(), negate<int>());
begin[0] // returns -10
begin[1] // returns -20
begin[2] // returns -30

// sum of [begin, end)
reduce(begin, end); // returns -60 (i.e. -10 + -20 + -30)
```



- zip_iterator
 - Looks like an array of structs (AoS)
 - Stored in structure of arrays (SoA)





zip_iterator

```
// initialize vectors
device vector<int> A(3);
device vector<char> B(3);
A[0] = 10; A[1] = 20; A[2] = 30;
B[0] = 'x'; B[1] = 'y'; B[2] = 'z';
// create iterator (type omitted)
begin = make zip iterator(make tuple(A.begin(), B.begin()));
      = make zip iterator(make tuple(A.end(), B.end()));
end
begin[0] // returns tuple(10, 'x')
begin[1] // returns tuple(20, 'y')
begin[2] // returns tuple(30, 'z')
// maximum of [begin, end)
maximum< tuple<int,char> > binary op;
reduce(begin, end, begin[0], binary op); // returns tuple(30, 'z')
```

Best Practices



- Fusion
 - Combine related operations together
- Structure of Arrays
 - Ensure memory coalescing
- Implicit Sequences
 - Eliminate memory accesses

Fusion



- Combine related operations together
 - Conserves memory bandwidth
- Example: SNRM2
 - Square each element
 - Compute sum of squares and take sqrt()

32

Fusion



Unoptimized implementation

```
// define transformation f(x) \rightarrow x^2
struct square
      host device
    float operator() (float x)
        return x * x;
};
float snrm2 slow(device vector<float>& x)
  // without fusion
  device vector<float> temp(x.size());
  transform(x.begin(), x.end(), temp.begin(), square());
  return sqrt( reduce(temp.begin(), temp.end()) );
```

Fusion



Optimized implementation (3.8x faster)

```
// define transformation f(x) -> x^2
struct square
{
     _host__ _device__
        float operator()(float x)
        {
          return x * x;
        }
};

float snrm2_fast(device_vector<float>& x)
{
        // with fusion
        return sqrt( transform_reduce(x.begin(), x.end(), square(), 0.0f, plus<float>());
}
```

34

Structure of Arrays (SoA)



- Array of Structures (AoS)
 - Often does not obey coalescing rules
 - device vector<float3>
- Structure of Arrays (SoA)
 - Obeys coalescing rules
 - Components stored in separate arrays
 - device_vector<float> x, y, z;
- Example: Rotate 3d vectors
 - SoA is 2.8x faster

Structure of Arrays (SoA)



```
struct rotate float3
{
             device
    host
  float3 operator() (float3 v)
    float x = v.x;
    float y = v.y;
    float z = v.z;
    float rx = 0.36f*x + 0.48f*y + -0.80f*z;
    float ry =-0.80f*x + 0.60f*y + 0.00f*z;
    float rz = 0.48f*x + 0.64f*y + 0.60f*z;
    return make float3(rx, ry, rz);
};
. . .
device vector<float3> vec(N);
transform(vec.begin(), vec.end,
          vec.begin(),
          rotate float3());
```

Structure of Arrays (SoA)



```
struct rotate tuple
{
    host
             device
  tuple<float, float> operator() (tuple<float, float, float> v)
    float x = get<0>(v);
    float y = get < 1 > (v);
    float z = get < 2 > (v);
    float rx = 0.36f*x + 0.48f*y + -0.80f*z;
    float ry =-0.80f*x + 0.60f*y + 0.00f*z;
    float rz = 0.48f*x + 0.64f*y + 0.60f*z;
    return make tuple(rx, ry, rz);
};
. . .
device vector\langle float \rangle \times (N), y(N), z(N);
transform(make zip iterator(make tuple(x.begin(), y.begin(), z.begin())),
          make zip iterator(make tuple(x.end(), y.end(),
                                                                z.end())),
          make zip iterator(make tuple(x.begin(), y.begin(), z.begin())),
          rotate tuple());
```

Implicit Sequences



- Avoid storing sequences explicitly
 - Constant sequences

Incrementing sequences

- Implicit sequences require no storage
 - constant iterator
 - counting_iterator
- Example
 - Index of the smallest element

Implicit Sequences



```
// return the smaller of two tuples
struct smaller tuple
  tuple<float,int> operator()(tuple<float,int> a, tuple<float,int> b)
    if (a < b)
      return a;
    else
      return b;
};
int min index(device vector<float>& vec)
  // create explicit index sequence [0, 1, 2, ...)
  device vector<int> indices(vec.size());
  sequence(indices.begin(), indices.end());
  tuple<float,int> init(vec[0],0);
  tuple<float,int> smallest;
  smallest = reduce(make zip iterator(make tuple(vec.begin(), indices.begin())),
                    make zip iterator(make tuple(vec.end(),
                                                               indices.end())),
                    init,
                    smaller tuple());
  return get<1>(smallest);
```

Implicit Sequences



```
// return the smaller of two tuples
struct smaller tuple
  tuple<float,int> operator()(tuple<float,int> a, tuple<float,int> b)
    if (a < b)
      return a;
    else
      return b;
};
int min index(device vector<float>& vec)
  // create implicit index sequence [0, 1, 2, ...)
  counting iterator<int> begin(0);
  counting iterator<int> end(vec.size());
  tuple<float,int> init(vec[0],0);
  tuple<float,int> smallest;
  smallest = reduce(make zip iterator(make tuple(vec.begin(), begin)),
                    make zip iterator(make tuple(vec.end(),
                                                                 end)),
                    init,
                    smaller tuple());
 return get<1>(small);
```

Recap



- Best Practices
 - Fusion
 - 3.8x faster
 - Structure of Arrays
 - 2.8x faster
 - Implicit Sequences
 - 3.4x faster

41

Additional Resources



Thrust

- Homepage
- Quick Start Guide
- Documentation
- Examples
- MegaNewtons (blog)
- thrust-users (mailing list)

Other

- NVIDIA Research
- CUDA