

DitroniX | AQIX and CWX3 ESP32-C3 Pin and GPIO Mapping

Pin	GP	Function <i>ESP32-C3-MINI-1x-N4</i>	Boot State	In	Out	Data	PU	PWM	Usage	AQIX/CWX3 Net	Comments
12	0	GPIO0, ADC1_CH0, XTAL_32K_P	-	-	-	-	-	-	OSC	XTAL	XTAL 32.768KHz <i>Fitted</i> 12p.
13	1	GPIO1, ADC1_CH1, XTAL_32K_N	-	-	-	-	-	-	OSC	XTAL	XTAL 32.768KHz <i>Fitted</i> 12p.
5	2	GPIO2, ADC1_CH2, FSP1Q	High	Yes	-	-	Yes	-	PU	GP2	Strapping Pin. Floating. Pulled High. <i>Unused</i> .
6	3	GPIO3, ADC1_CH3	-	Yes	-	-	-	-	In	VBAT	Battery Sensor
18	4	GPIO4, ADC1_CH4, FSP1HD, MTMS	-	Yes	-	-	-	-	In	LIGHT	Light Sensor
19	5	GPIO5, ADC2_CH0, FSP1WP, MTDI	-	-	Yes	Yes	Yes	-	Data	SCL	I2C SCL
20	6	GPIO6, FSP1CLK, MTCK	-	Yes	Yes	Yes	Yes	Yes	In	DOW	Dallas OneWire Sensor(s) <i>Optional Use</i> . Could be a GPIO Output Too.
21	7	GPIO7, FSP1D, MTDO	-	Yes	Yes	Yes	Yes	-	Data	SDA	I2C SDA
22	8	GPIO8	High	Yes	-	-	Yes	-	PU	GP8	Strapping Pin. Floating. Pulled High. <i>Unused</i> .
23	9	GPIO9	High	Yes	-	-	Yes	-	In	/PGM	Strapping Pin. Internal weak pull-up. Boot LOW. BOOT. PGM Button
16	10	GPIO10, FSP1CS0	-	-	Yes	-	-	Yes	Out	LED_Red	RGB LED
26	18	GPIO18, USB_D-	-	-	Yes	-	-	Yes	Out	LED_Green	RGB LED
27	19	GPIO19, USB_D+	-	-	Yes	-	-	Yes	Out	LED_Blue	RGB LED
8	EN	Enable	High	Yes	-	-	Yes	-	PRG	/RST	Strapping Pin. Boot LOW. BOOT. PGM Button High: on, enables the chip. Low: off, the chip powers off.
30	RXD0	GPIO20, U0RXD	-	-	-	Yes	-	-	Data	USB_RXD	UART Interface
31	TXD0	GPIO21, U0TXD	-	-	-	Yes	-	-	Data	USB_TXD	USB_TXD LED. UART Interface