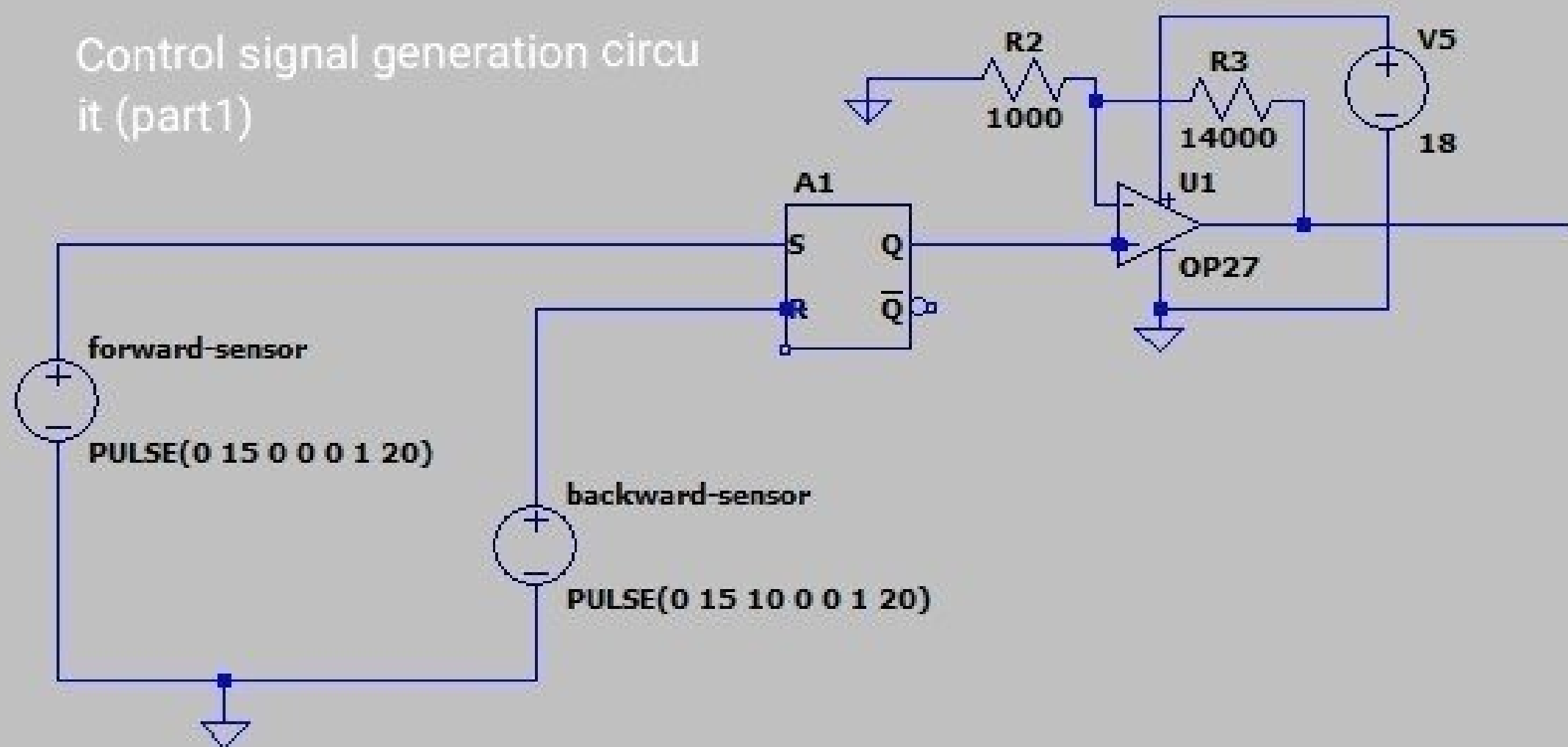
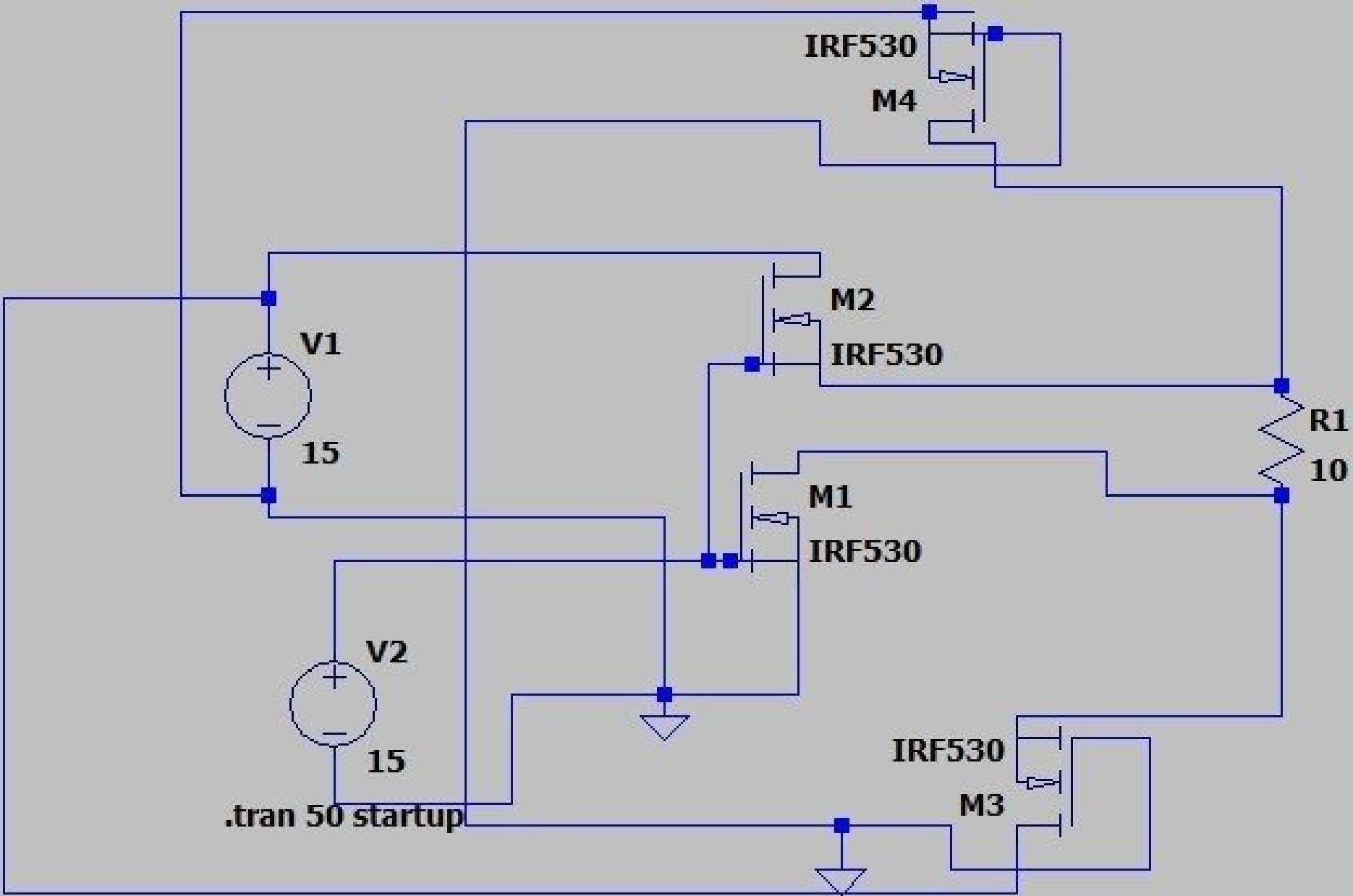


Control signal generation circuit (part1)



current reverse circuit (part2)



complete circuit

