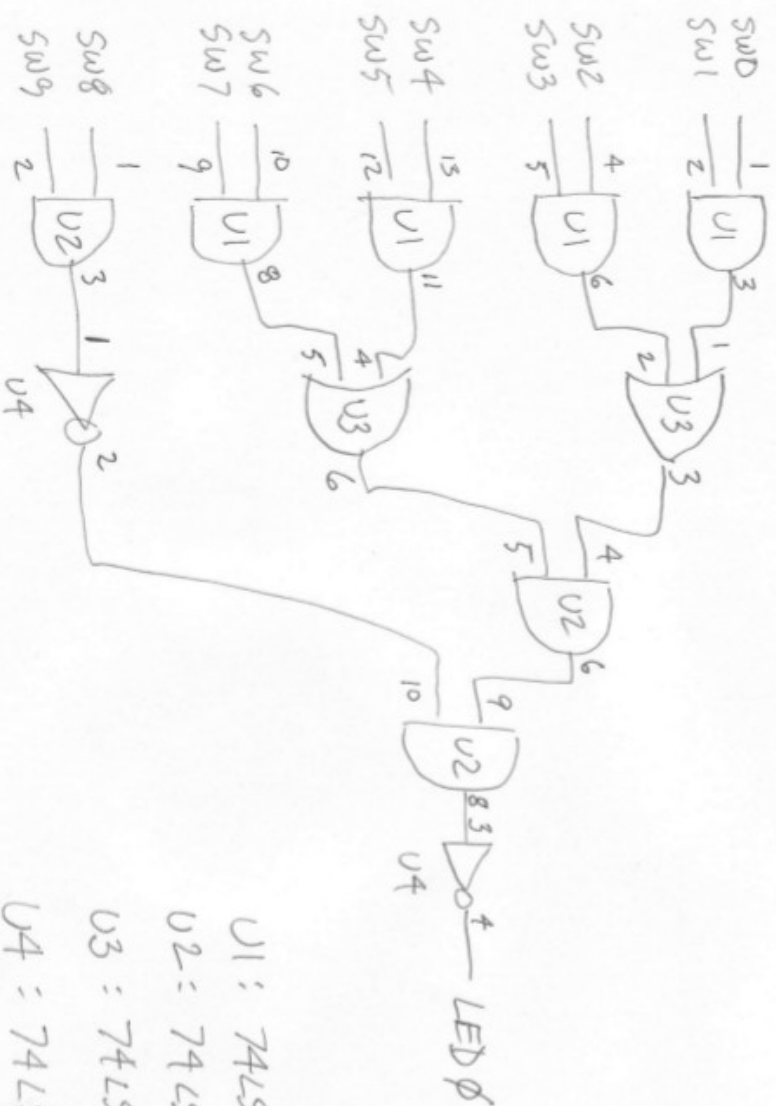


# Schematic Example



U1: 74LS08  
 U2: 74LS08  
 U3: 74LS32  
 U4: 74LS04

Don't forget to connect power  
 and ground on your physical  
 circuit!

SILLY CIRCUIT  
 PAUL CHOW  
 SEPT 11, 2014

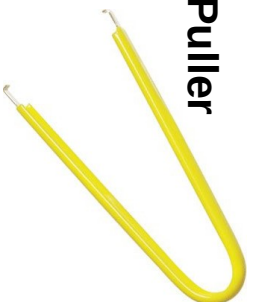
# Equipment

Logic Probe

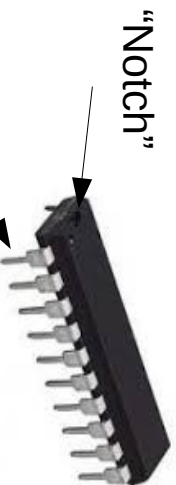


MEM/PULSE  
Set to MEM  
Hi/Lo Indicator

Chip Puller



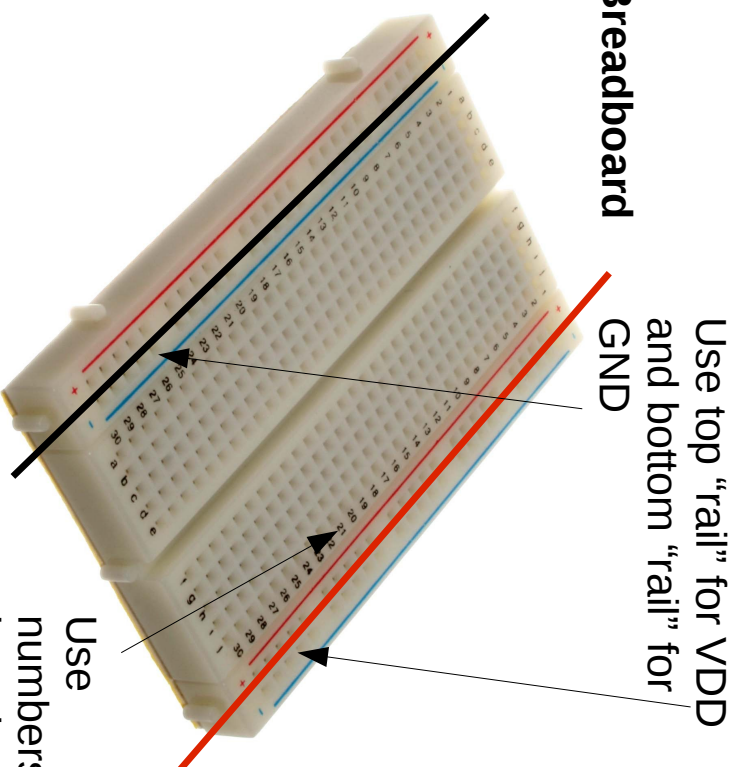
74XX Chip



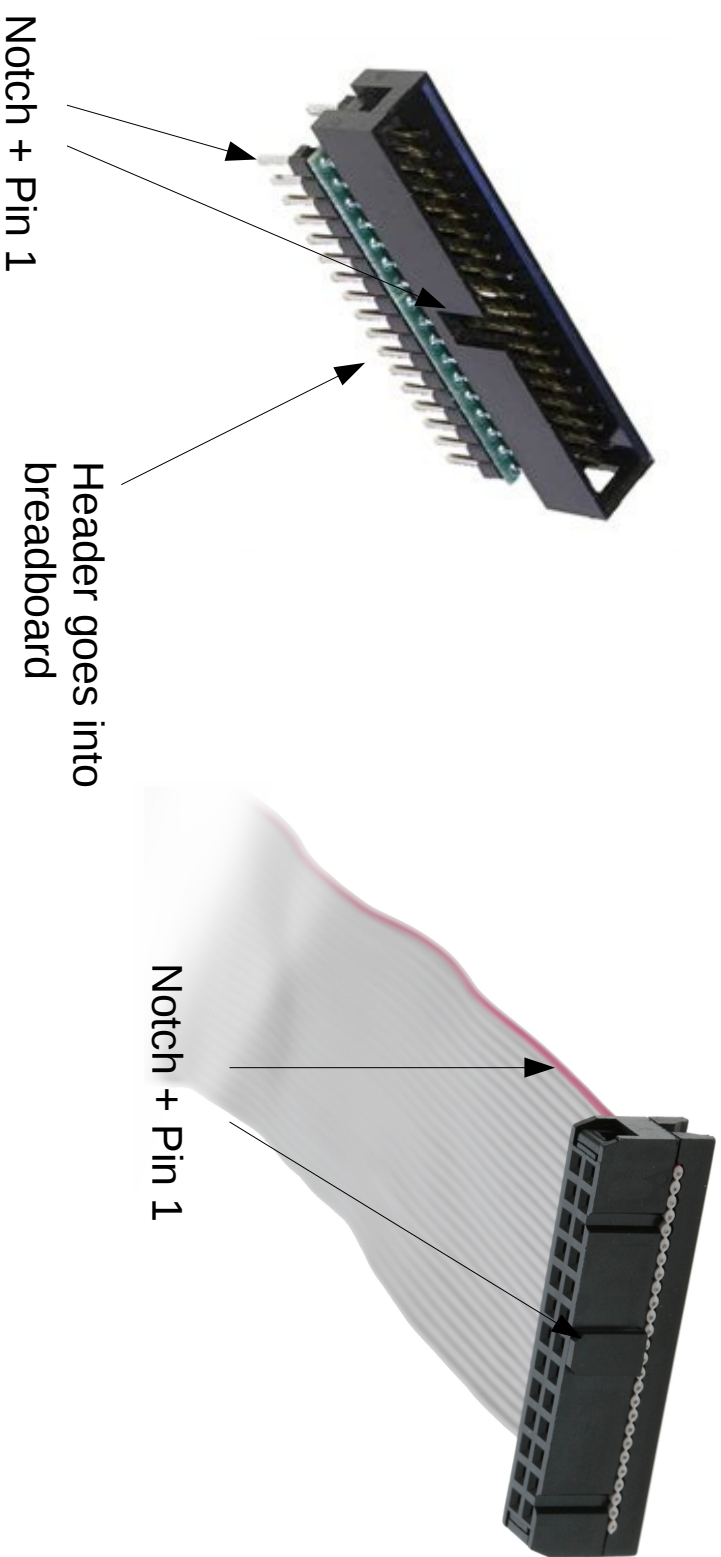
Wire Strippers + Wire



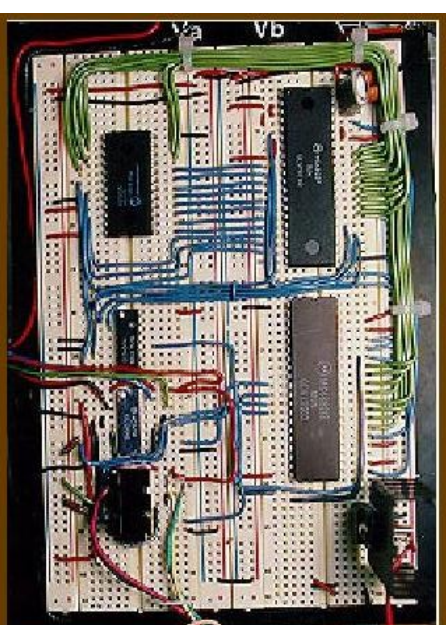
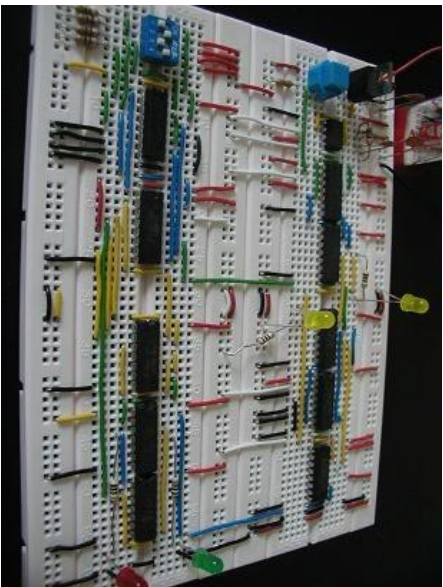
Breadboard



# Pin Headers + Ribbon Cables

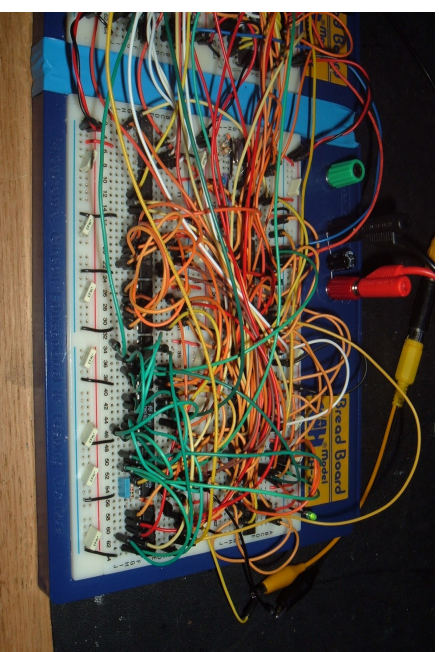
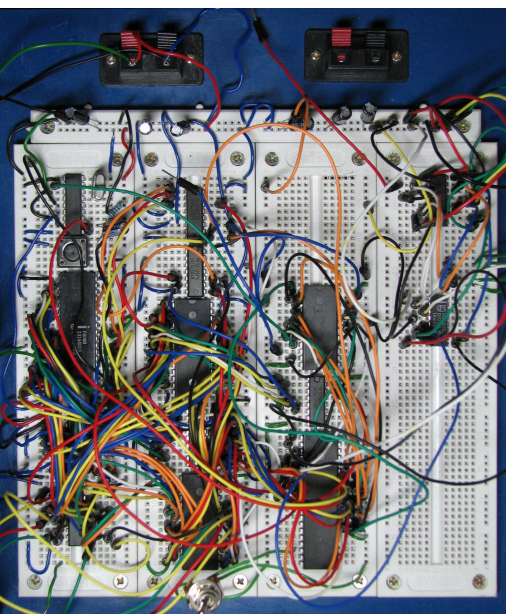


# Breadboard Neatness



Overkill for Lab 1

Well planned, Easily debugged



OK for Lab 1

Lab Nightmare...