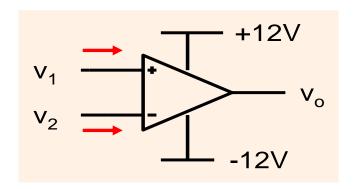
ESC201T: Introduction to **Electronics**

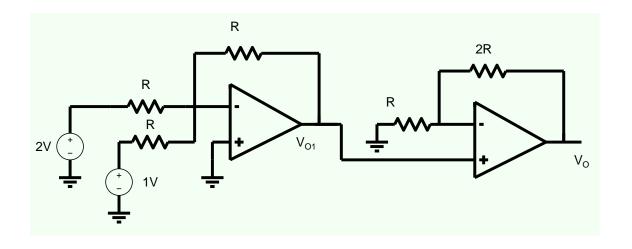
Lecture 30: Operational Amplifier Circuits-2

B. Mazhari Dept. of EE, IIT Kanpur Important properties for analyzing ideal opamp circuits under negative feedback



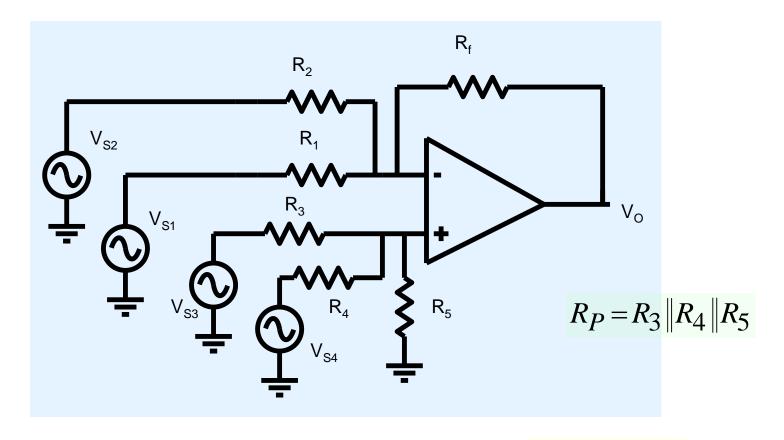
1.
$$v_1 = v_2$$

2.
$$i_1 = i_2 = 0$$



One stage does not load the preceding stage due to very small output impedance of the opamp

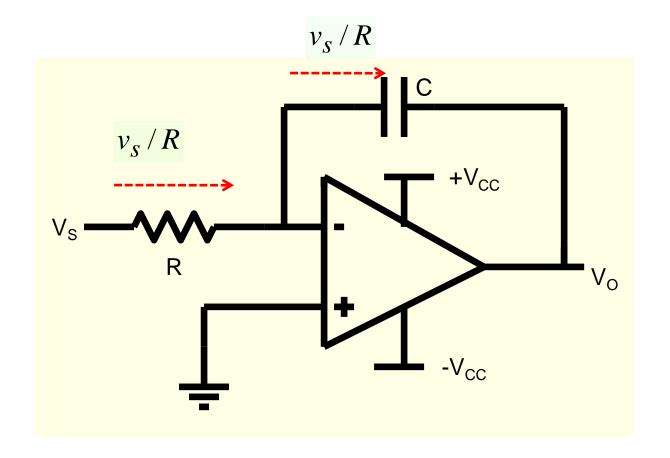
Adder/Subtractor



$$v_{o} = -(\frac{R_{f}}{R_{1}})v_{s1} + -(\frac{R_{f}}{R_{2}})v_{s2} + v_{s3}\frac{R_{P}}{R_{3}} \times (1 + \frac{R_{f}}{R_{1}\|R_{2}})$$

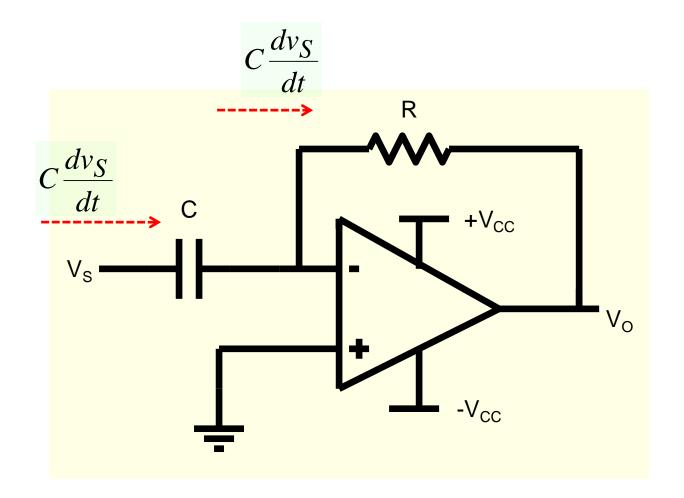
$$+ v_{s4}\frac{R_{P}}{R_{4}} \times (1 + \frac{R_{f}}{R_{1}\|R_{2}})$$

Integrator



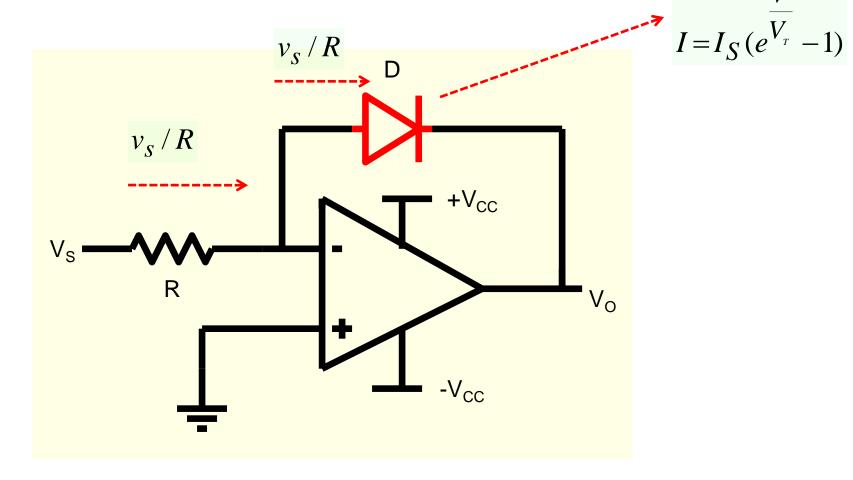
$$\frac{V_S}{R} = -C\frac{dV_O}{dt} \implies V_O(t) = -\frac{1}{RC}\int V_S dt$$

Differentiator



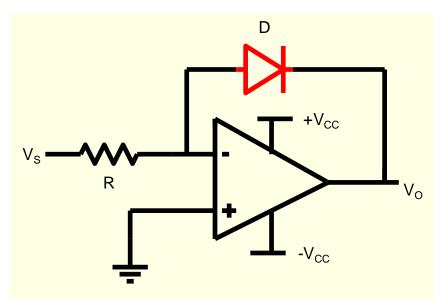
$$-\frac{V_O}{R} = C\frac{dV_S}{dt} \implies V_O(t) = -RC\frac{dV_S}{dt}$$

Log Amplifier



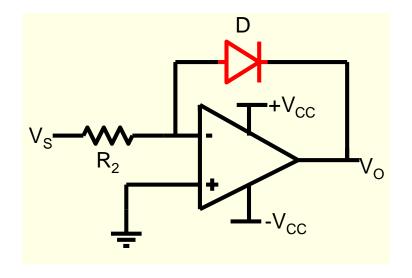
$$\frac{V_S}{R} = I_S(e^{-\frac{V_O}{V_T}} - 1) \implies -V_O = V_T \times \ln(1 + \frac{V_S}{RI_S}) \cong V_T \times \ln(\frac{V_S}{RI_S})$$

Temperature Sensor?

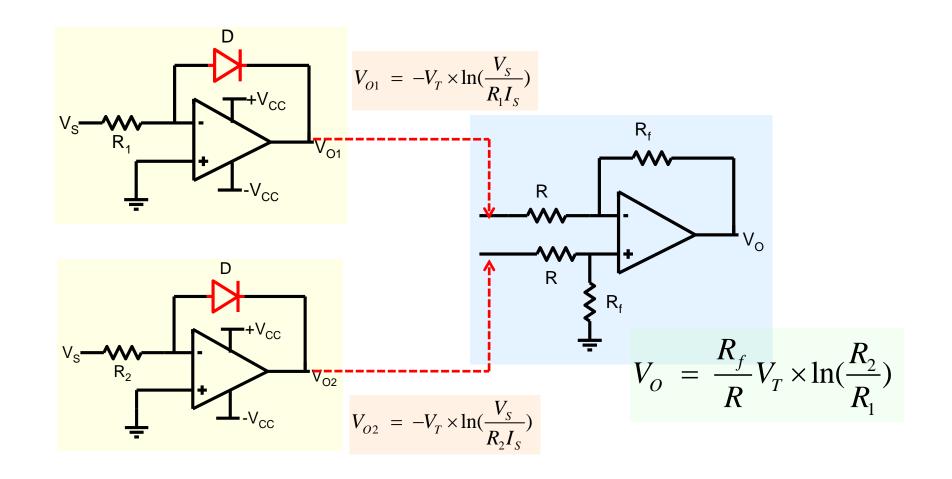


$$V_O = -V_T \times \ln(\frac{V_S}{RI_S}); V_T = \frac{k_B T}{q}$$

But I_S is a function of temperature as well.

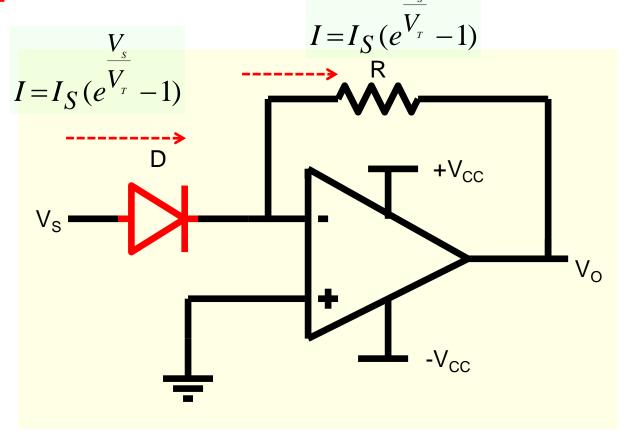


$$V_O = -V_T \times \ln(\frac{V_S}{R_2 I_S})$$



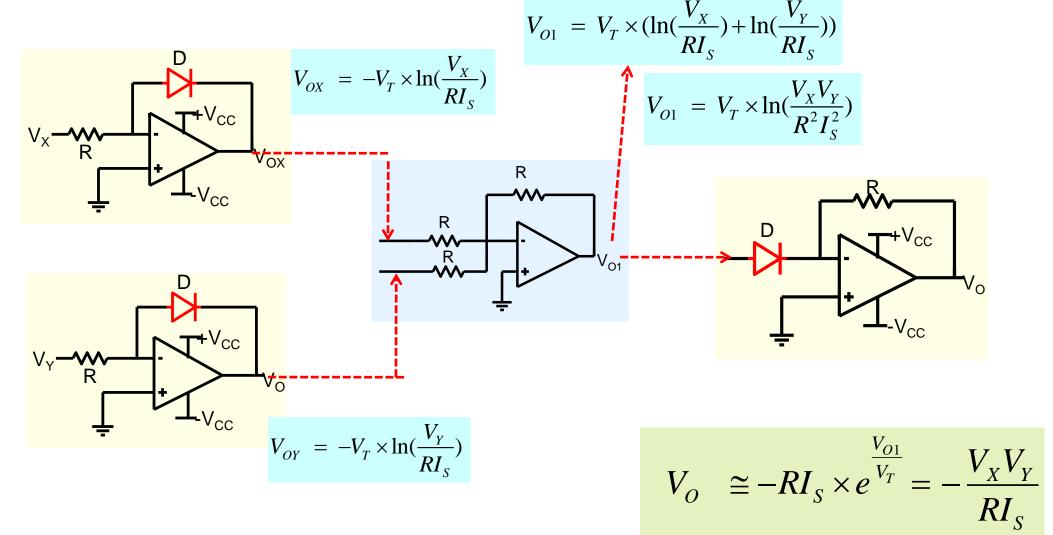
Output voltage is directly proportional to temperature

AntiLog Amplifier

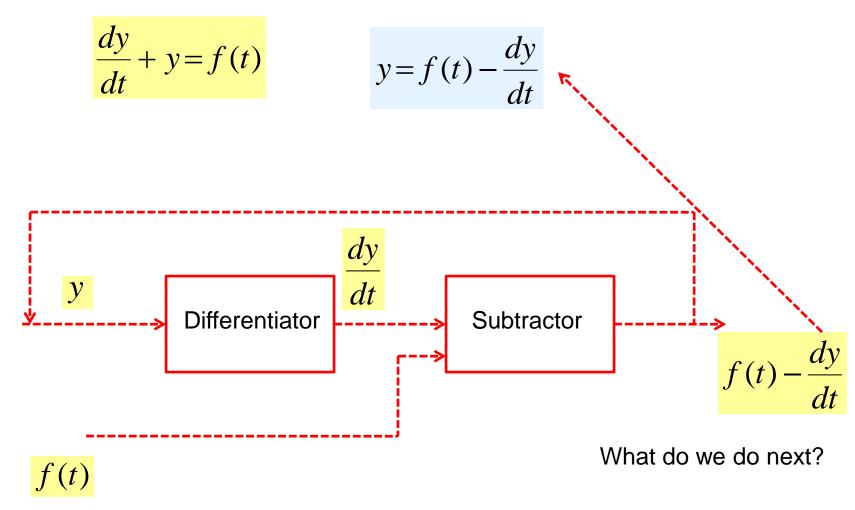


$$-\frac{V_O}{R} = I_S(e^{\frac{V_S}{V_T}} - 1) \implies V_O = -RI_S(e^{\frac{V_S}{V_T}} - 1) \cong -RI_S \times e^{\frac{V_S}{V_T}}$$

Multiplier



Solving Differential Equations?



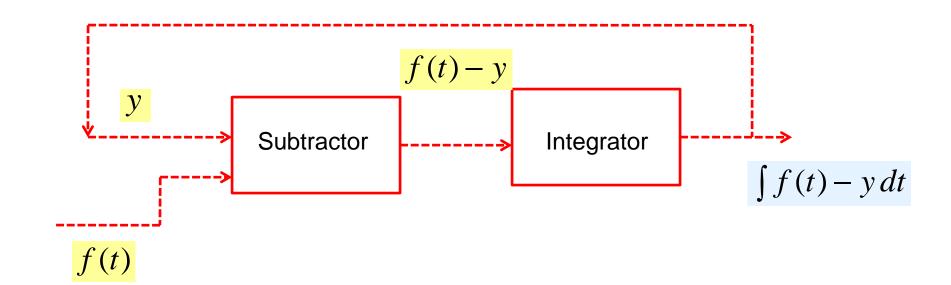
Where do we get y from?

Integrators are preferred over Differentiators because they are less sensitive to noise

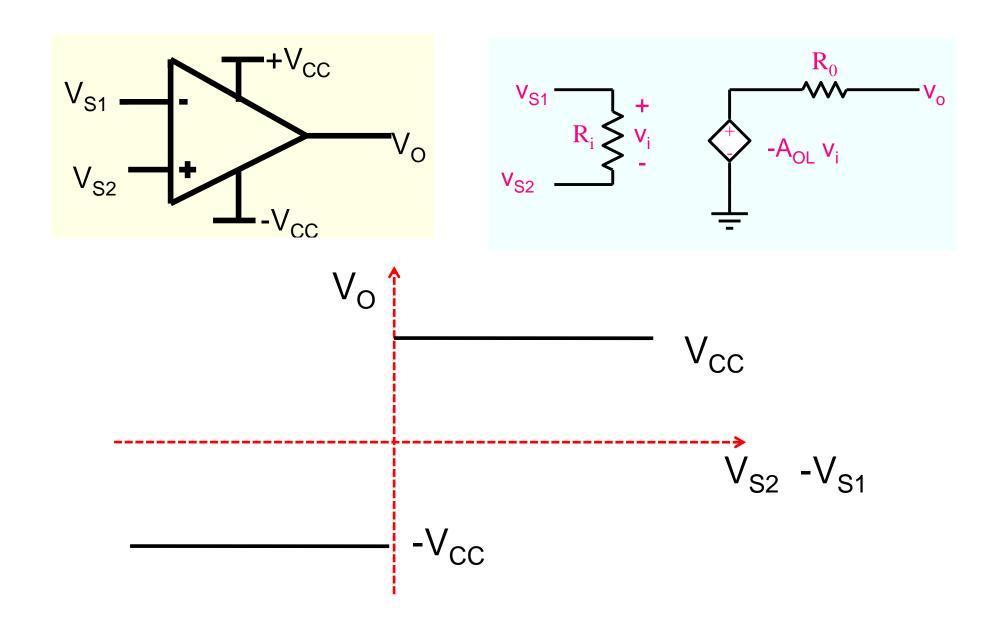
$$\frac{dy}{dt} + y = f(t)$$

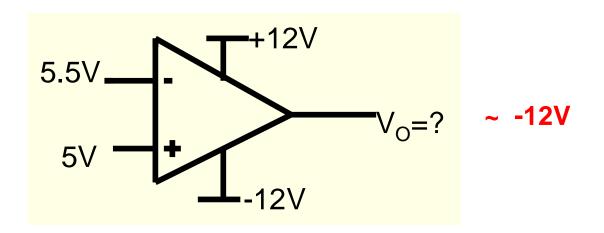
$$\frac{dy}{dt} = f(t) - y \Rightarrow$$

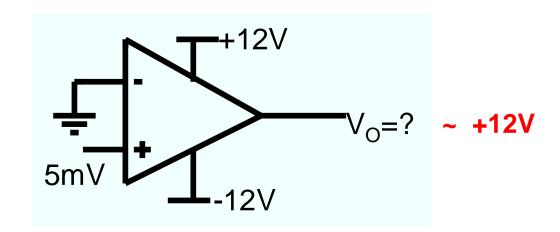
$$y = \int f(t) - y \, dt$$



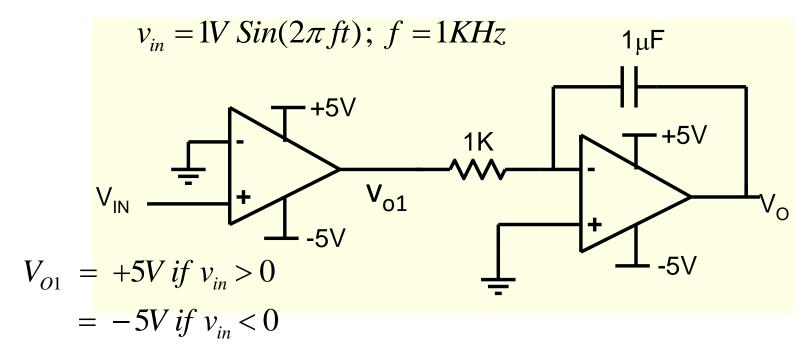
Comparator: Opamp under open Loop condition

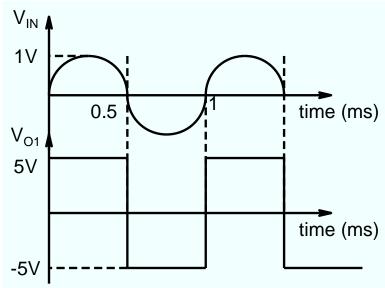


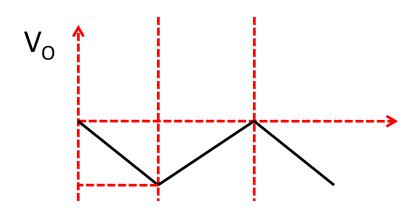




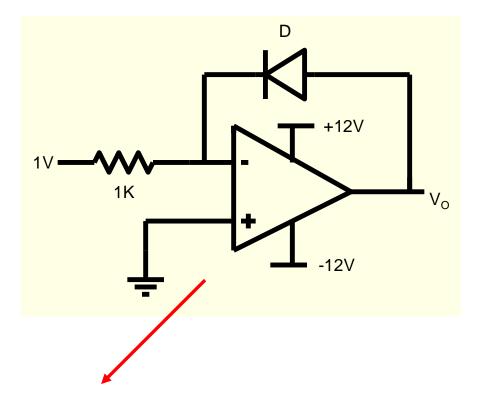
Example

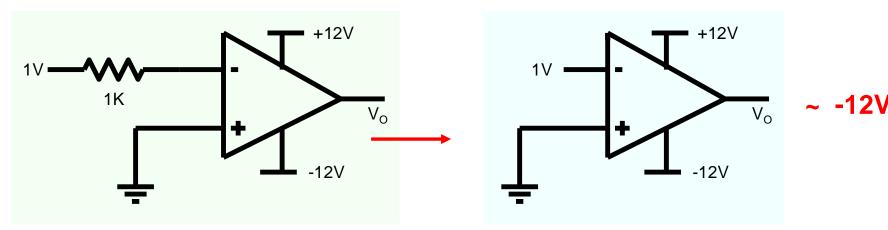




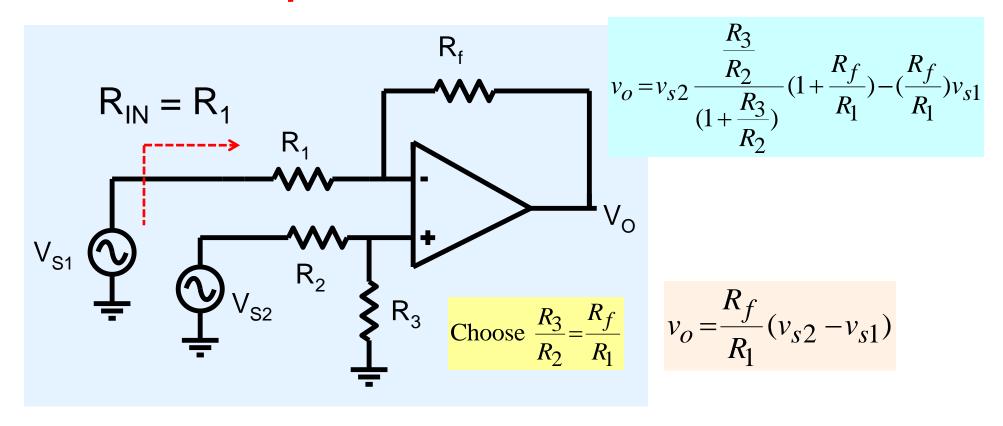


Example





Difference Amplifier



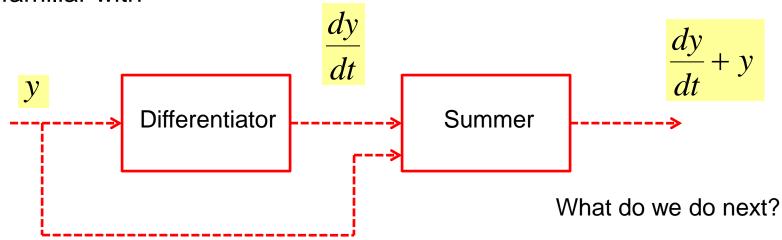
A drawback is that input resistance is relatively Lower

To change gain, we have to change two resistors and a slight mismatch can drastically reduce common mode rejection ratio

Solving Differential Equations?

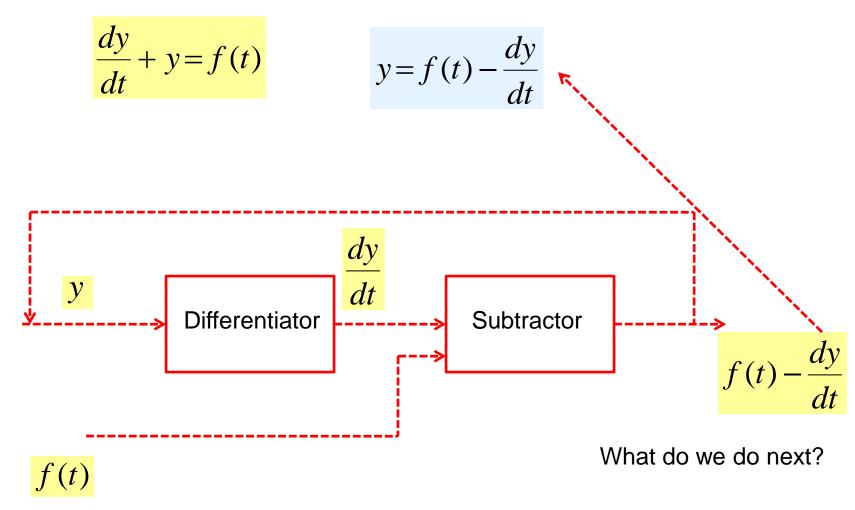
$$\frac{dy}{dt} + y = f(t)$$

Let us try and solve this equation using opamp circuit blocks that we are familiar with



Where do we get y from?

Solving Differential Equations?



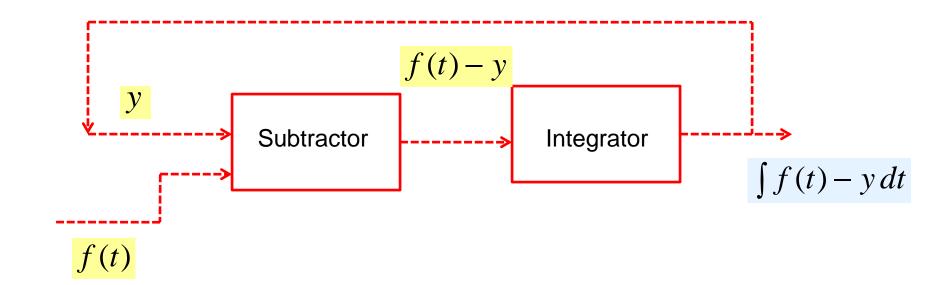
Where do we get y from?

Integrators are preferred over Differentiators because they are less sensitive to noise

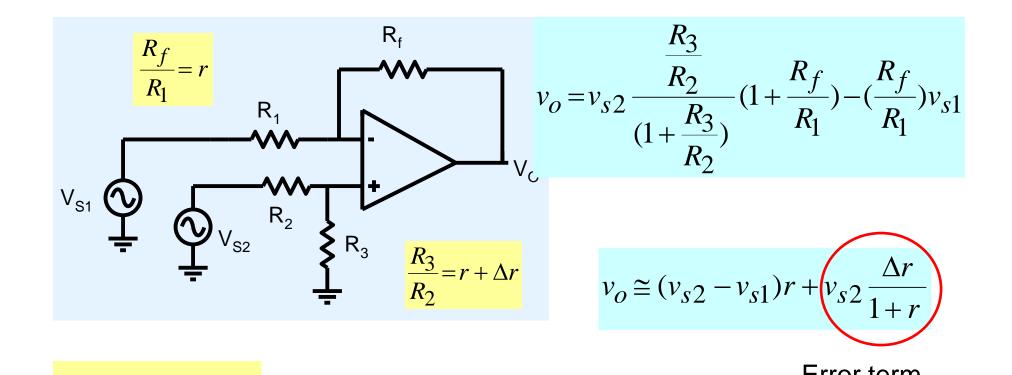
$$\frac{dy}{dt} + y = f(t)$$

$$\frac{dy}{dt} = f(t) - y \Rightarrow$$

$$y = \int f(t) - y \, dt$$



Effect Of Mismatches



$$v_{id} = v_{S2} - v_{S1}$$

$$v_{ic} = \frac{v_{S1} + v_{S2}}{2}$$

$$v_{S2} = 0.5v_{id} + v_{ic}$$

$$v_o = A_d v_{id} + A_{cm} v_{ic}$$

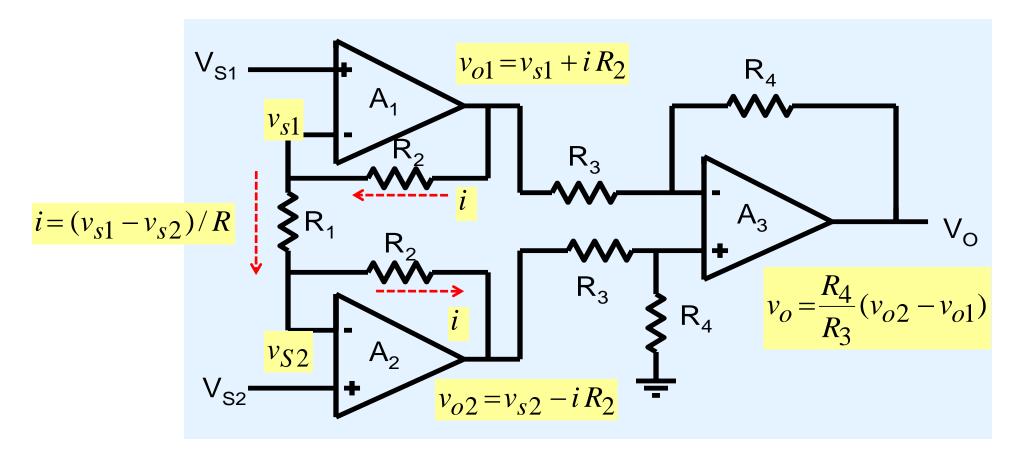
$$A_{dm} = r$$

$$A_{cm} = \frac{\Delta r}{1+r}$$

Error term

Common mode gain and CMRR depend on mismatches

Instrumentation Amplifier



$$v_o = \frac{R_4}{R_3} \times (1 + \frac{2R_2}{R_1}) \times (v_{s2} - v_{s1})$$

Very high input Resistance

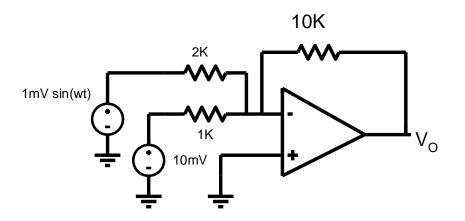
Can change one resistor R₁ and change gain

ESC102: Introduction to Electronics

HW10: Solution

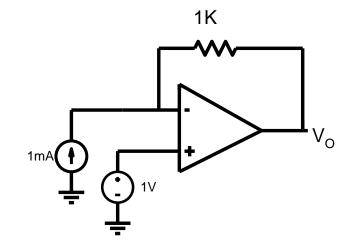
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Q.1 Determine the output of the ideal opamp circuits shown below



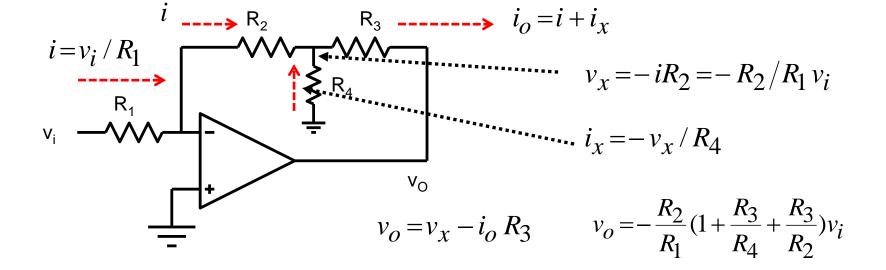
$$v_o = -\{\frac{10K}{1K} \times 10mV + \frac{10K}{2K} \times 1mV \sin(\omega t)\}\$$

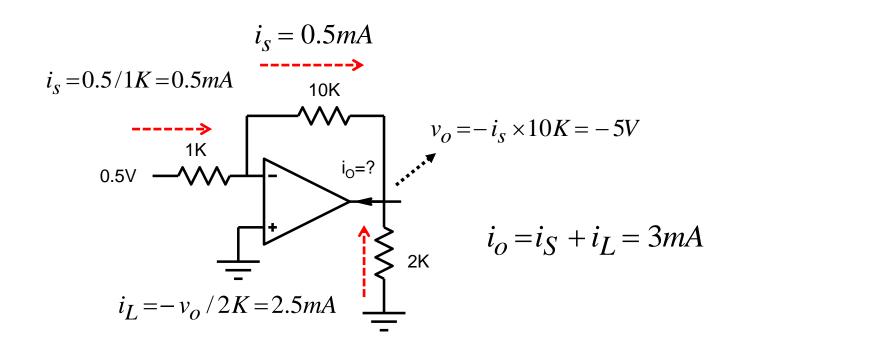
= -\{0.1 + 5 \times 10^{-3} \sin(\omega t)\}



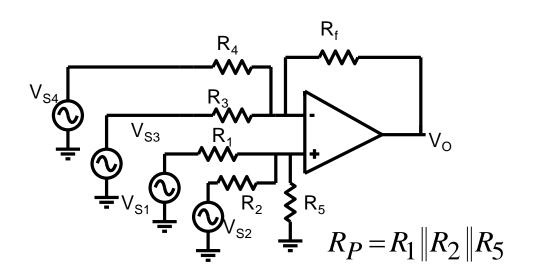
$$v_+ = v_- = 1V$$

$$\frac{1 - v_O}{1K} = 1mA \qquad v_O = 0V$$





Q.2 Design an opamp circuit that would generate the following output voltage where Vs1, Vs2, Vs3 and Vs4 are input voltages $V_O = 2v_{s1} + 4v_{s2} - 8v_{s3} - 10v_{s4}$

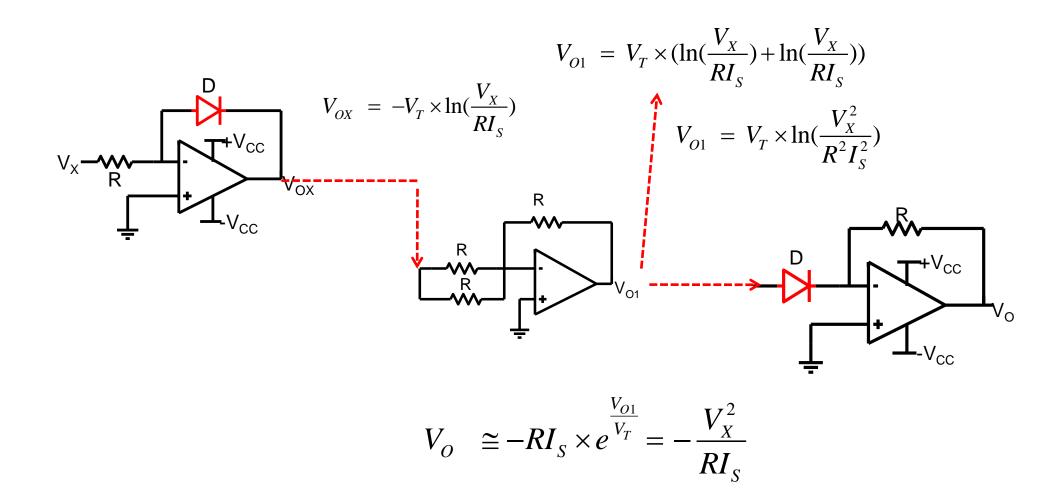


$$v_{o} = -(\frac{R_{f}}{R_{3}})v_{s3} - -(\frac{R_{f}}{R_{4}})v_{s4} + (1 + \frac{R_{f}}{R_{3}\|R_{4}}) \times \frac{R_{P}}{R_{1}}v_{s1} + (1 + \frac{R_{f}}{R_{3}\|R_{4}}) \times \frac{R_{P}}{R_{2}}v_{s2}$$
Choose: $R_{f} = 10K \implies R_{3} = 1.25K \implies R_{4} = 1K$

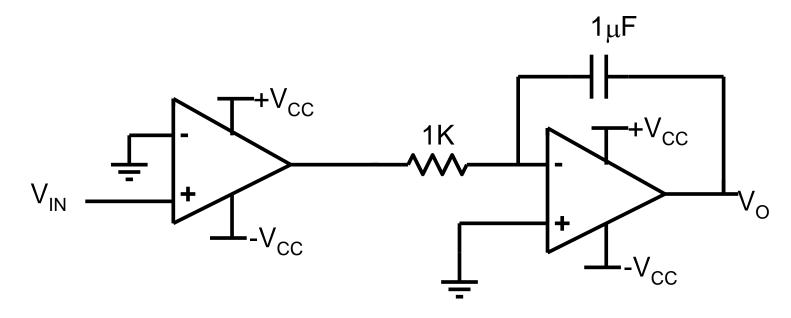
$$\Rightarrow \frac{R_{P}}{R_{1}} = 0.105 \implies \frac{R_{P}}{R_{2}} = 0.211 \implies \frac{R_{1}}{R_{2}} = 2$$

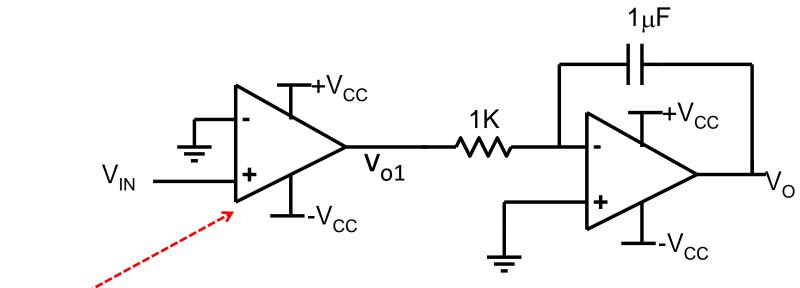
Choose: $R_2 = 1K$ $\Rightarrow R_1 = 2K$ $\Rightarrow R_P = 0.211K$ $\Rightarrow R_5 = 0.308K$

Q.3 Design an opamp circuit that can produce $V_O = K \times V_{IN}^2$ where Vin is the input voltage.



Q.4 Sketch the output voltage of the circuit shown below for $V_{in}=1V~Sin(2\pi ft);~f=1KHz$ and supply voltages of $\pm 5V$

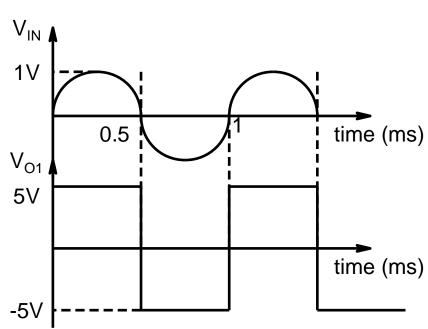


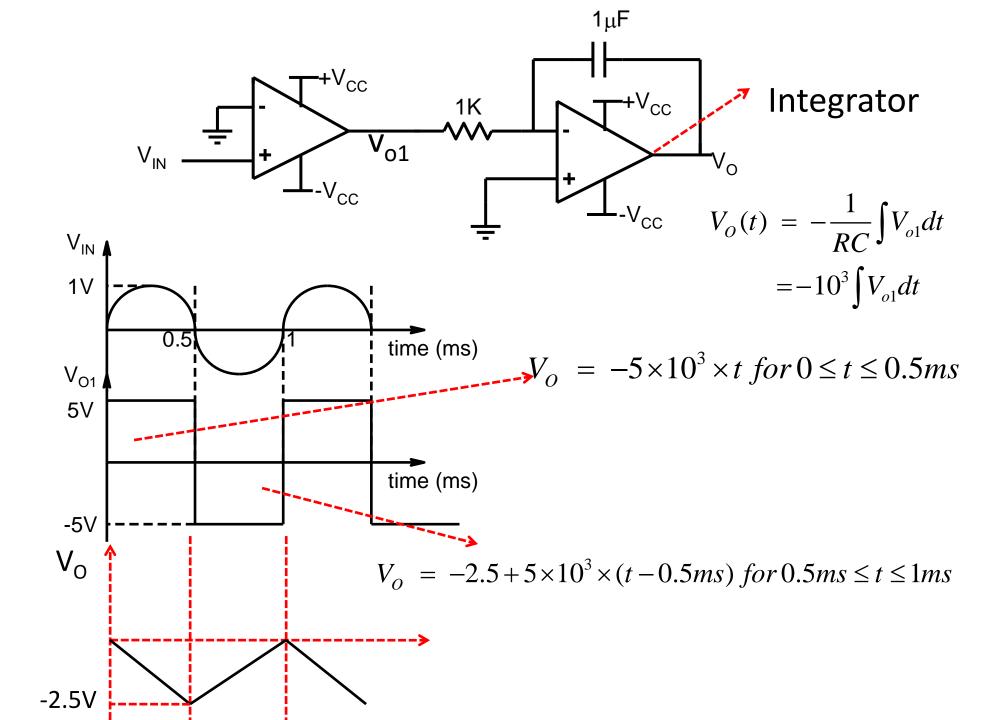


comparator

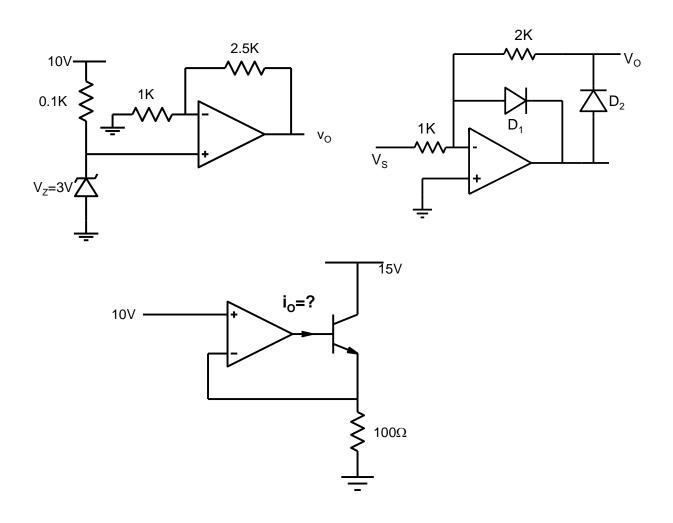
$$V_{O1} = +5V \ if \ v_{in} > 0$$

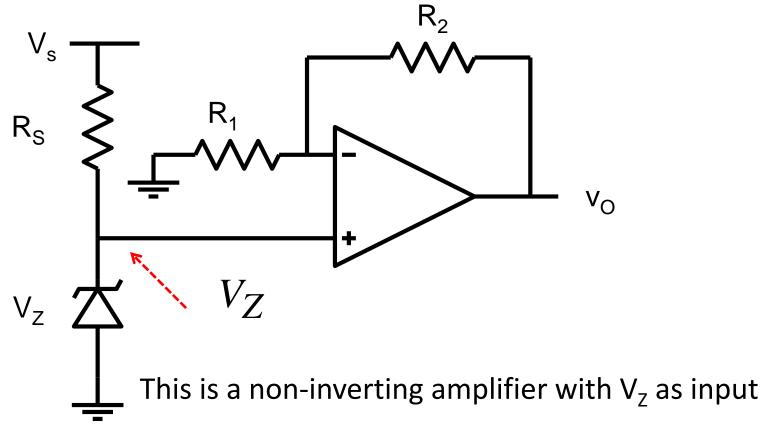
= -5V if $v_{in} < 0$





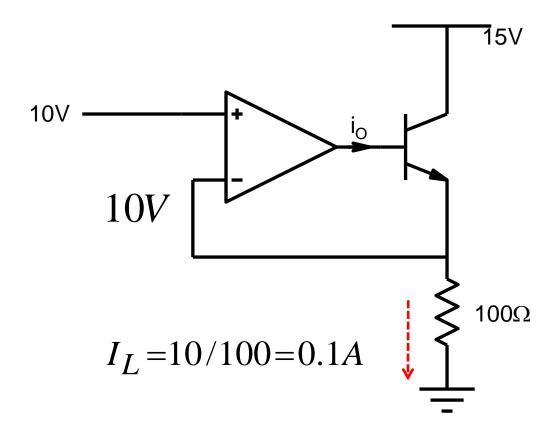
Q.5 Determine the output for the ideal opamp circuits shown below. For the circuit on the right assume that diodes have cut-in voltage of 0.7V. Analyze the circuit for Vs = 1V and Vs = -1V. For the transistor assume a current gain of 100. What is the usefulness of each of the circuits?





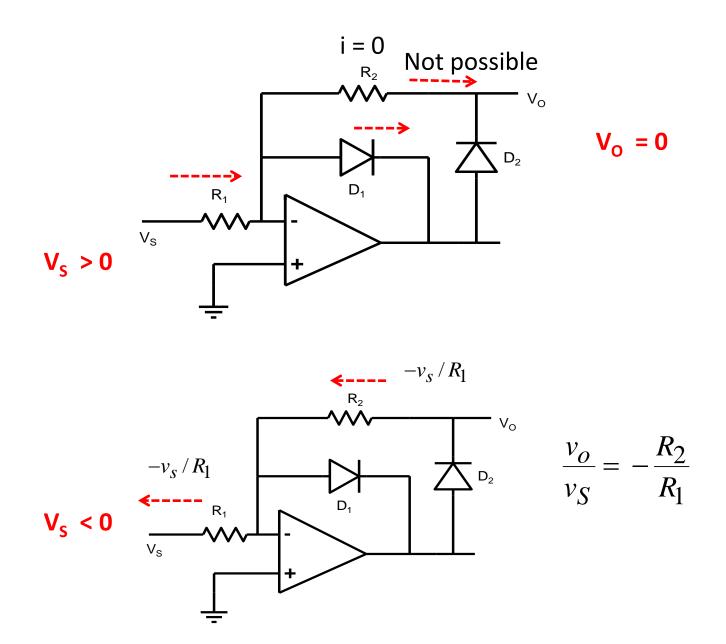
$$v_O = V_Z (1 + \frac{R_2}{R_1})$$

The circuit produces a constant output voltage Vo even though supply voltage may vary and thus acts like a voltage regulator.



$$I_o = I_B = \frac{I_E}{\beta + 1} = 0.99 mA$$

The circuit can supply load current that is much larger than opamp output current



The circuit acts like a rectifier