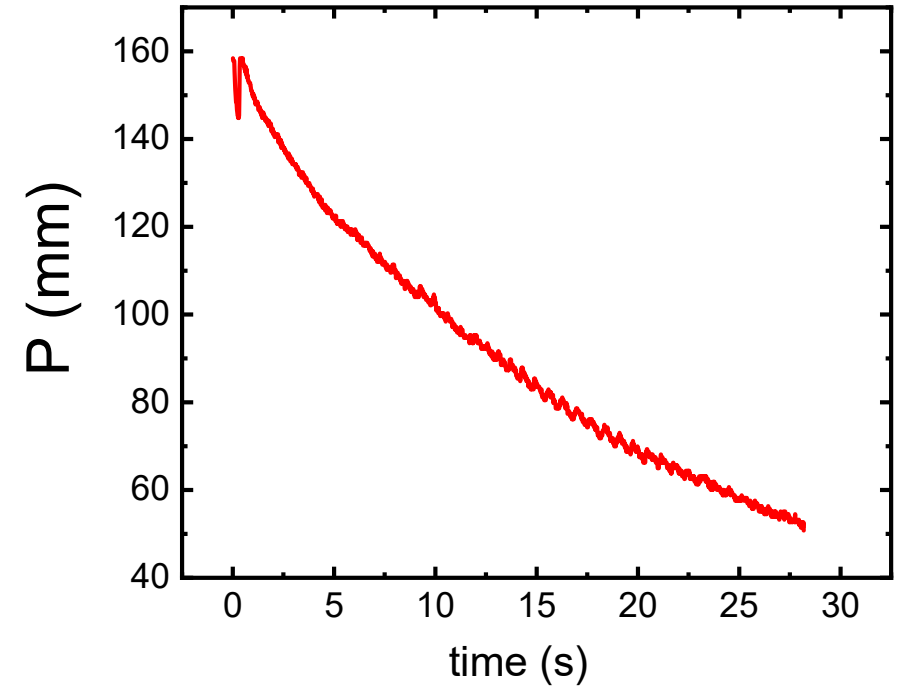


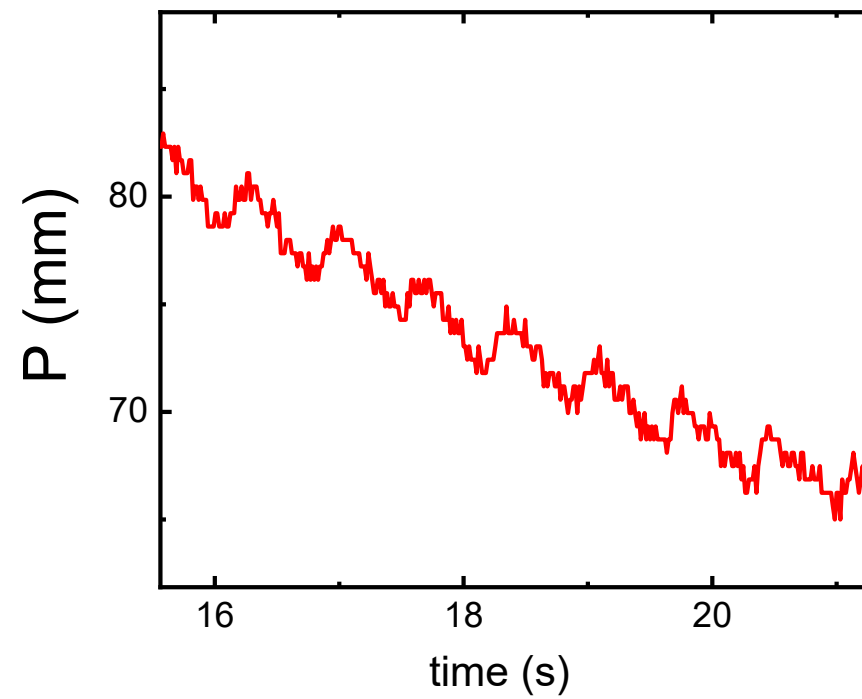
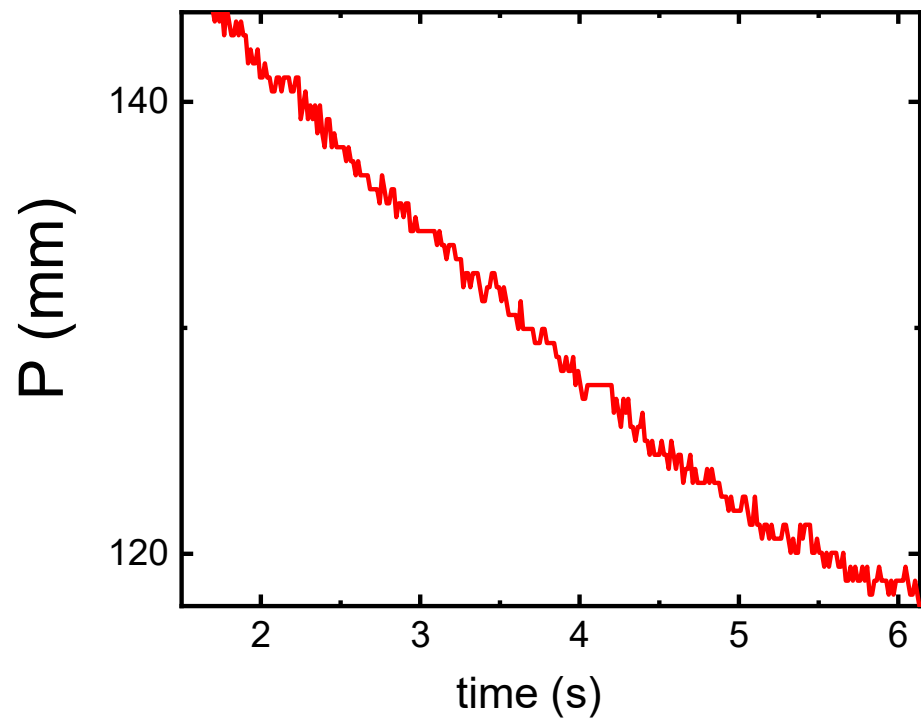
ESC102T : Introduction to **Electronics**

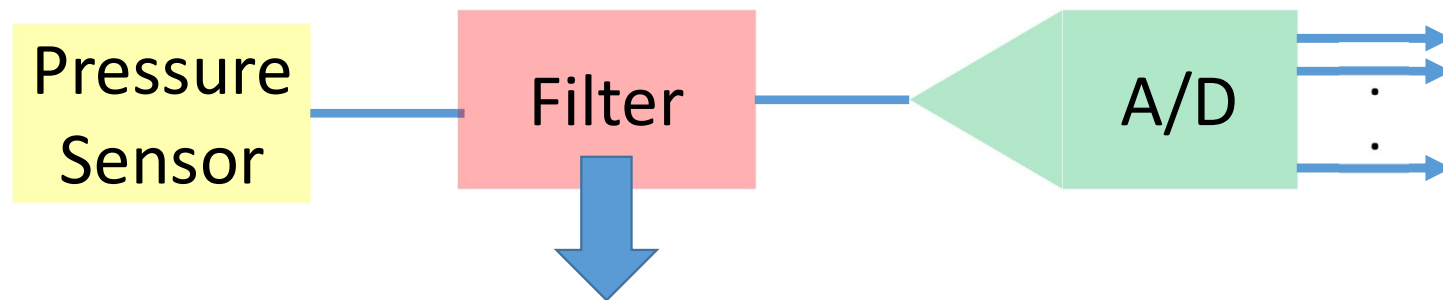
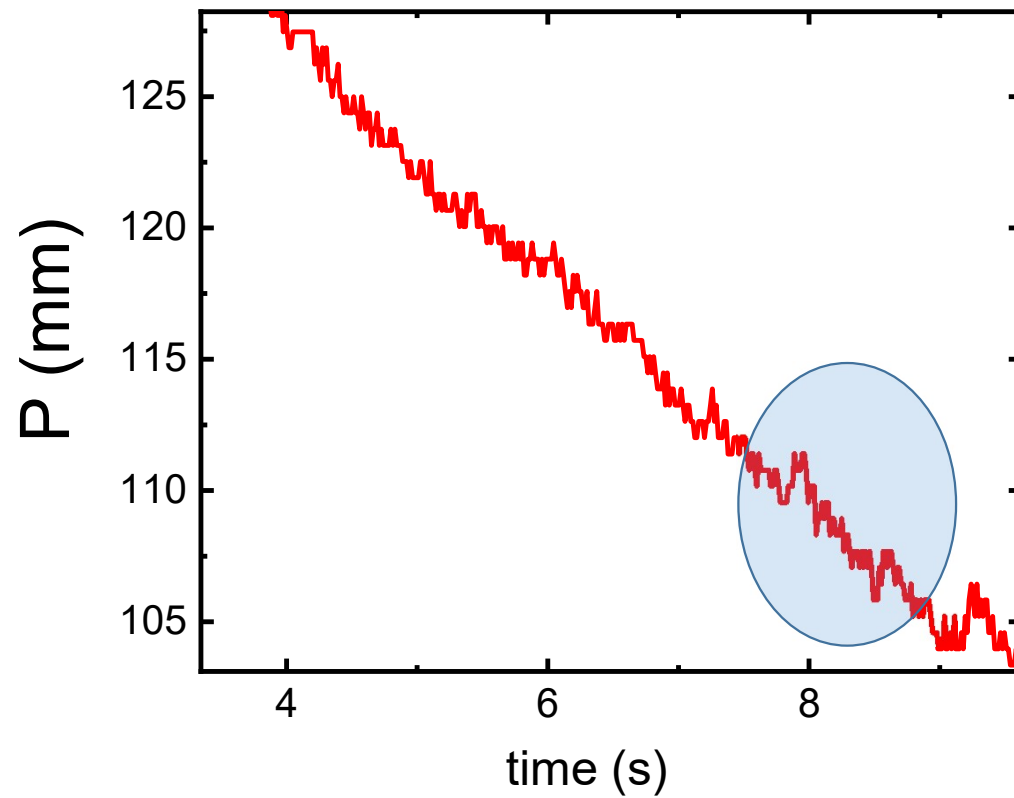
L9: Capacitive and Inductive Circuits

B. Mazhari
Dept. of EE, IIT Kanpur

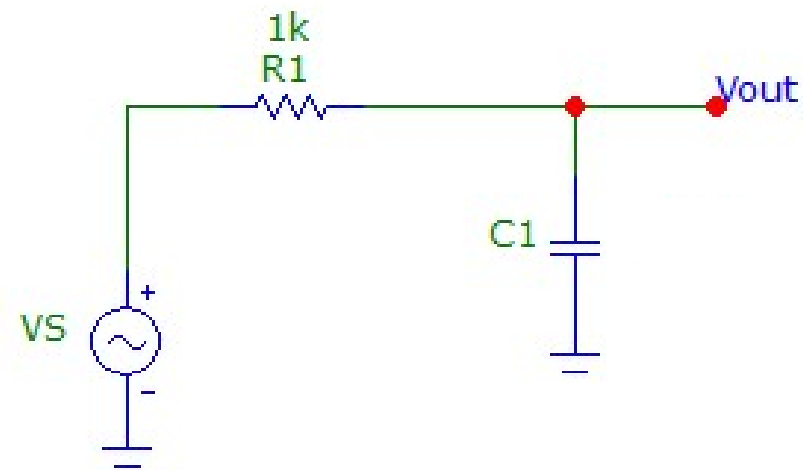
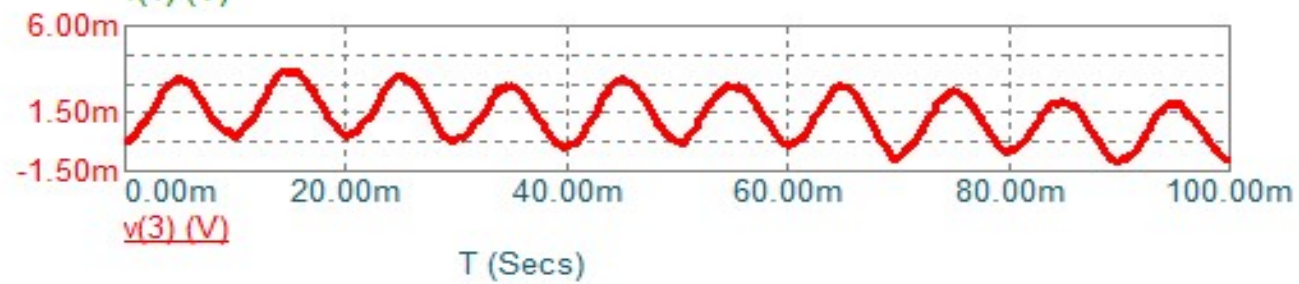
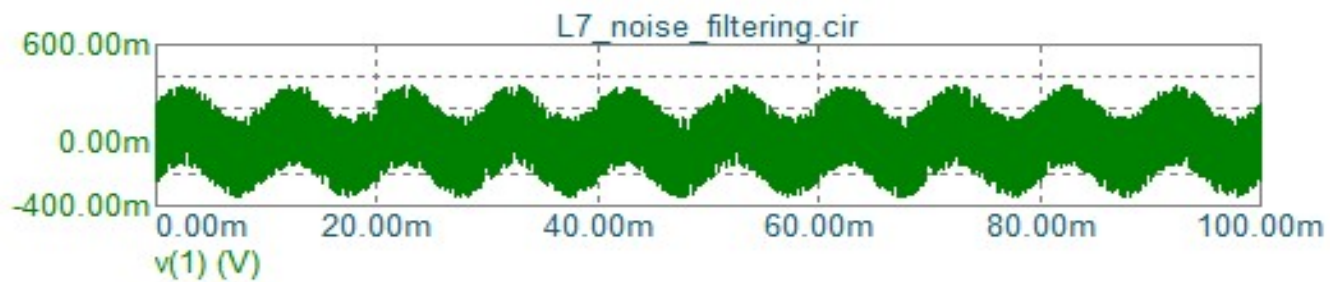
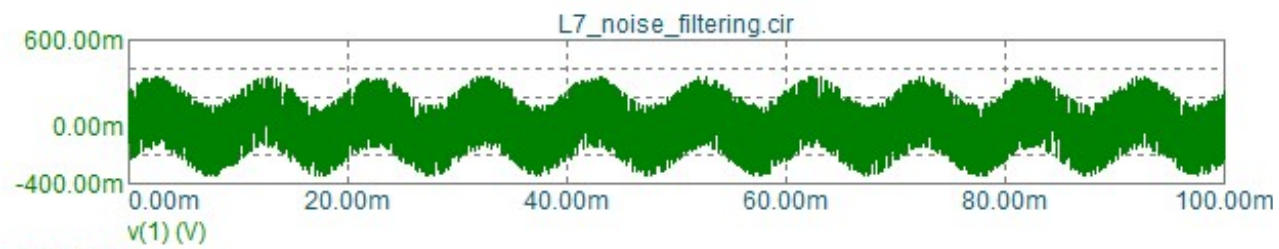
“All of you are carrying billions of capacitors in your pocket”



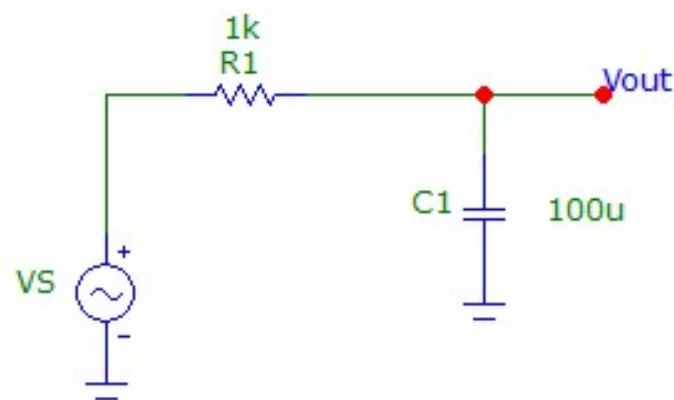
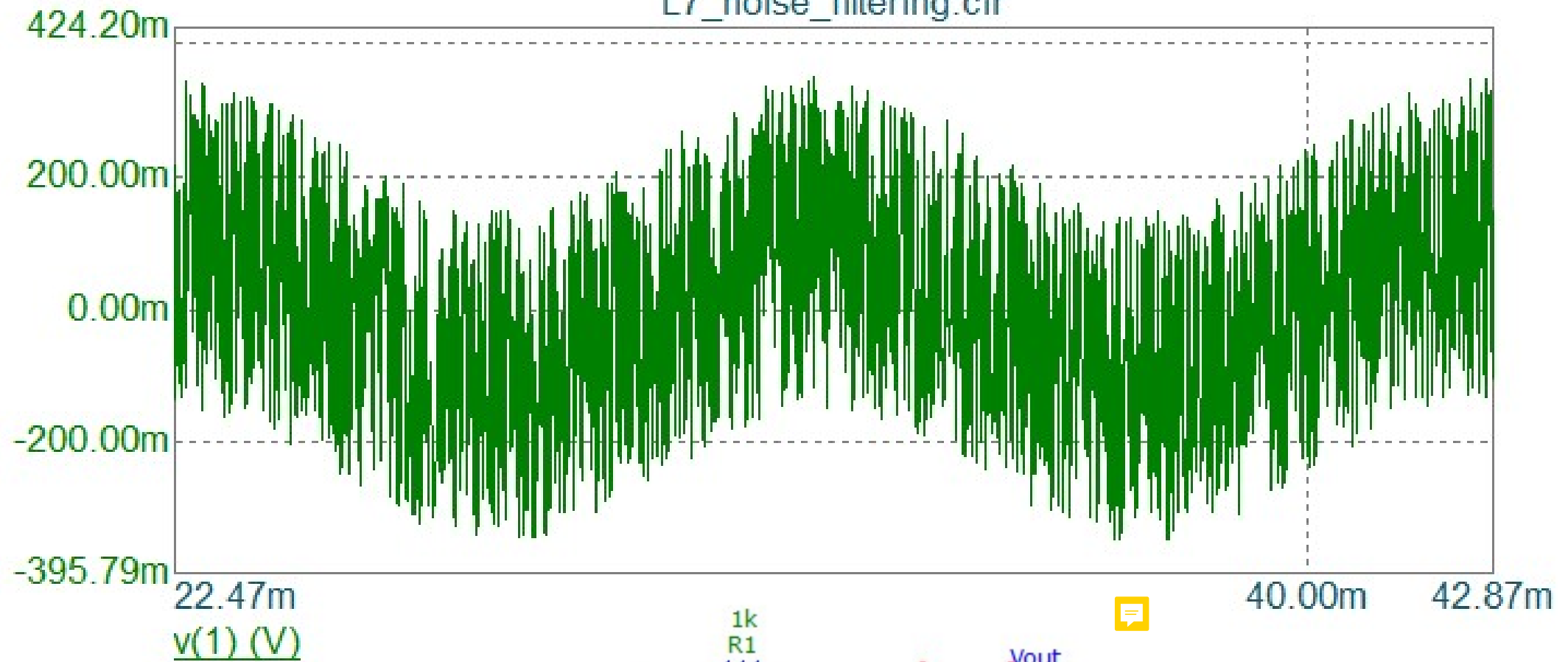


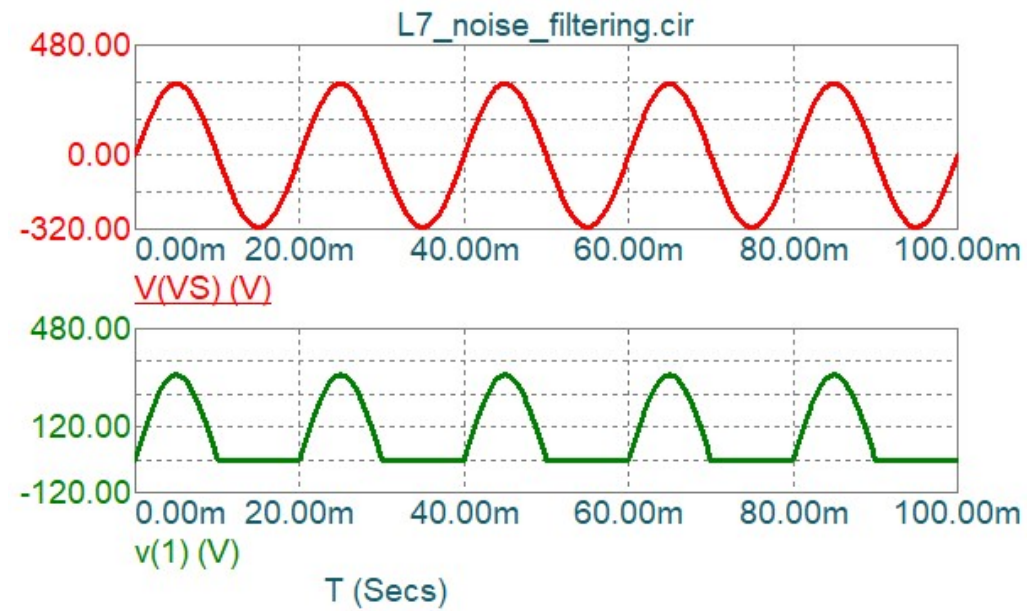
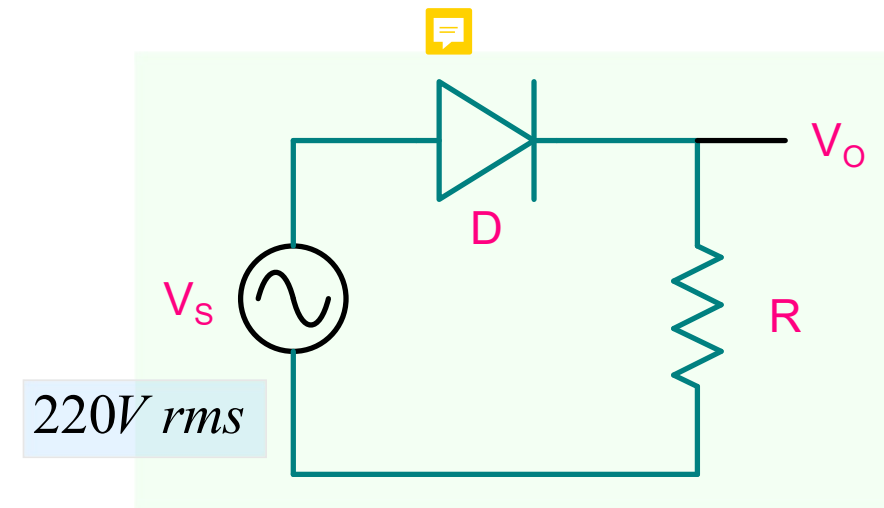
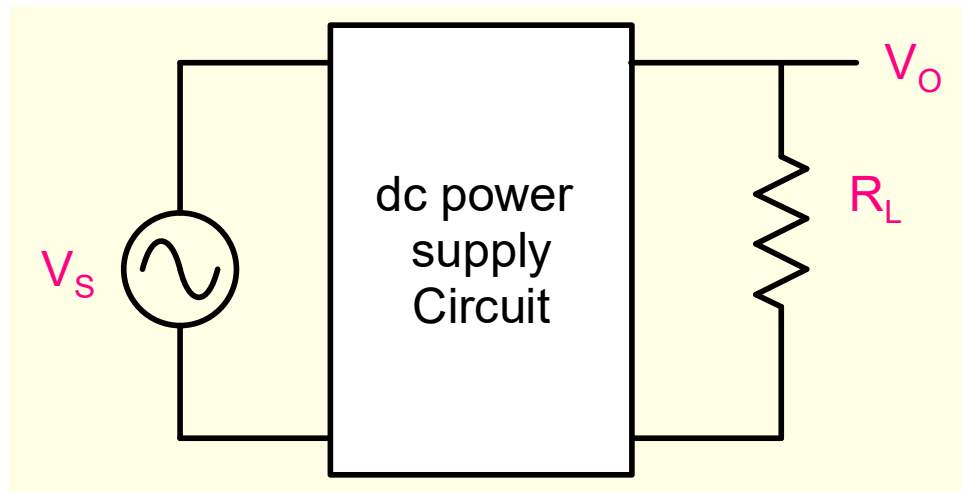


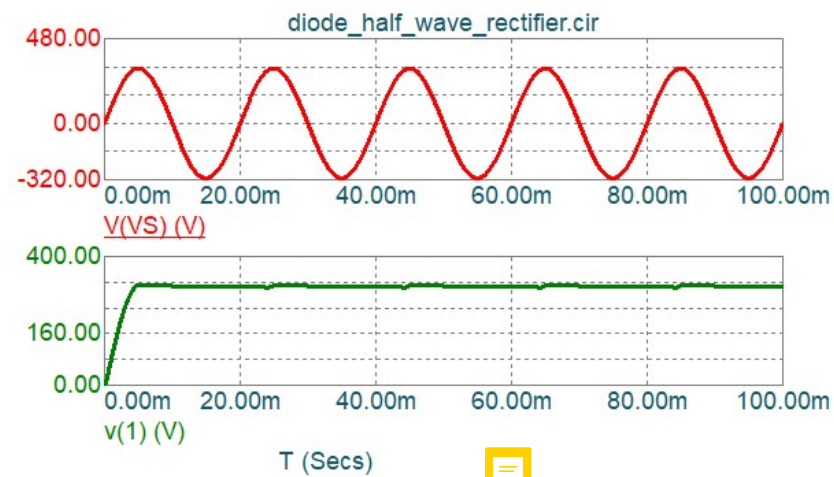
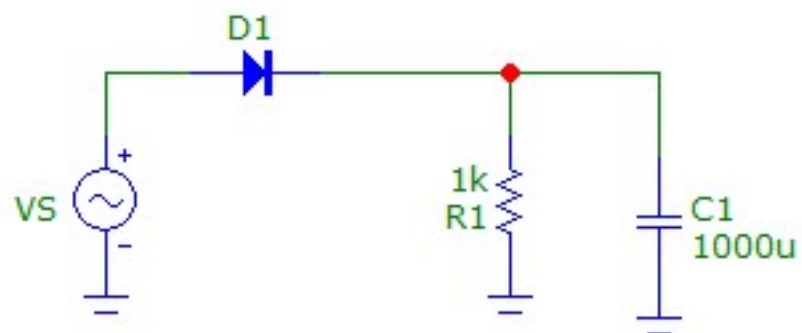
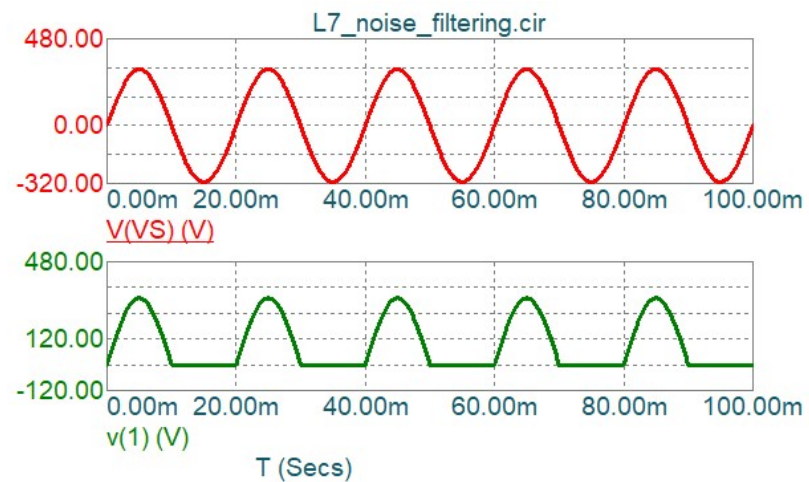
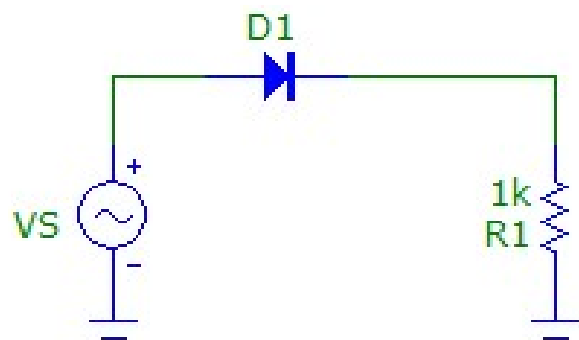
Variations over three different time scales are visible, only one of which is signal of interest



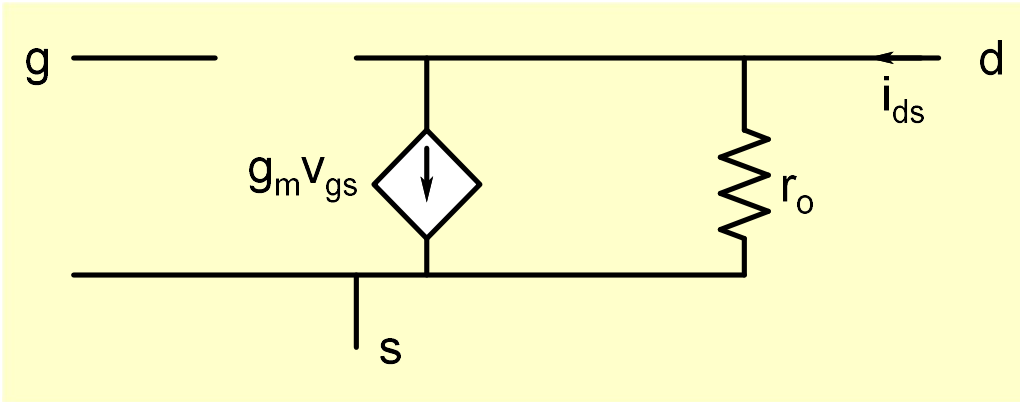
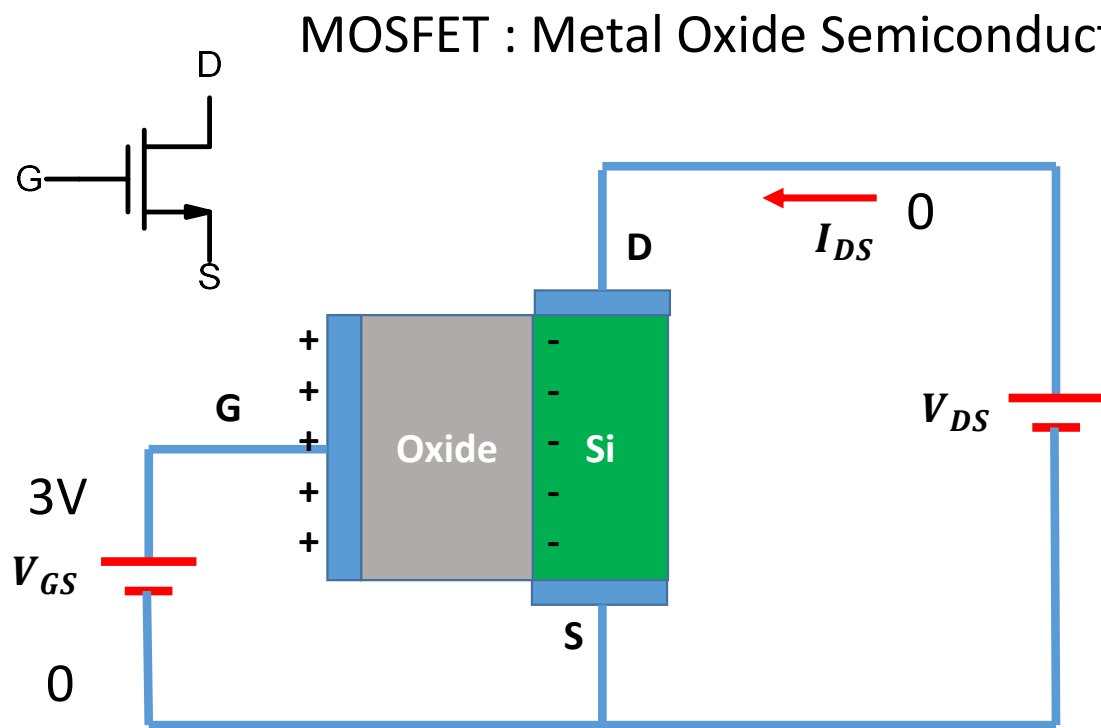
L7_noise_filtering.cir





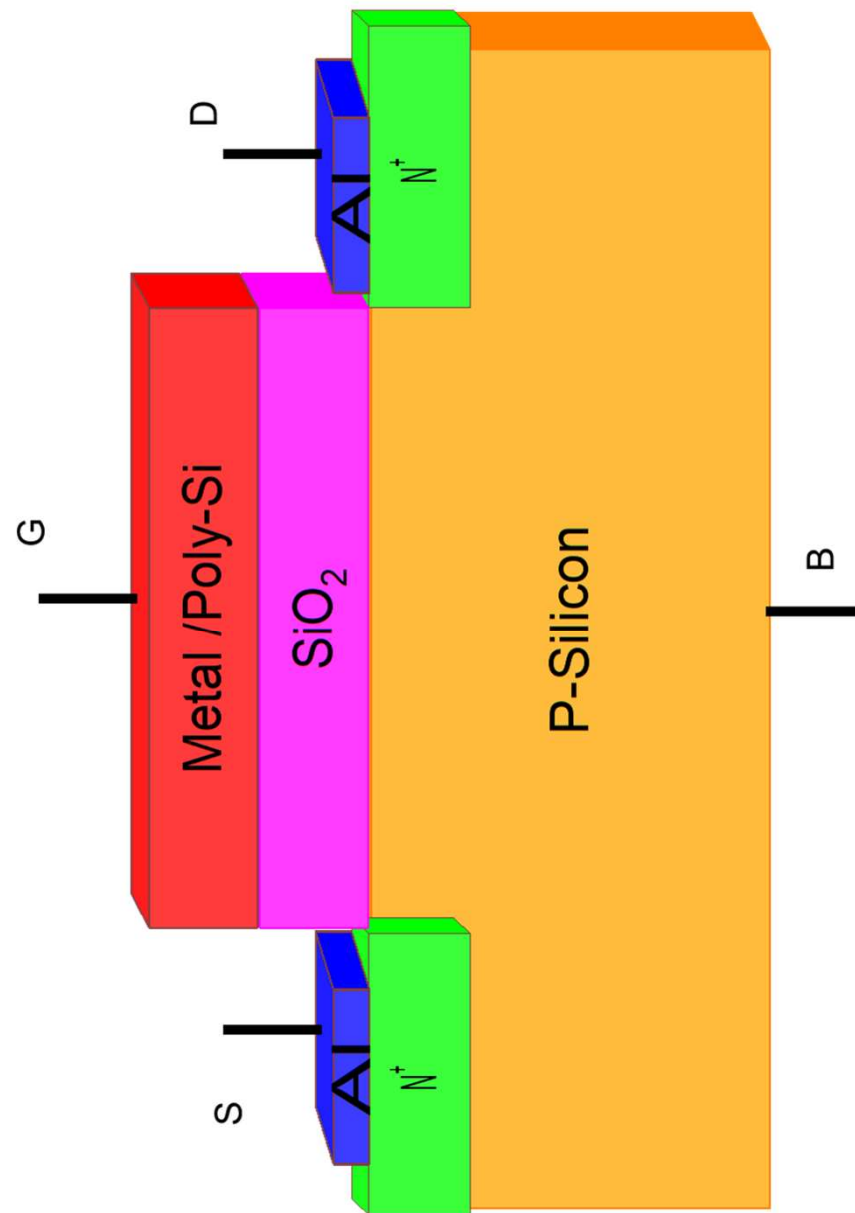
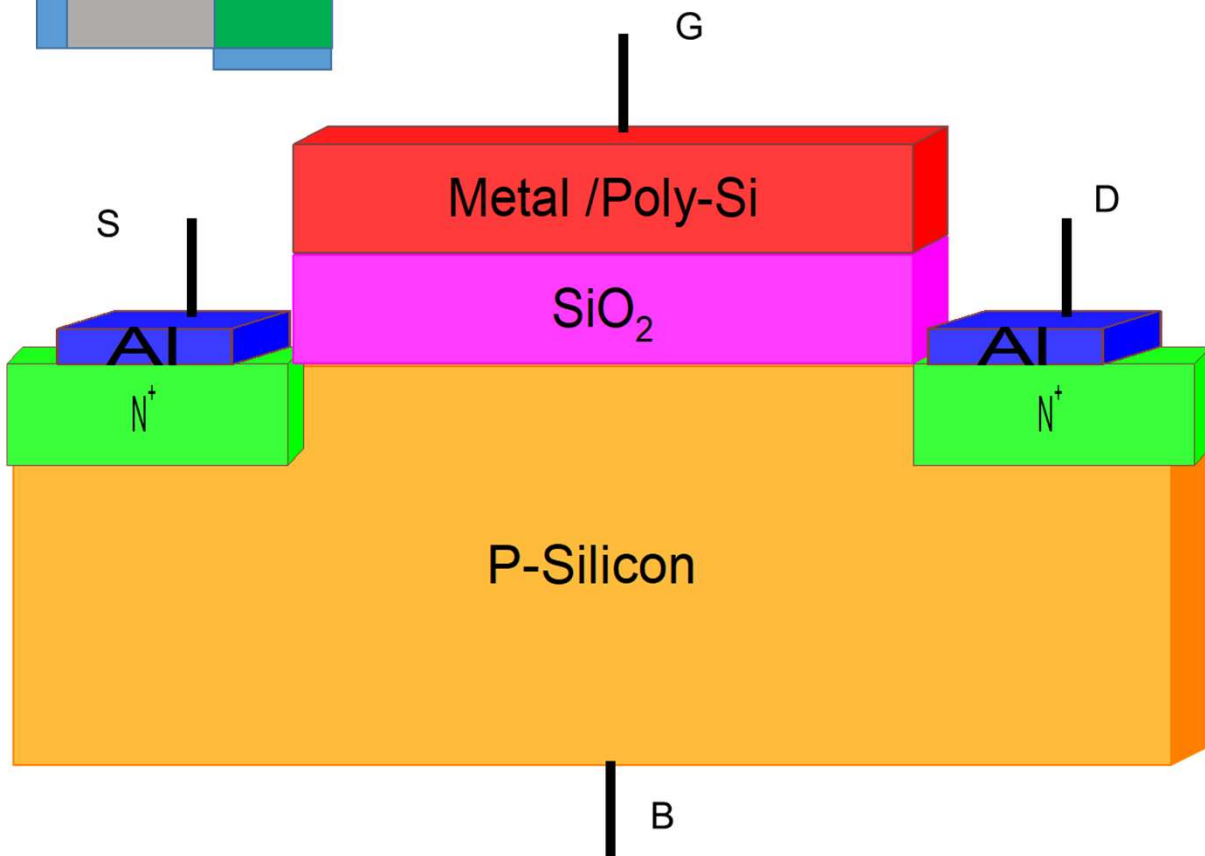
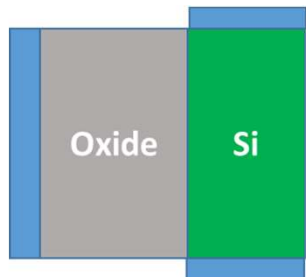


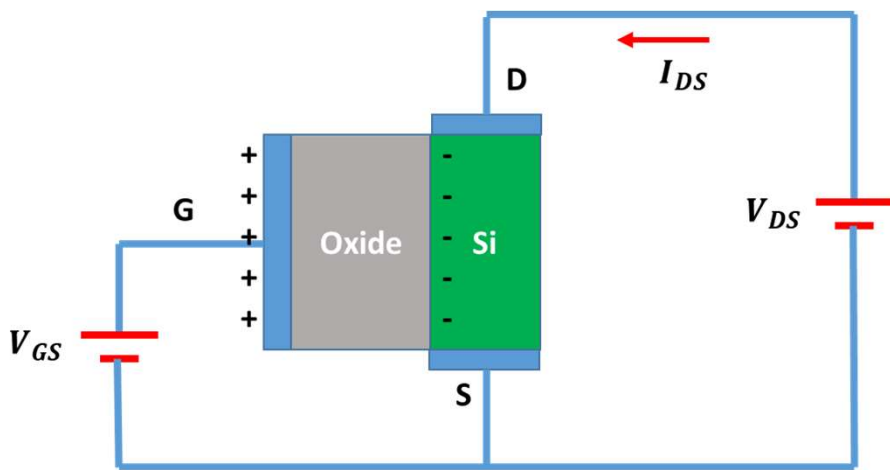
Fundamental Principle underlying Modern electronics is charge/discharge of a capacitor



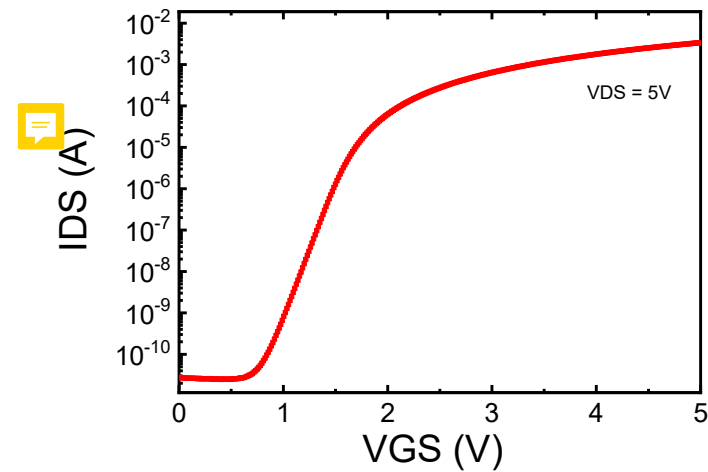
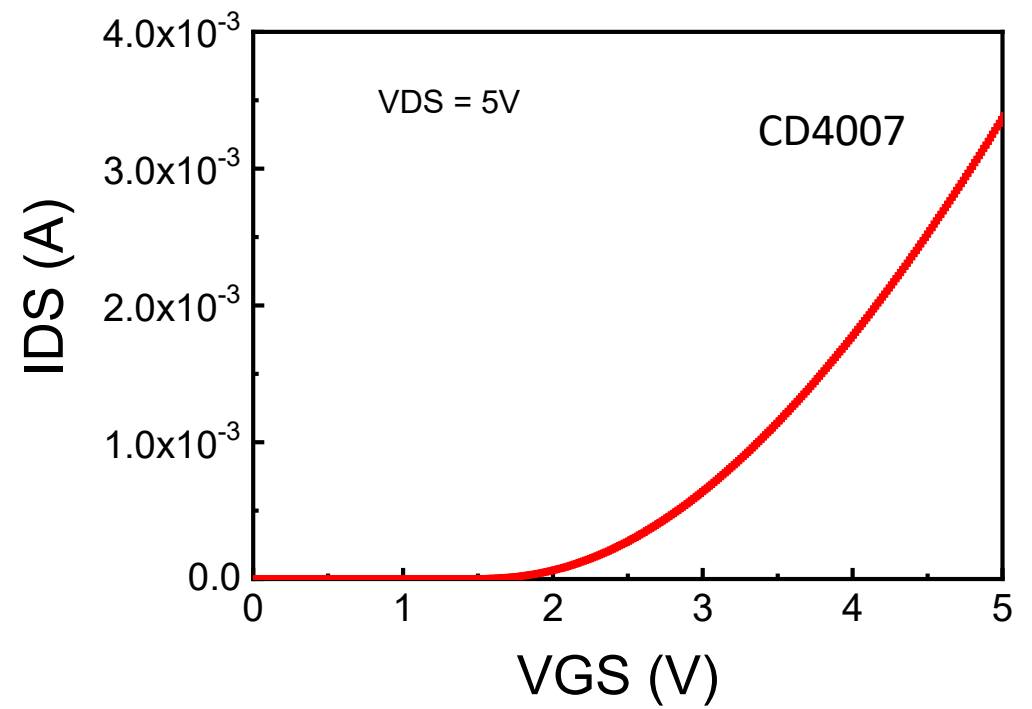
Voltage controlled current source

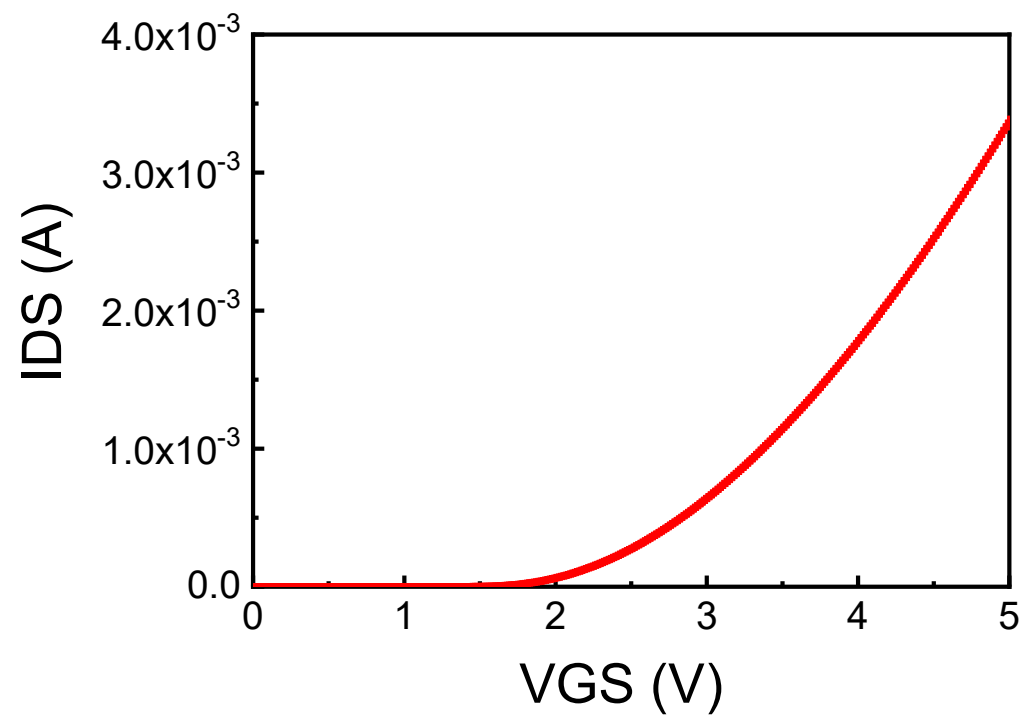
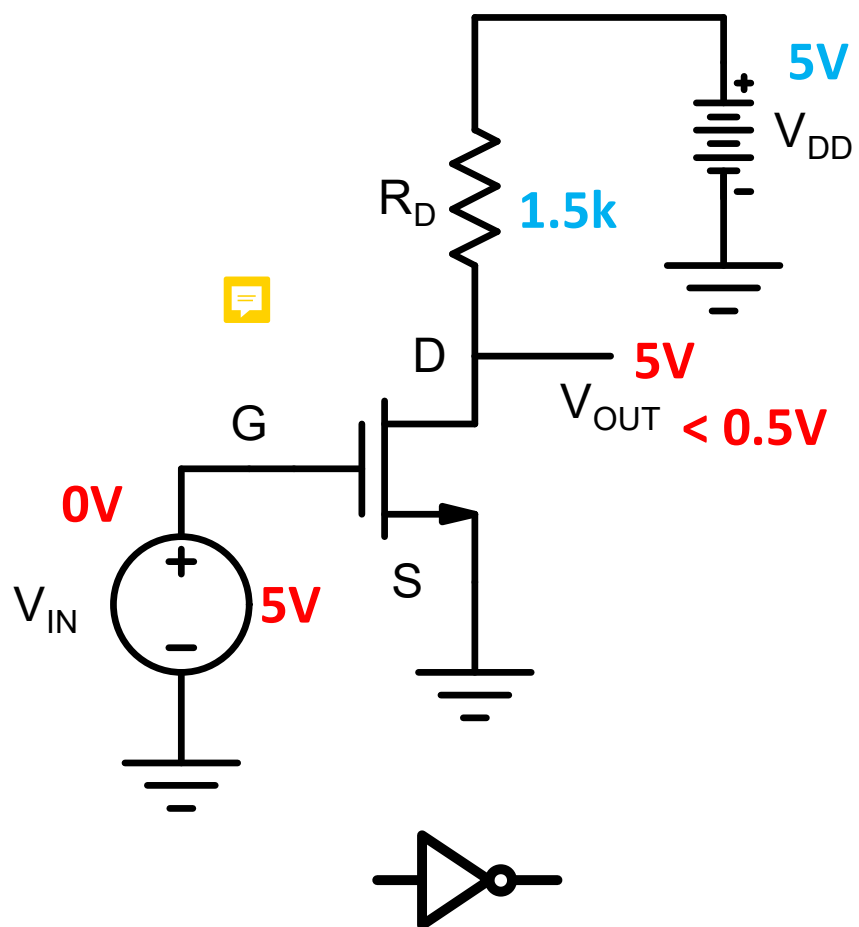
Drain current is controlled by gate voltage

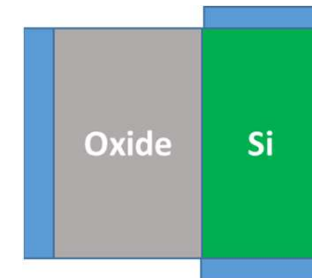
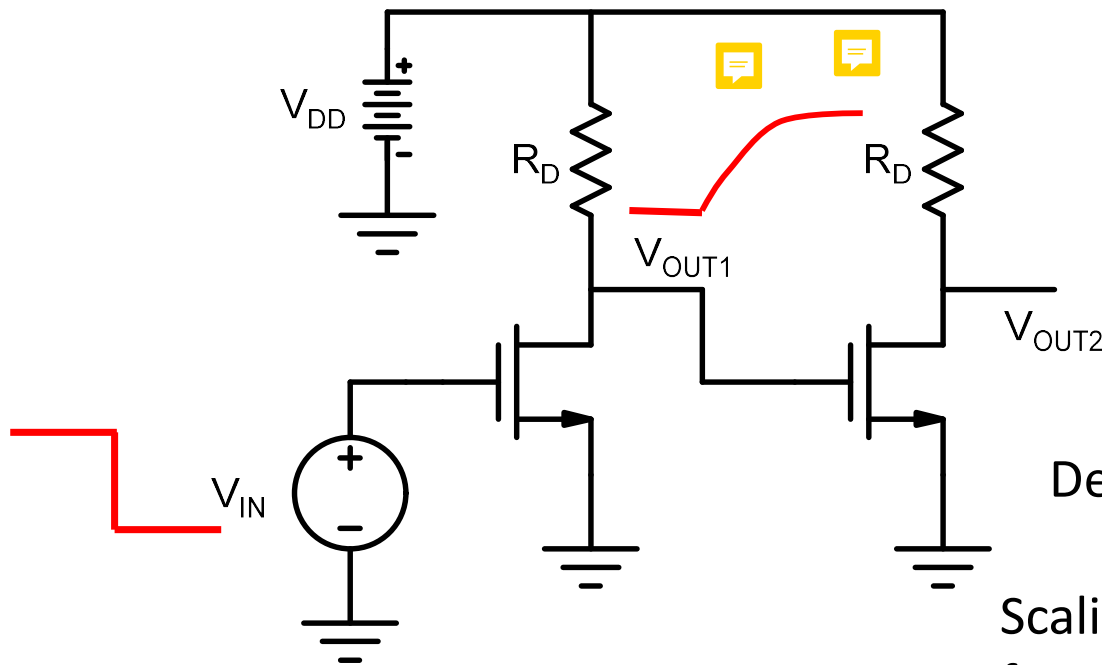
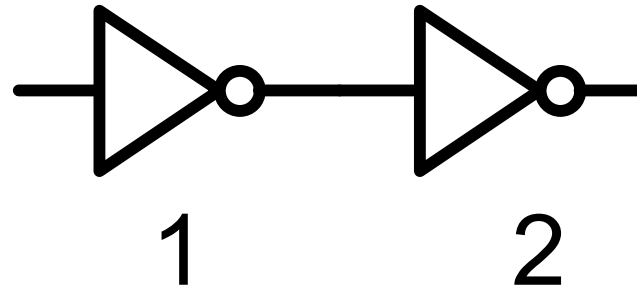




Drain current is controlled by gate voltage







Delay due to RC charge/discharge

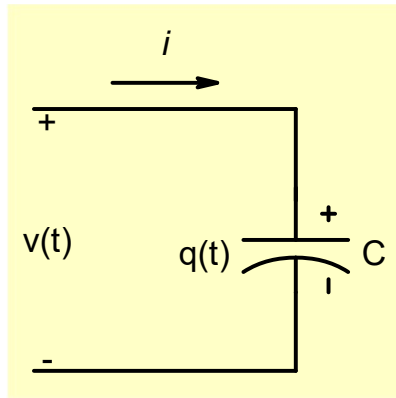
Scaling down of Transistor size leads to faster circuits

Capacitance

Two sheets of conductors separated by a layer of insulating material

– The insulating material is called dielectric. This could be air, polyester, ...





$$q = C \times v$$

Coulombs

Farad

Volt

$$i = \frac{dq}{dt}$$

$$i = C \frac{dv}{dt}$$

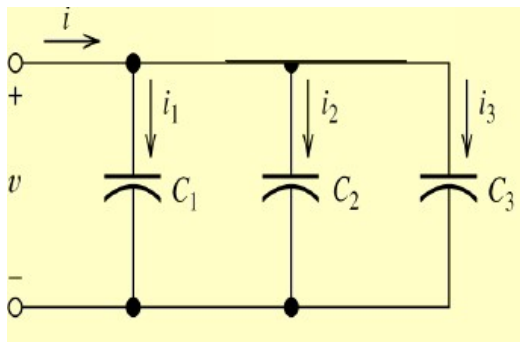
$$v(t) = \frac{1}{C} \int_{t_o}^t i dt + v(t_o)$$

$$w_c(t) = \frac{1}{2} C \times v_c^2(t)$$

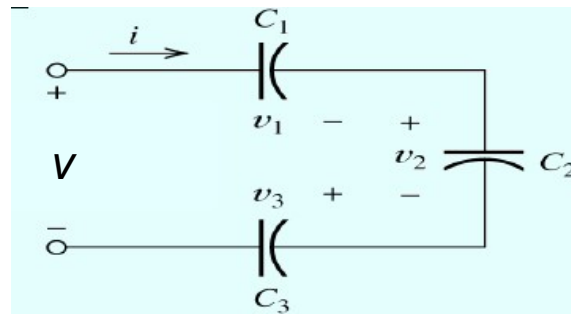
For dc or steady state when the voltage does not vary with time

$$i = 0$$

A capacitor under dc or steady state acts like an **open circuit**

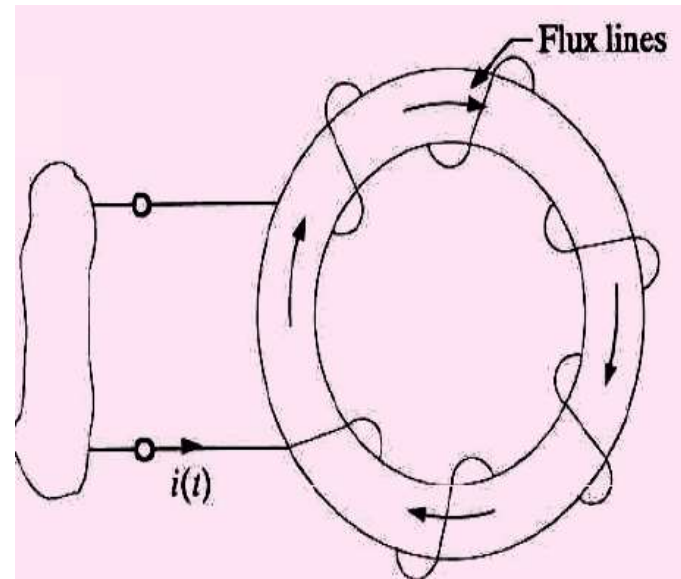
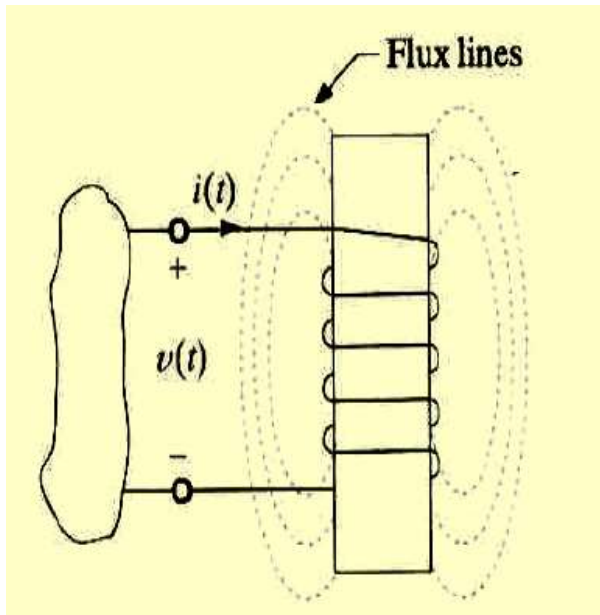


$$C_{eq} = C_1 + C_2 + C_3$$



$$\frac{1}{C_{eq}} = \frac{1}{C_1} + \frac{1}{C_2} + \frac{1}{C_3}$$

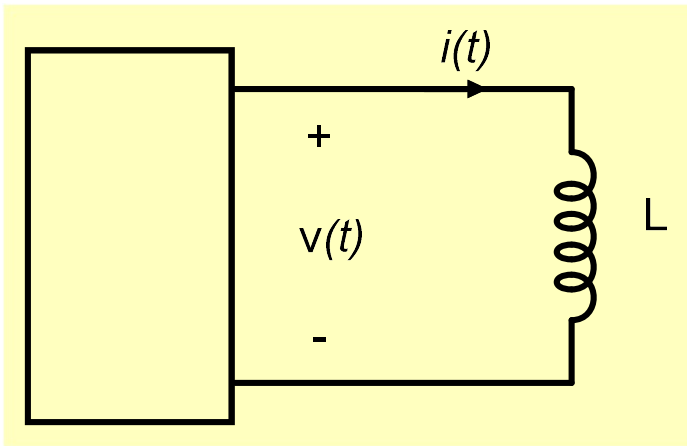
Inductance



$$\phi = L \times i$$

A time varying flux causes voltage to appear across the device terminals

$$v = \frac{d\phi}{dt} = L \times \frac{di}{dt}$$



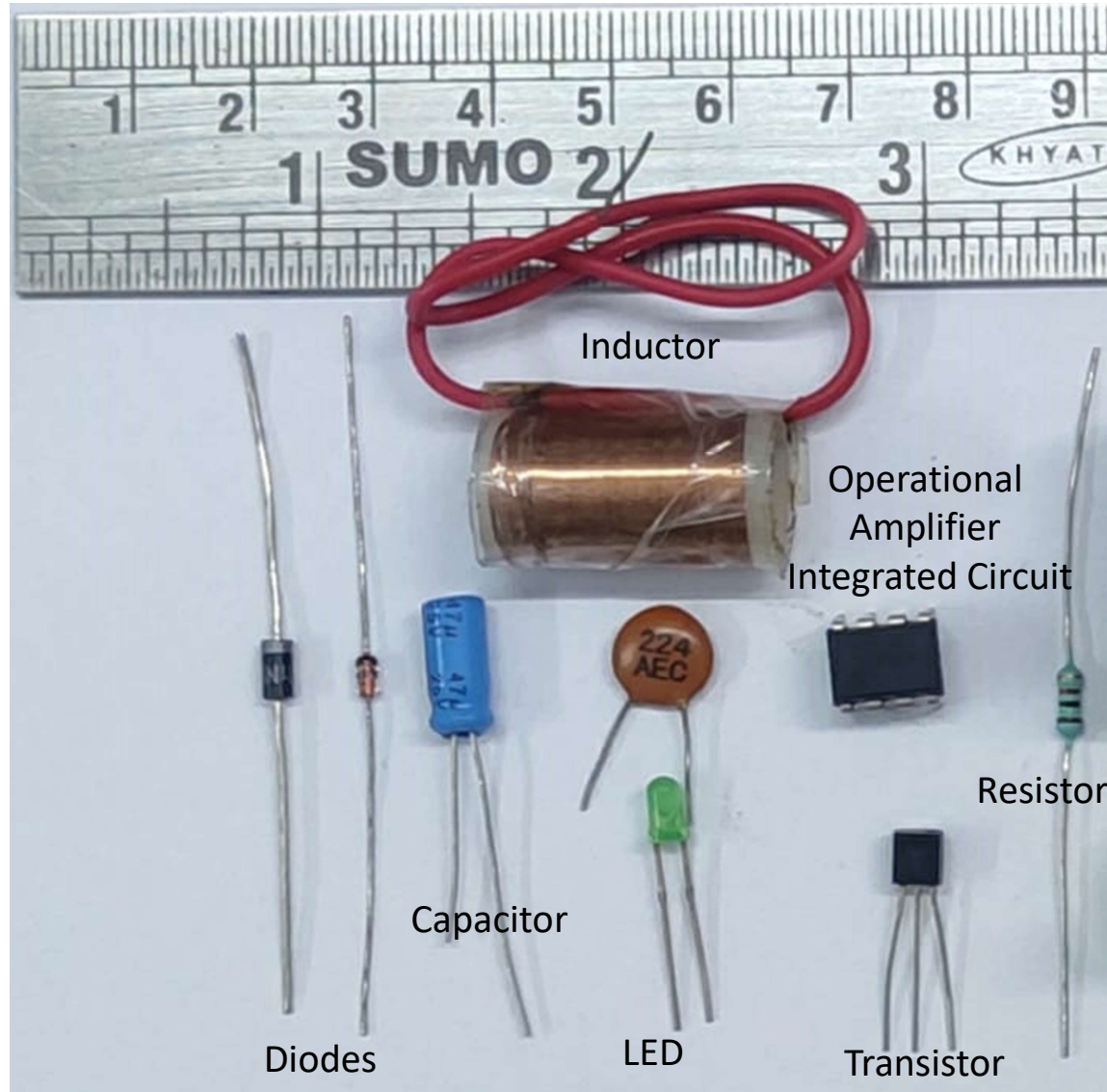
$$v = L \times \frac{di}{dt}$$

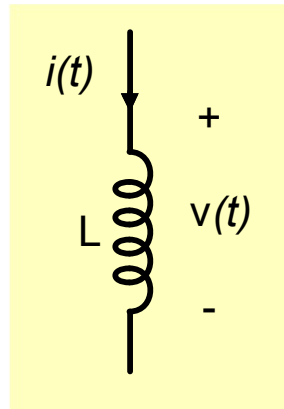
Henry

For dc or steady state when the current does not vary with time

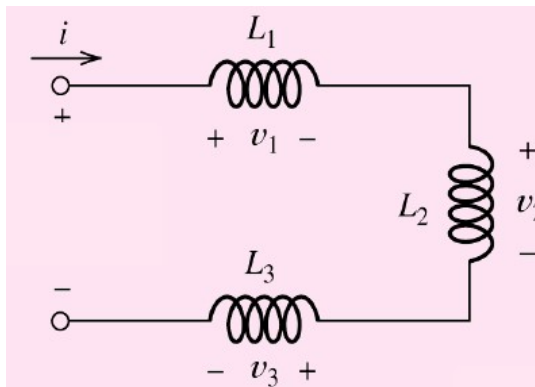
$$v = 0$$

An inductor under dc or steady state acts like a **short circuit**

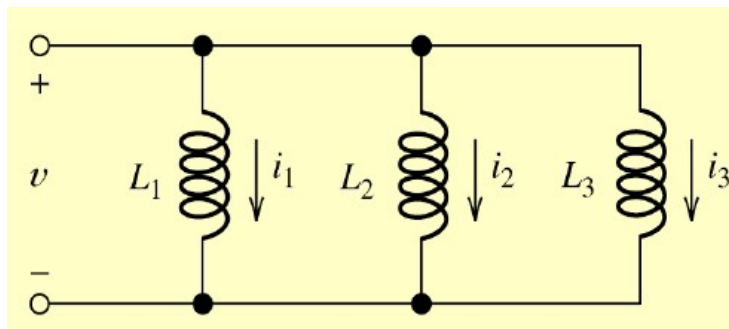




$$w_L(t) = \frac{1}{2} L \times i^2(t)$$



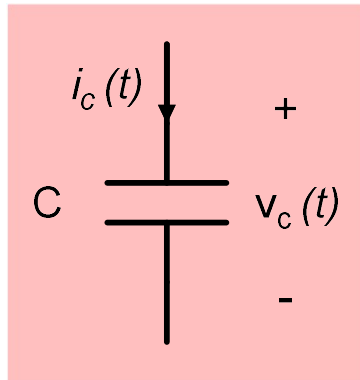
$$L_{eq} = L_1 + L_2 + L_3$$



$$\frac{1}{L_{eq}} = \frac{1}{L_1} + \frac{1}{L_2} + \frac{1}{L_3}$$

Two important concepts

Voltage across a capacitor cannot change instantaneously

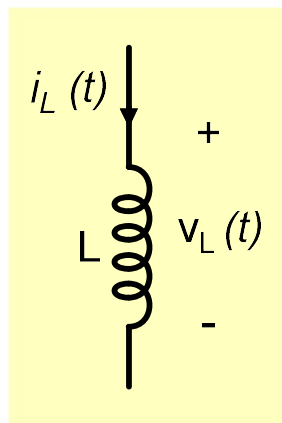


$$i_c = C \frac{dv_c}{dt}$$

$$v_c = \frac{1}{C} \int i_c(t) dt$$

Instantaneous change implies infinite current!

Current through an inductor cannot change instantaneously

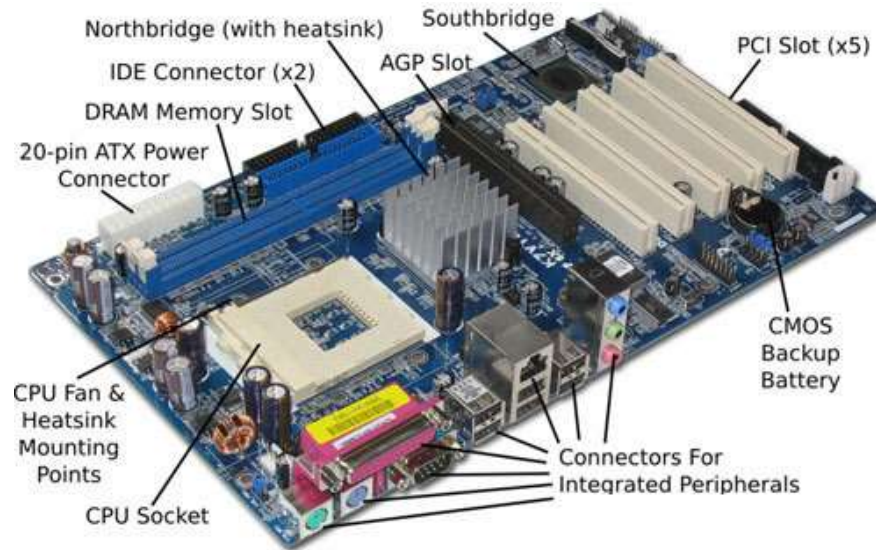


$$v_L = L \frac{di_L}{dt}$$

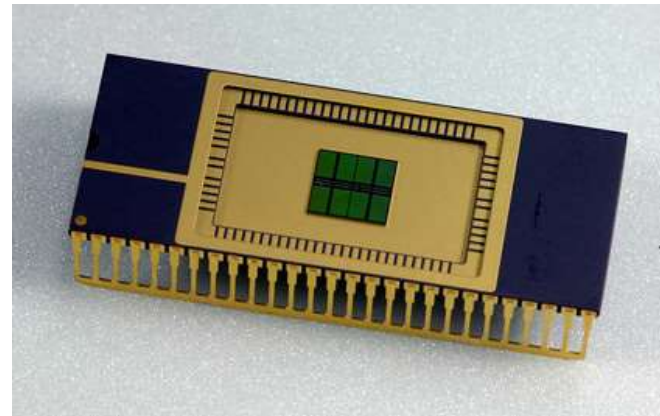
$$i_L = \frac{1}{L} \int v_L(t) dt$$

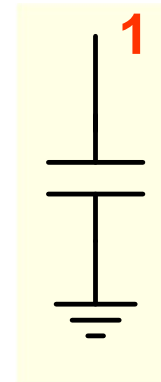
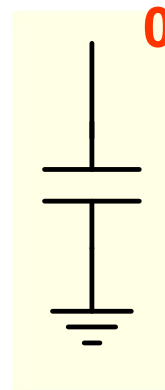
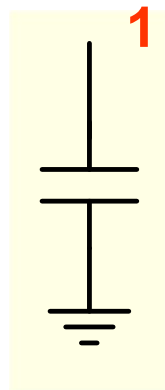
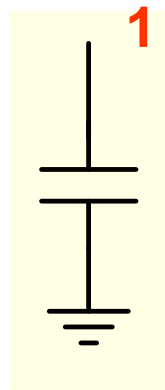
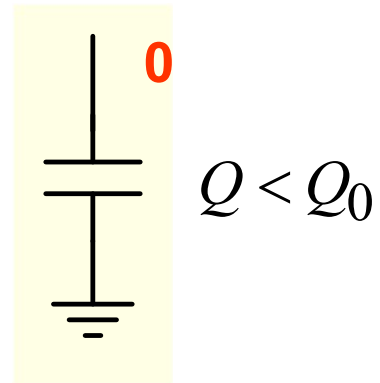
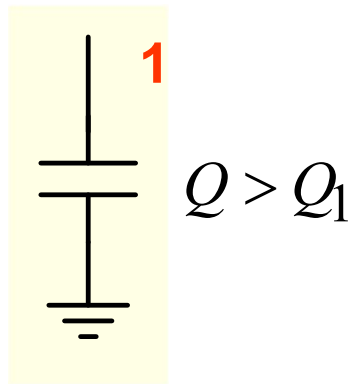
Instant change in voltage implies infinite voltage!

DRAM



Basic Storage Unit is a capacitor

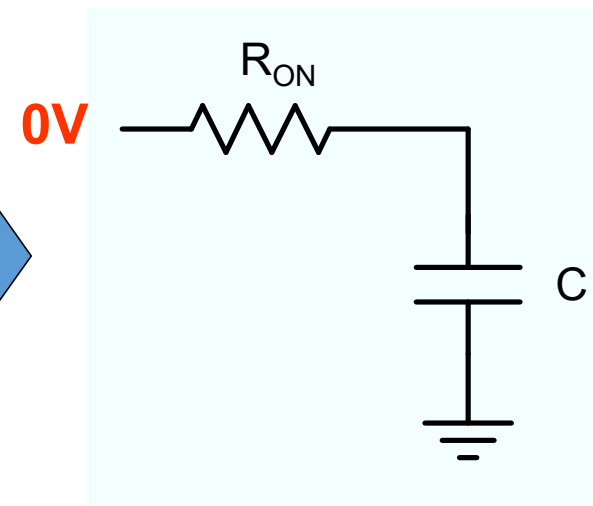
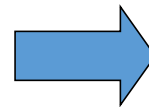
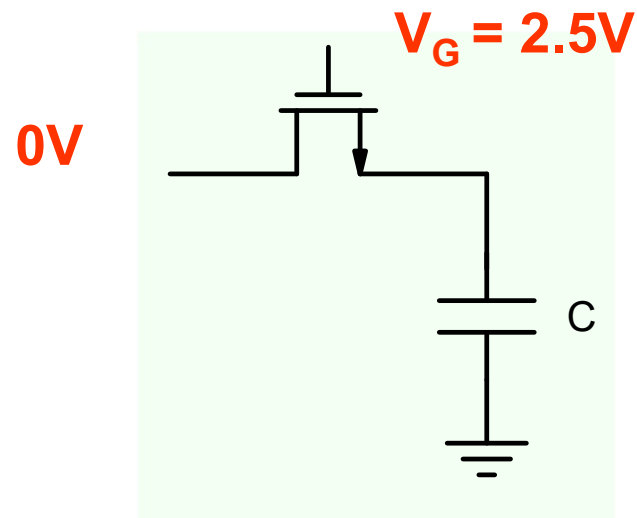
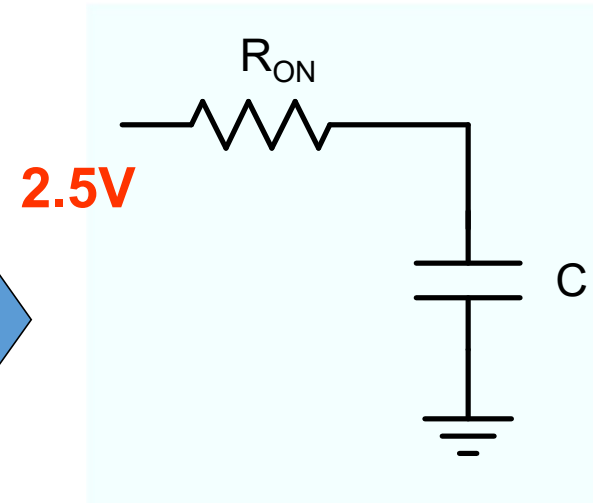
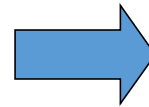
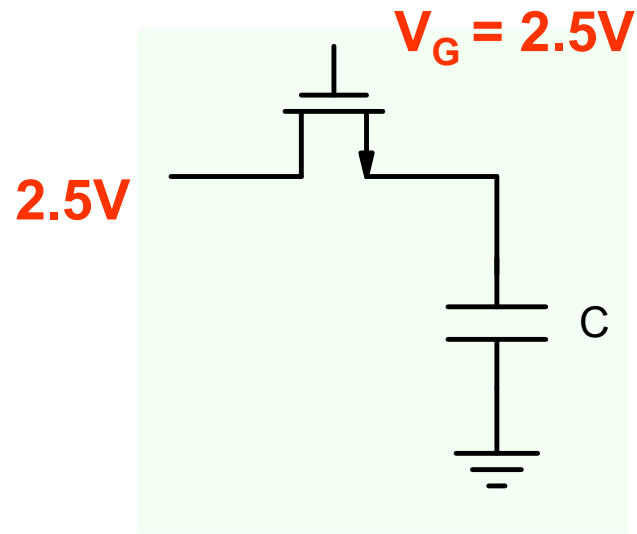


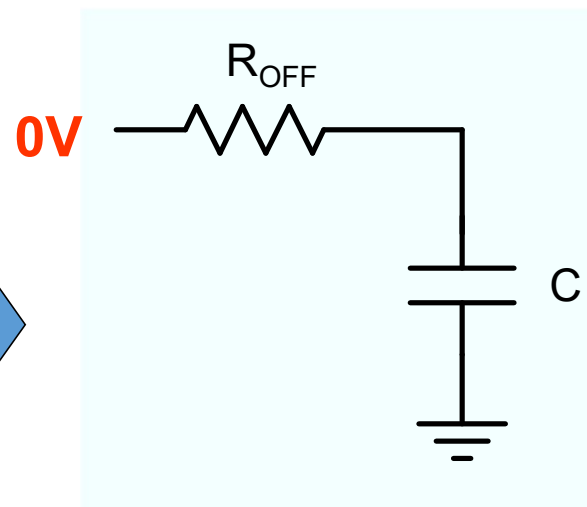
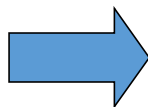
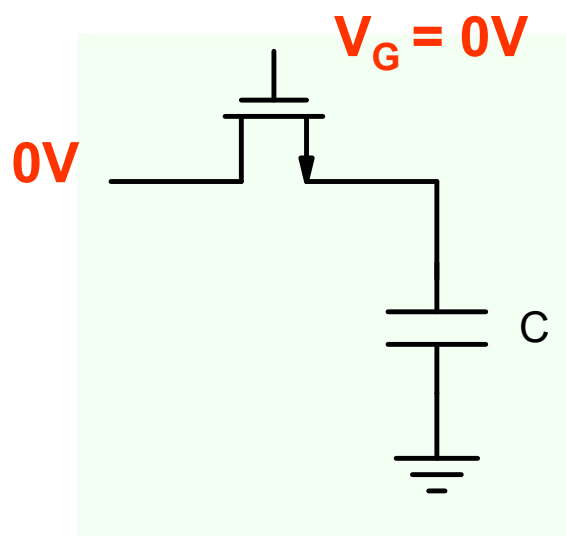


1101 in binary number format represents number 13

Each capacitor can store 1 bit of information

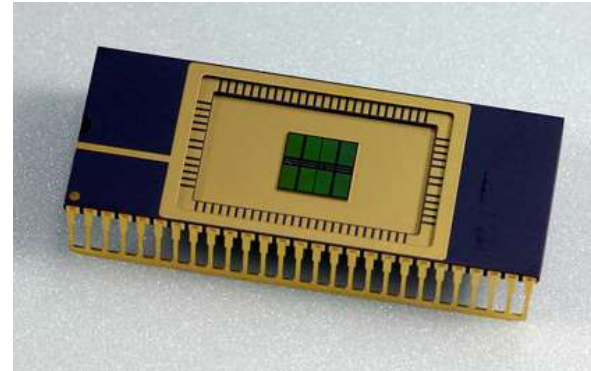
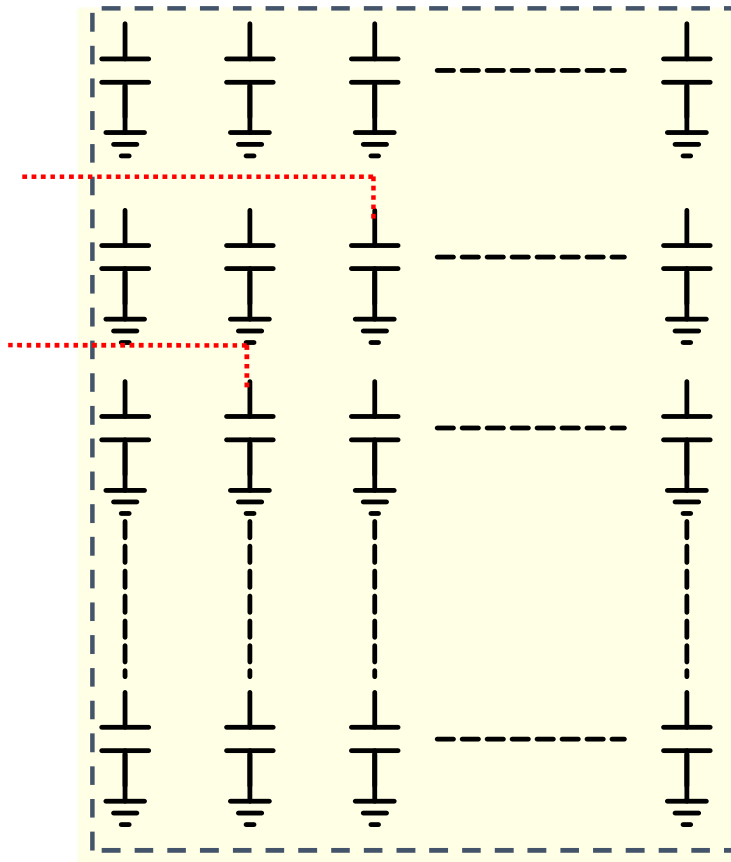
How do we store charge on the capacitor?





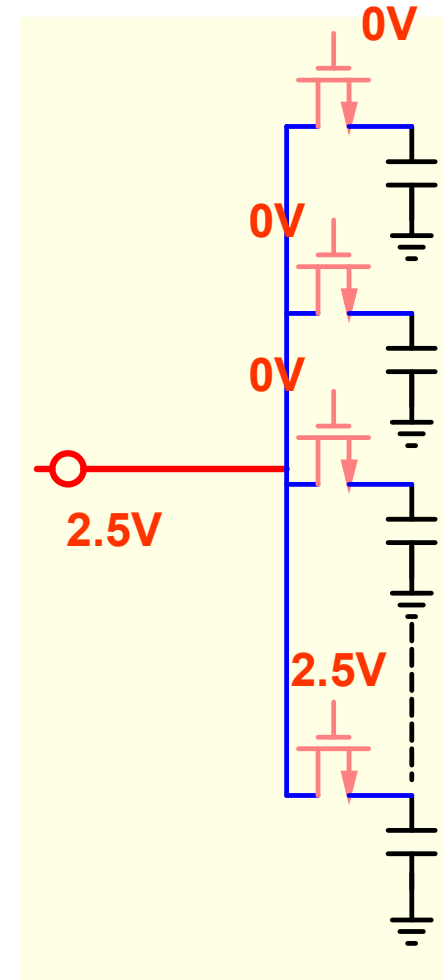
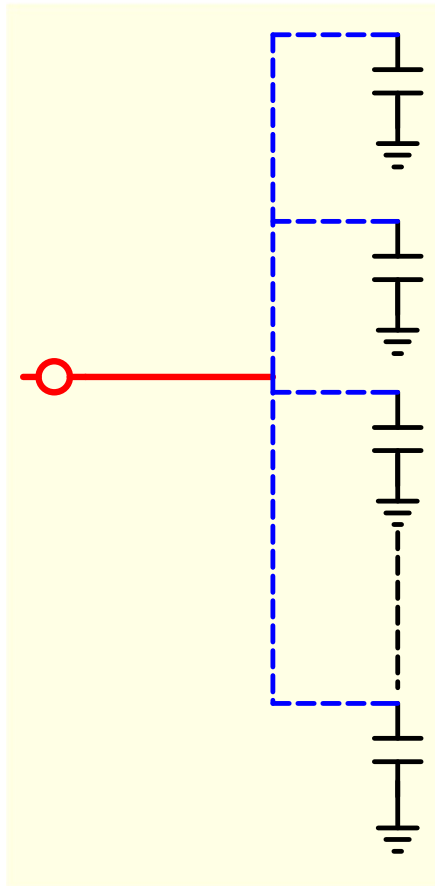
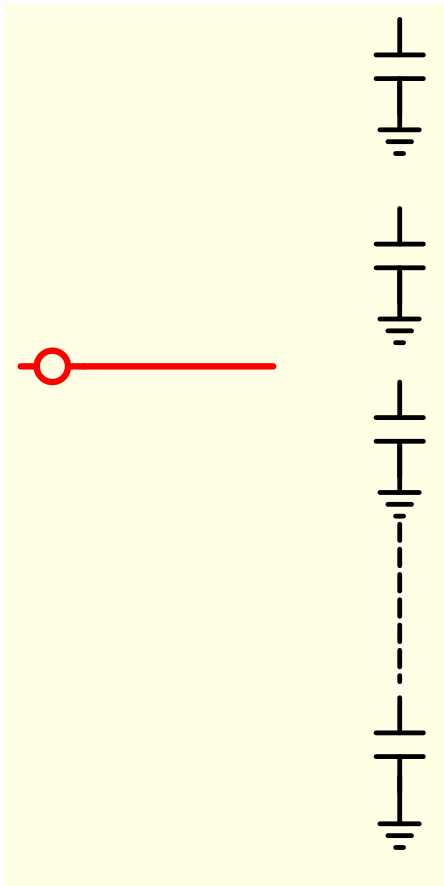
Why do we require transistor switches?

We have billions of capacitors in our overall memory ! How do we access them?



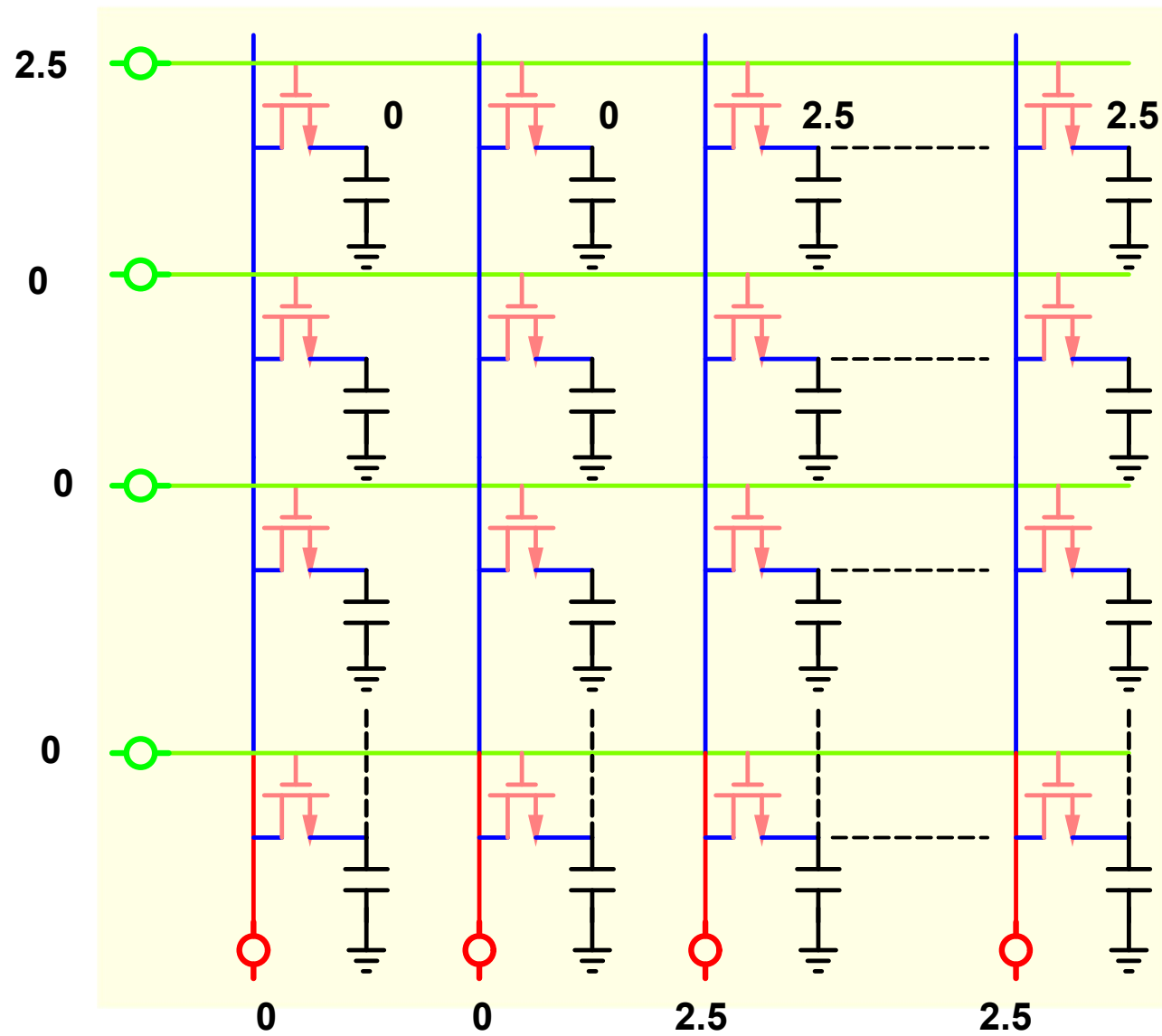
We do not have enough pins to individually access each capacitor !

One pin has to be connected to several capacitors !

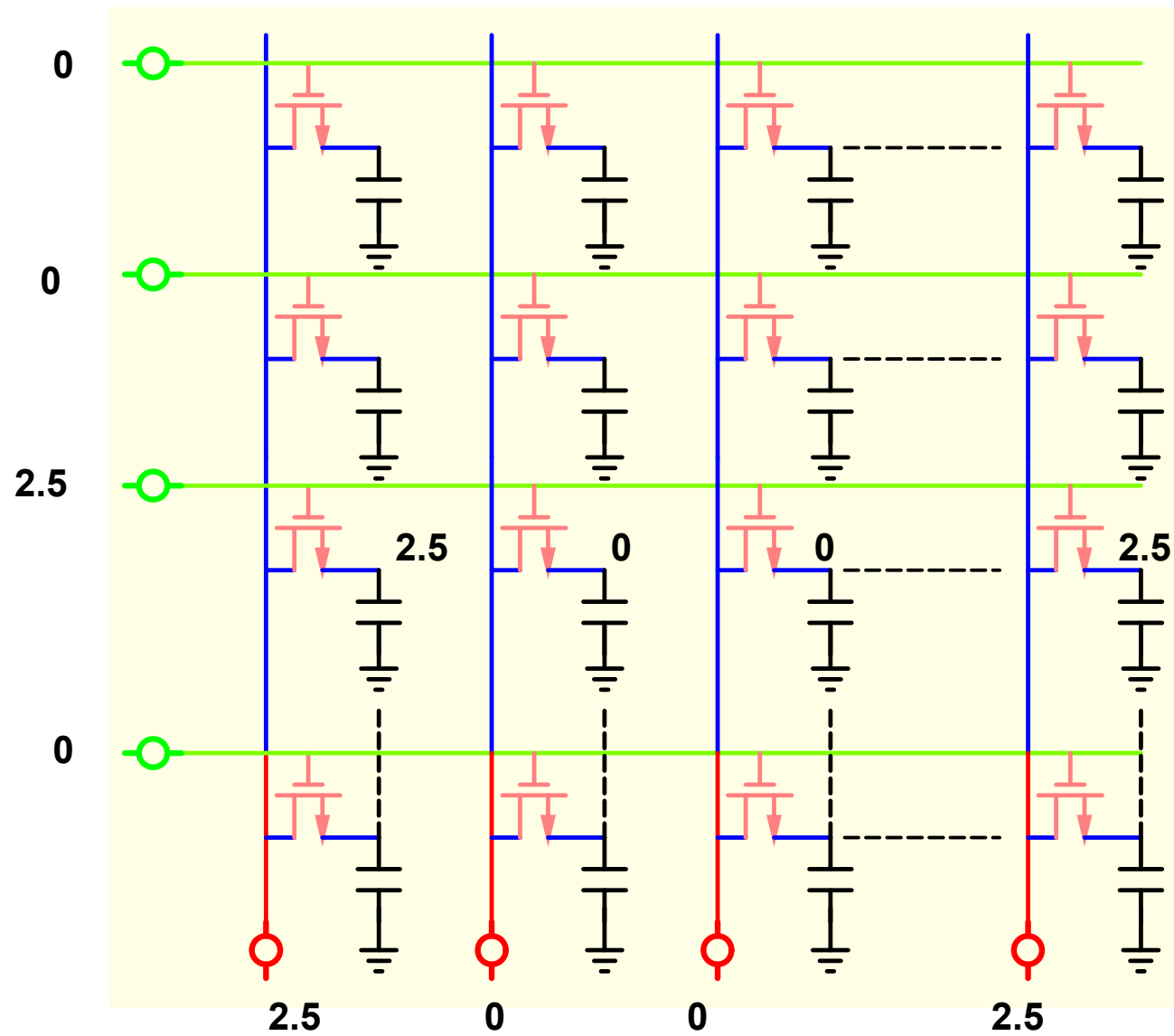


We can charge several capacitors through one single pin

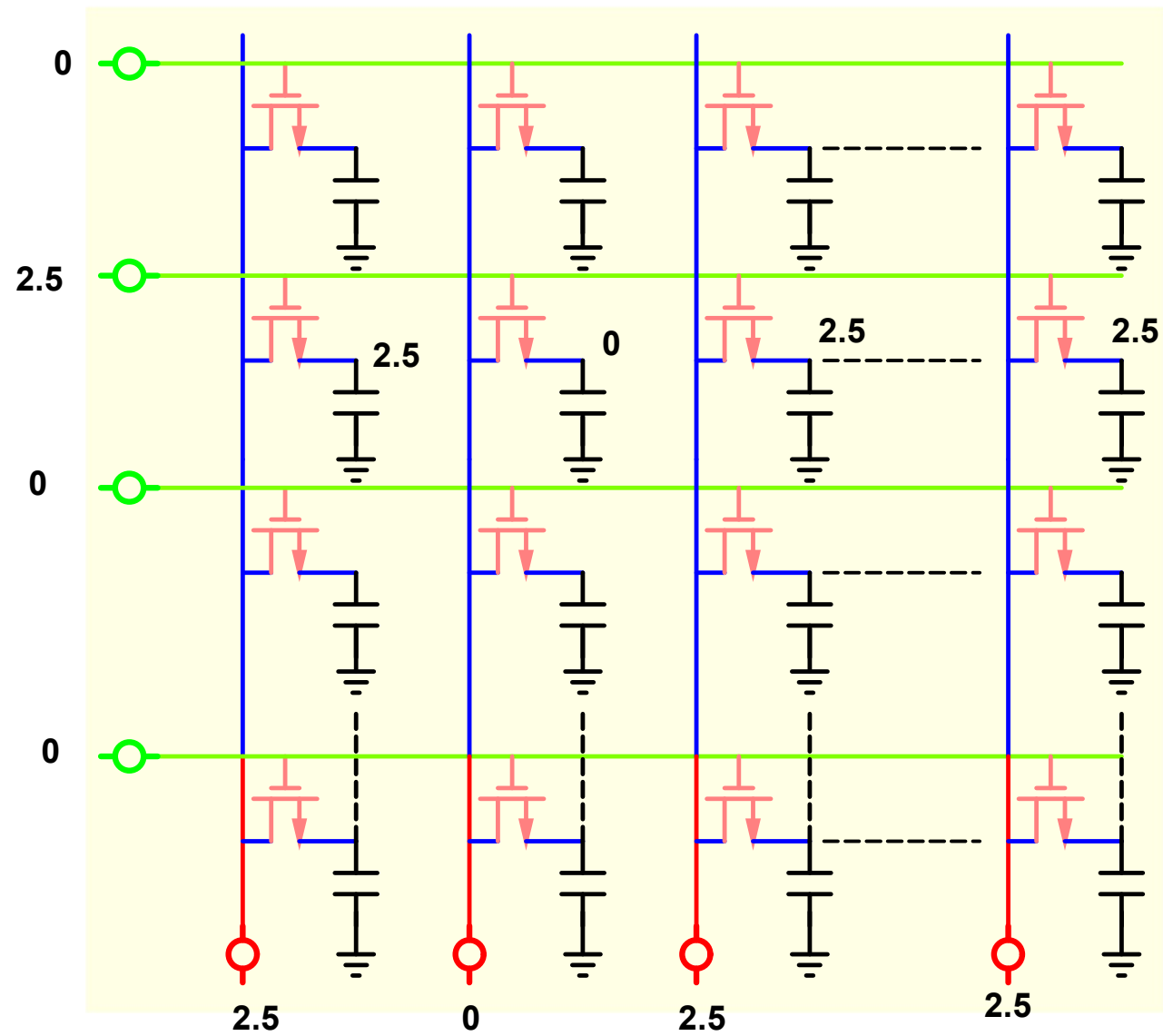
We can see the savings in pins now !

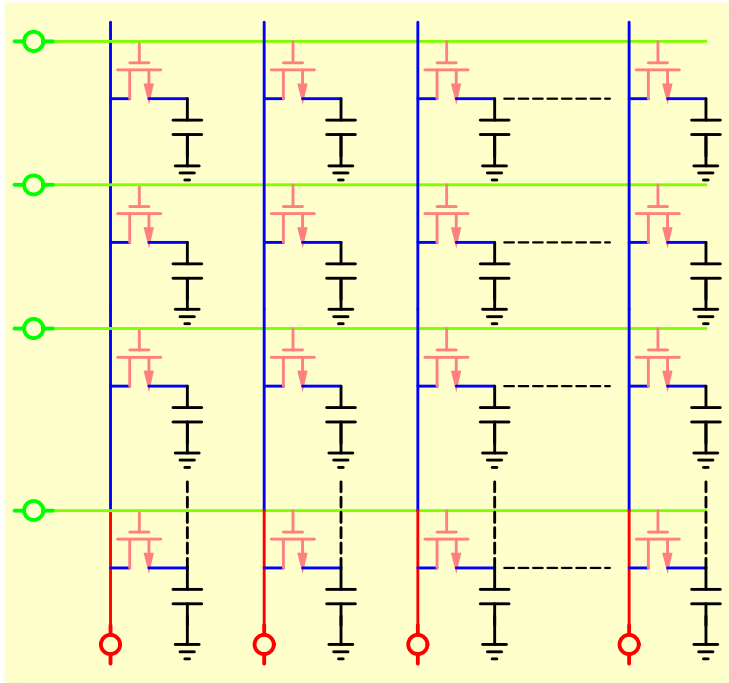


We can see the savings in pins now !

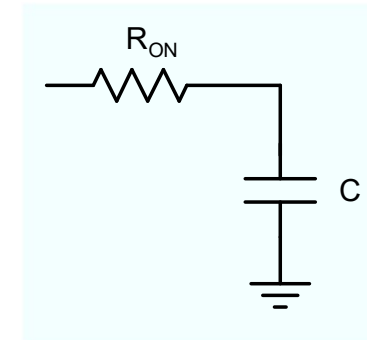
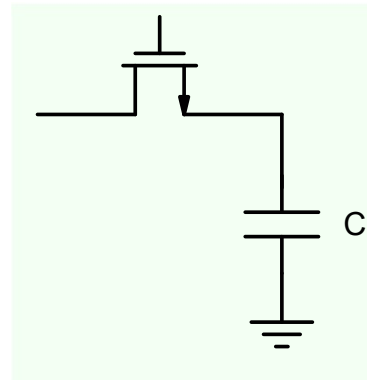


What about reading the memory?



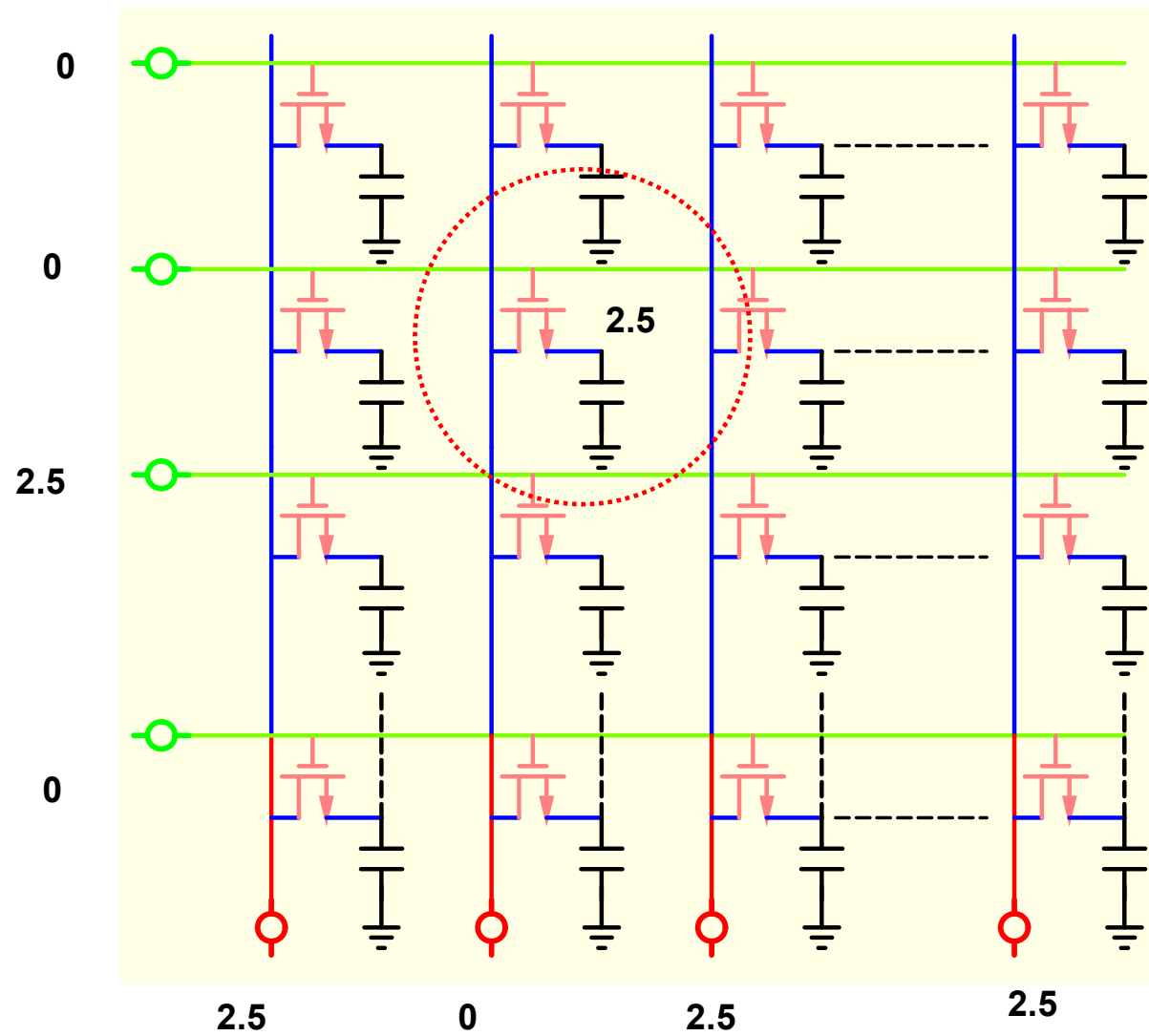


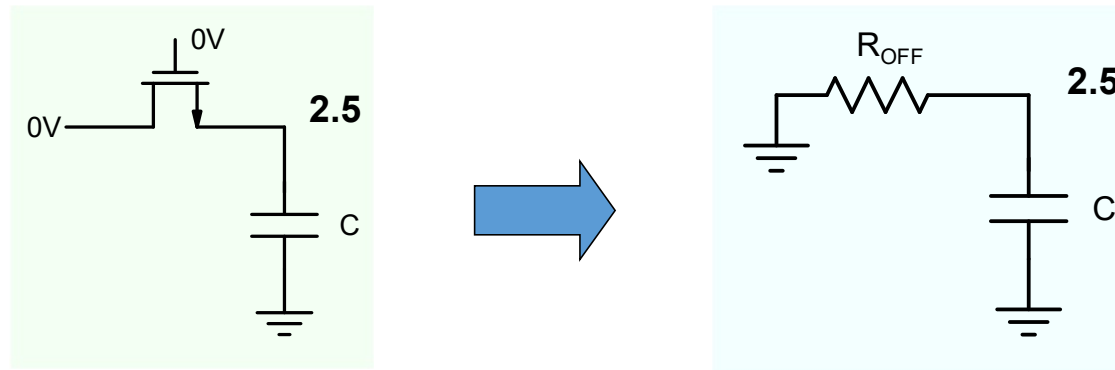
Writing is sequential in nature



Make C as small as possible for fast memory. This also helps in making dense memory !

Over time, the capacitor loses charge





The memory cells need to be refreshed periodically. Hence the name dynamic RAM