

RESUME

S. DIVYATH ALI



S/O, Shajahan S
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Personal profile

Father's Name

Shajahan.S

Mother's Name

Nisar banu.S

Date of Birth

12th June, 1995

Nationality

Indian

Linguistic Fluency:

English(S,W,R)

Tamil (S, W, R)

Passport Details

Passport No : N8821006

Issue Date : 9/6/2016

Expiry Date : 8/6/2026

Place of Issue: Coimbatore

Career Objective:

To work in a challenging environment where I can contribute myself that enables me to cope with emerging as well as latest technologies by continuous improvement in my knowledge.

Academic Credentials:

Course	Institution	Percentage / CGPA	Year
M.E VLSI Design and Embedded systems	Anna University, Madras Institute of Technology Campus, Chennai	7.57	2019
B.E Electronics and Communication Engineering	Bharathidasan institute of Technology, Trichy.	6.68	2016
HSC	Reliance matriculation Hr.Sec.School, Kumarapalayam.	87.5	2012
SSLC	Govt Hr.Sec.School, Kokkarayan pettai.	91.8	2010

Areas of Interest:

- Website design
- Embedded Programming.
- RTL design.
- VLSI Placement and Routing design

Project Details:

- Undertaken a Project on **"RUNTIME OPTIMIZATION FOR FPGA ROUTING"** that involves to optimization of routing and placement of FPGA.
- Undertaken a Project on **"FAST AND MEMORY COMPACT ROUTING ALGORITHM FOR FPGA"** which efficiently handles the data and prevent unnecessary routing traces.
- Undertaken a Project on **"SECURING WIRELESS SENSOR NETWORKS USING AES ENCRYPTION"** that involves securing wireless nodes in order to communicate with group tunneling protocol and hand shaking procedure are encrypted in AES encryption.
- Undertaken a Mini-Project on **"LINE FOLLOWER ROBOT"** that involves controlling the robot movement by following a line that can be used in various industrial transportation.
- Undertaken a Mini-Project on **"SMART WASHING MACHINES WITH REAL TIME CLOCK"** involves controlling the motor rotation to perform three different operating mode.

Computer Proficiency:

- **Programming Languages** : HTML, CSS, JS, Python, Embedded C, Verilog, Basics of C, C++.
- **Software Skills**
 - Website design : Flask,Django,visual studio,Node.js,Atom.,Angular,React.
 - Electronics design : Altera Quartus II, Cadence virtuoso, VPR,Xilinx ISE, Digital logic design, PSpice,Vivado14.5,VMware.
 - Embedded design : Open CV,Anaconda,Micro C, Arduino UNO, Proteus VSM,MATLAB.
 - Basic software : MS-Office.

Co-Curricular Activities:

- Participated and present the paper of “Memory efficient Routing algorithm for FPGA” in the conference held in Anna University-CEG campus, Chennai.
- Participated and present the paper of “Fast and Memory compact algorithm for FPGA” in IEEE 1st International Conference on Energy, Systems and Information Processing.
- Underwent a course on Network Administration in “Bharat Sanchar Nigam Limited “at Erode.
- Attended one days of workshop at "Ethical hacking and crypto security” at Tanjore.
- Attended one days of workshop at “Advanced digital design flow using Cadence” at MIT campus, Chennai.

Extra-Curricular Activities:

- Co-ordinated Gaming event Kho-Kho in Bharathidasan institute of Technology, Trichy.
- Have won first place in Kho-Kho inter-college tournament in University College of engineering, Pattukkottai.
- Member of “National social service” in University College of engineering, Pattukkottai.

Responsibilities Undertaken:

- Organizer of the event “**CIRCUIT DEBUGGING**” in a National Level Technical Symposium conducted during September, 2015 in Bharathidasan institute of Technology. Trichy.
- Co - Organizer of the symposium TROY’15, a National Level Technical Symposium conducted during September 2015 in Bharathidasan institute of Technology. Trichy.
- Organizer of the symposium EXNORA’13 and EXNORA’14, a National Level Technical Symposium conducted during August 2013 and 2014 in UCE Pattukkottai campus, Pattukkottai.
- Office bearer of **ECE Association** in Bharathidasan institute of Technology, Trichy during 2015-16.

Experience:

- Currently doing Internship at Tech safe as a Embedded Programmer (May 2019 - Present).

Declaration:

I, S. DIVIYATH ALI, do hereby solemnly declare that all the particulars furnished by me are true to the best of my knowledge.

Place :

Date :



[S. DIVIYATH ALI]