

S. DIVYATH ALI



Address:

6/67, west street, kokkarayan
pettai, Kumarapalayam(Tk),
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Email ID:

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Contact Number:

+91 6374413806

Personal Profile

Father Name

Shajahan S

Mother Name

Nisar Banu S

Date of Birth

12th June, 1995

Nationality

Indian

Linguistic Fluency

English (S,W,R)

Tamil (S,W,R)

Hindi (S)

Passport Details

Passport No : N8821006

Issue Date : 9/6/2016

Expiry Date : 8/6/2026

Place of Issue: Coimbatore

Resume

Career Objective:

To work in a challenging environment where I can contribute myself that enables me to cope with emerging as well as latest technologies by continuous improvement in my knowledge.

Areas of Interest:

- Web Development.
- RTL Design.
- VLSI Architecture.
- Embedded Programming.

Project Details:

20+ Projects completed. (VLSI, ML, Web design)

Check in: <https://github.com/Divyathali?tab=repositories>

Certification:

15+ Certification completed.

Check in: <https://github.com/Divyathali/Certificate>

Paper published:

- IEEE explore – **Fast and Memory compact Routing Algorithm for FPGA.**

Check in: <https://ieeexplore.ieee.org/document/8938275/authors#authors>

- Participated and present the paper of “**Memory efficient Routing algorithm for FPGA**” in the conference held in Anna University-CEG campus, Chennai.

Academic Credentials:

| Course | Institution | Percentage / CGPA | Year |
|---|---|-------------------|------|
| M.E VLSI Design and Embedded systems | Anna University, Madras Institute of Technology Campus, Chennai | 7.57 | 2019 |
| B.E Electronics and Communication Engineering | Bharathidasan institute of Technology, Trichy. | 6.68 | 2016 |
| HSC | Reliance matriculation Hr. Sec.School, Kumarapalayam. | 87.5 | 2012 |
| SSLC | Govt Hr. Sec.School, Kokkarayan pettai. | 91.8 | 2010 |

Computer Proficiency:

- **Programming Languages** : Verilog, Python, HTML, CSS, JS, Embedded C, Basics of C, C++.
- **Software Skills**
 - Electronics design : Altera Quartus II, Cadence virtuoso, VPR, Xilinx ISE, Digital logic design, PSpice, Vivado14.5, VMware.
 - Embedded design : Open CV, Anaconda, Micro C, Arduino UNO, Proteus VSM.
 - Basic software : MS-Office.

Co-Curricular Activities:

- Participated and present the paper of “Memory efficient Routing algorithm for FPGA” in the conference held in Anna University-CEG campus, Chennai.
- Participated and present the paper of “Fast and Memory compact algorithm for FPGA” in IEEE 1st International Conference on Energy, Systems and Information Processing.
- Underwent a course on Network Management in “Bharat Sanchar Nigam Limited “at Erode.
- Attended one days of workshop at "Ethical hacking and crypto security” at Tanjore.
- Attended one days of workshop at “Advanced digital design flow using Cadence” at MIT campus, Chennai.

In-Plant Training Details:

- Bharat Sanchar Nigam Limited, Erode.

Extra-Curricular Activities:

- Co-ordinated Gaming event Kho-Kho in Bharathidasan institute of Technology, Trichy.
- Have won first place in Kho-Kho inter-college tournament in University College of engineering, Pattukkottai.
- Member of “National social service” in University College of engineering, Pattukkottai.

Responsibilities Undertaken:

- Organizer of the event “**CIRCUIT DEBUGGING**” in a National Level Technical Symposium conducted during September, 2015 in Bharathidasan institute of Technology. Trichy.
- Co - Organizer of the symposium TROY’15, a National Level Technical Symposium conducted during September 2015 in Bharathidasan institute of Technology. Trichy.
- Organizer of the symposium EXNORA’13 and EXNORA’14, a National Level Technical Symposium conducted during August 2013 and 2014 in UCE Pattukkottai campus, Pattukkottai.
- Office bearer of **ECE Association** in Bharathidasan institute of Technology, Trichy during 2015-16.

Declaration:

I, S. DIVIYATH ALI, do hereby solemnly declare that all the particulars furnished by me are true to the best of my knowledge.



Place:

Date:

[S. DIVIYATH ALI]