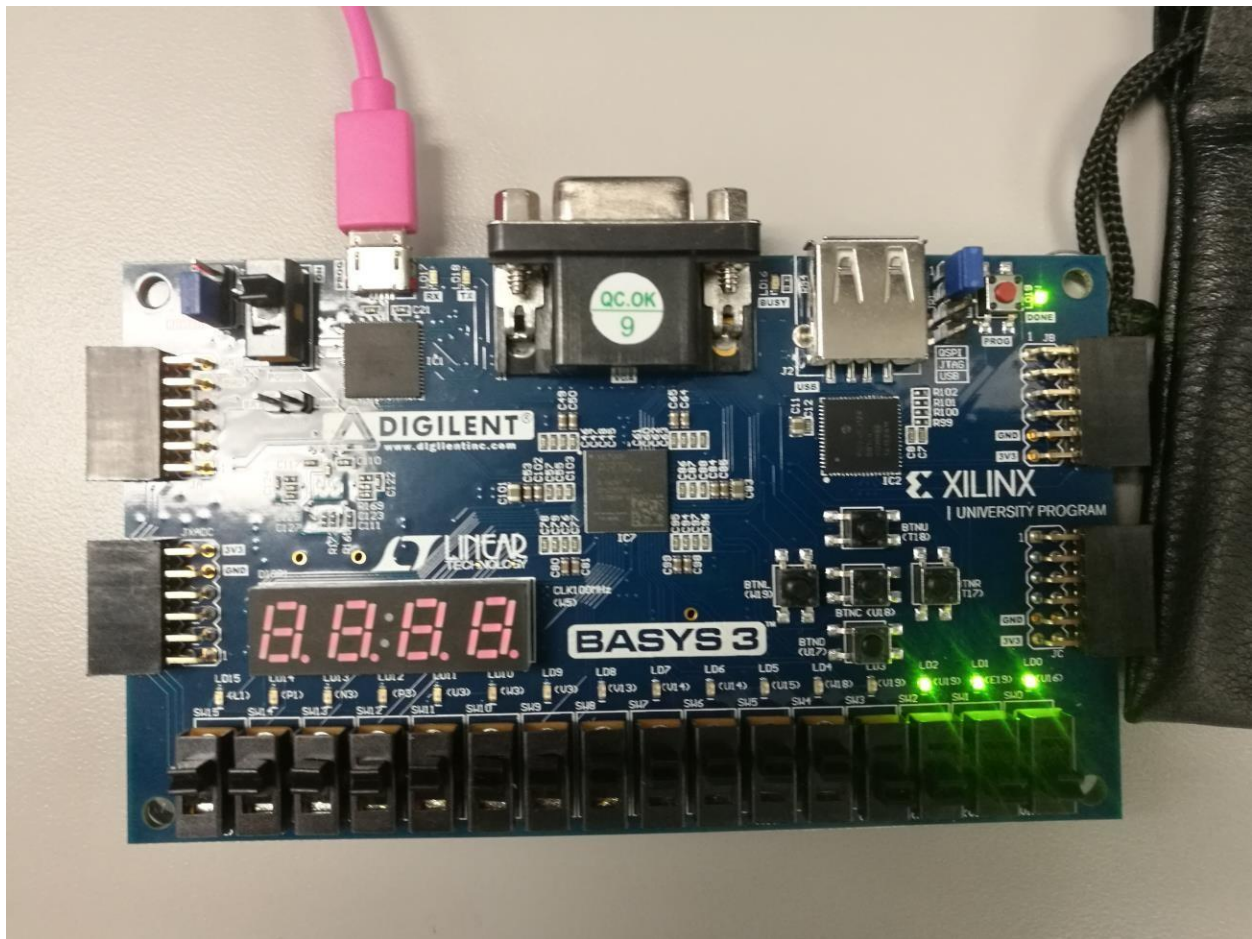CSE 3381 Digital Logic Design – Lab 3

Post Lab Exercises

1. No
2. No, because the number of available operations remains the same
3. Various combinations of inputs should be tested on every selectable gate
4. I had few difficulties, because I used a modular design instead of basing my design off of the most reduced function of the inputs.
5. An XOR gate
6. 1