

UM10945

NTAG I²C *plus* Explorer Kit Program and Debug Start-up

Rev. 1.0 — 16 February 2016
364310

User manual
COMPANY PUBLIC

Document information

Info	Content
Keywords	NTAG I ² C <i>plus</i> , Explorer Kit, Android, NFC tag, OM5569/NT322
Abstract	This User Manual aims at describing the procedure how to flash firmware to OM5569/NT322X Connected Tags Explorer Board and use Android application to check successful flashing.



Revision history

Rev	Date	Description
1.0	20160216	First version

Contact information

For more information, please visit: <http://www.nxp.com>

1. Object

NTAG I²C *plus* Explorer kit is an all-in-one demonstration and development resource to demonstrate the unique properties of the NTAG I²C *plus* tag chip. By including a full complement of hardware and software tools, users can not only investigate the capabilities of the chip through the various demonstrations, but also develop and test their own applications (additional LPC-Link2 debug probe¹).

This User Manual explains how to upload new firmware using LPCLink2 to Connected Tags Explorer Boards Rev 2.0 (and up) and older version Rev G.

Technical aspects related to the IC functioning are beyond the scope of this document. In order to get further technical details please consult the dedicated Datasheet “NTAG I²C *plus*, NFC Forum type 2 Tag compliant IC with I²C interface” (refer to [NTAGI2Cplus]).

2. Download and install latest LPCXpresso

Download latest version from:

<https://www.lpcware.com/lpcxpresso/downloads/windows>

Install it, then activate free edition – free registration is required.

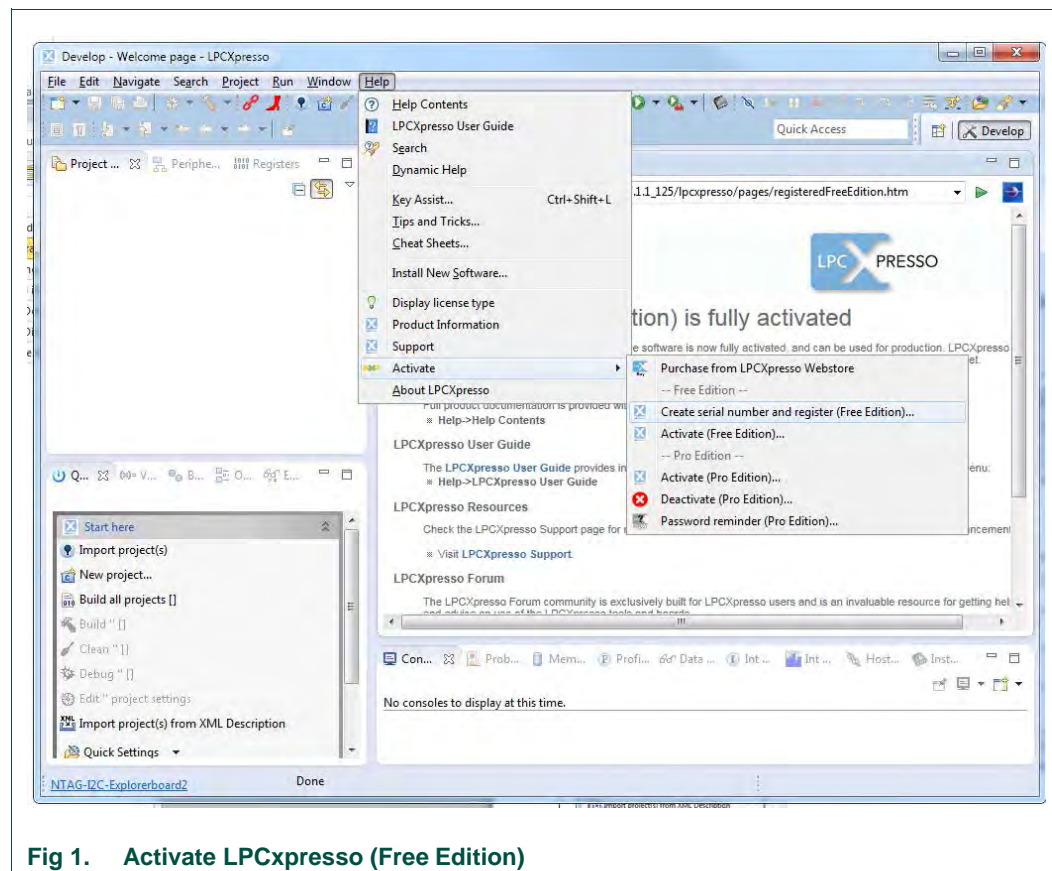


Fig 1. Activate LPCxpresso (Free Edition)

¹ <https://www.lpcware.com/lpclink2>

3. Importing source files

Download firmware source files from [NXP Explorer kit internet pages](#).

Run LPCXpresso software.

3.1 Create new workspace for new template.

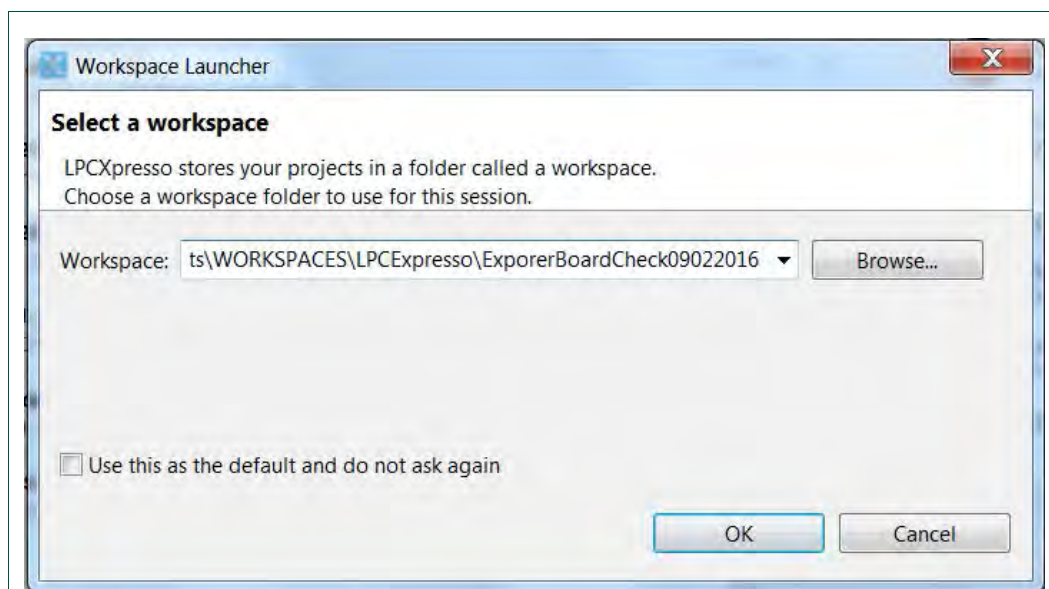


Fig 2. Create new workspace

3.2 Import project

Select “File/Import”, then “General/Existing Project into Workspace”.

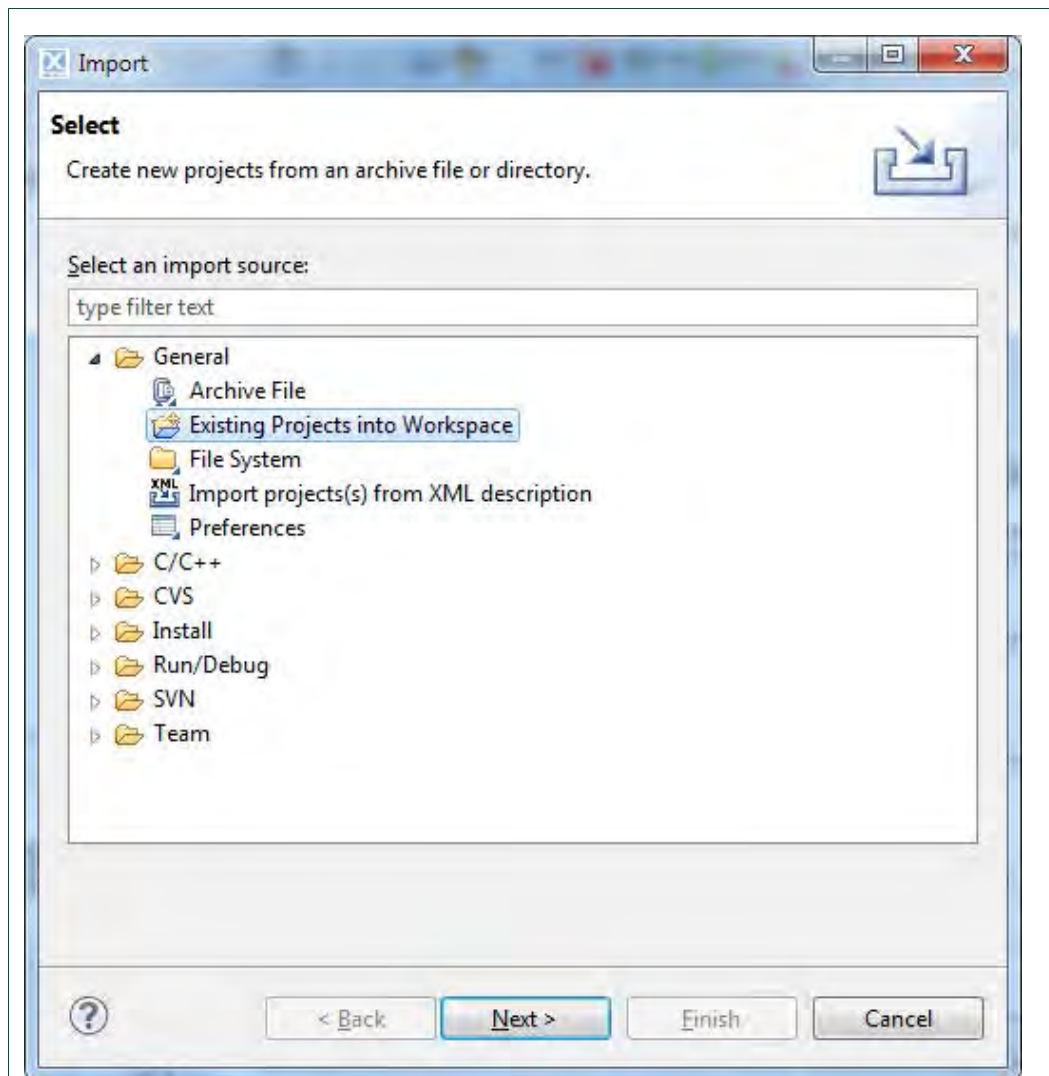


Fig 3. Insert Existing Project into Workspace

Click on “Browse” to the left of “Select Archive File” and select the Project .zip file.

Click “Finish”, six (6) projects are now imported.

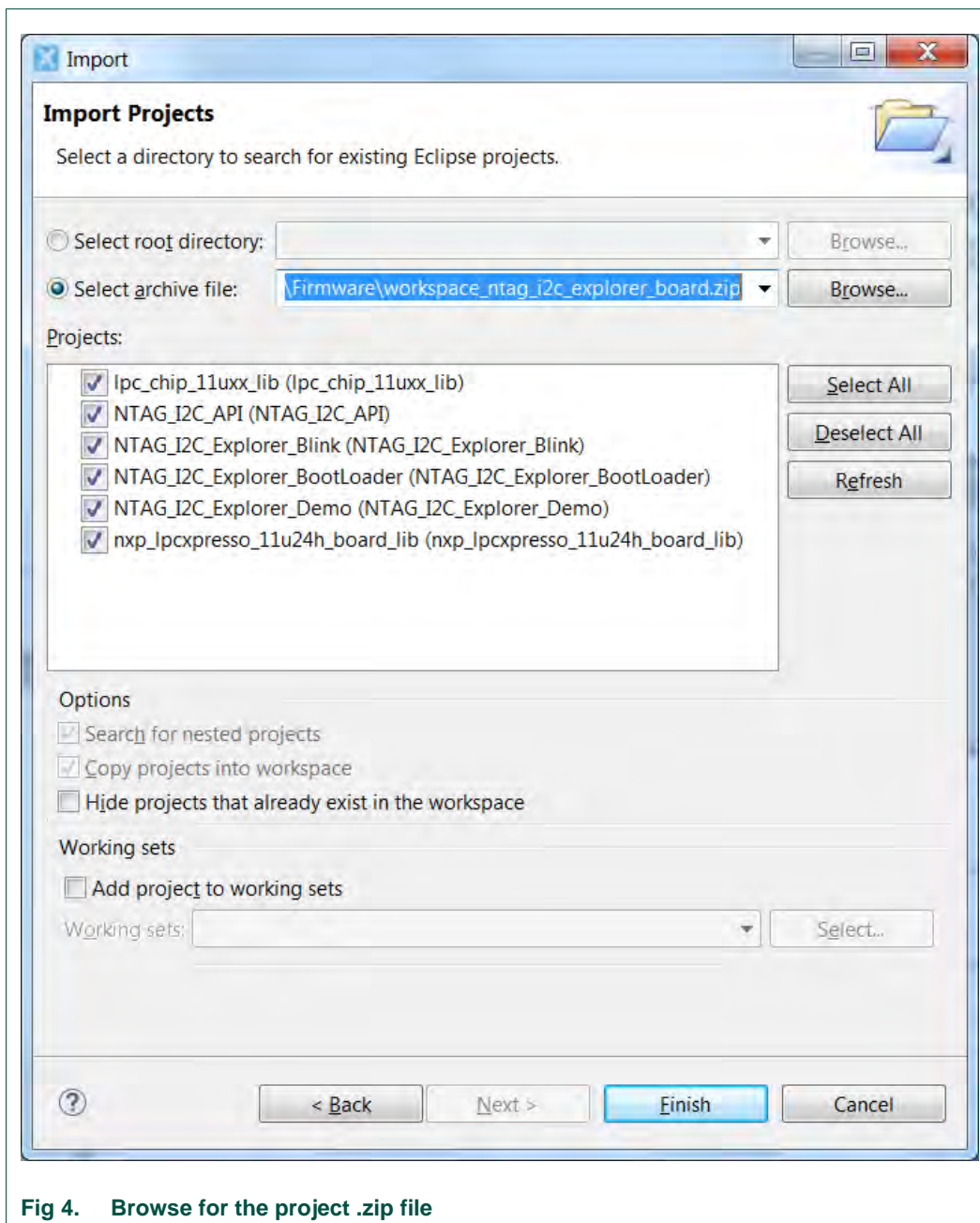


Fig 4. Browse for the project .zip file

4. Programming (flashing) Explorer Board

Connect the LPCLink2 with Explorer Board using 10-pin flat cable using J7 connector on LPCLink2 PCB, then connect LPCLink2 with USB to PC.

If you are using power supply from LPCLink2 you should have jumper on J2 connected.

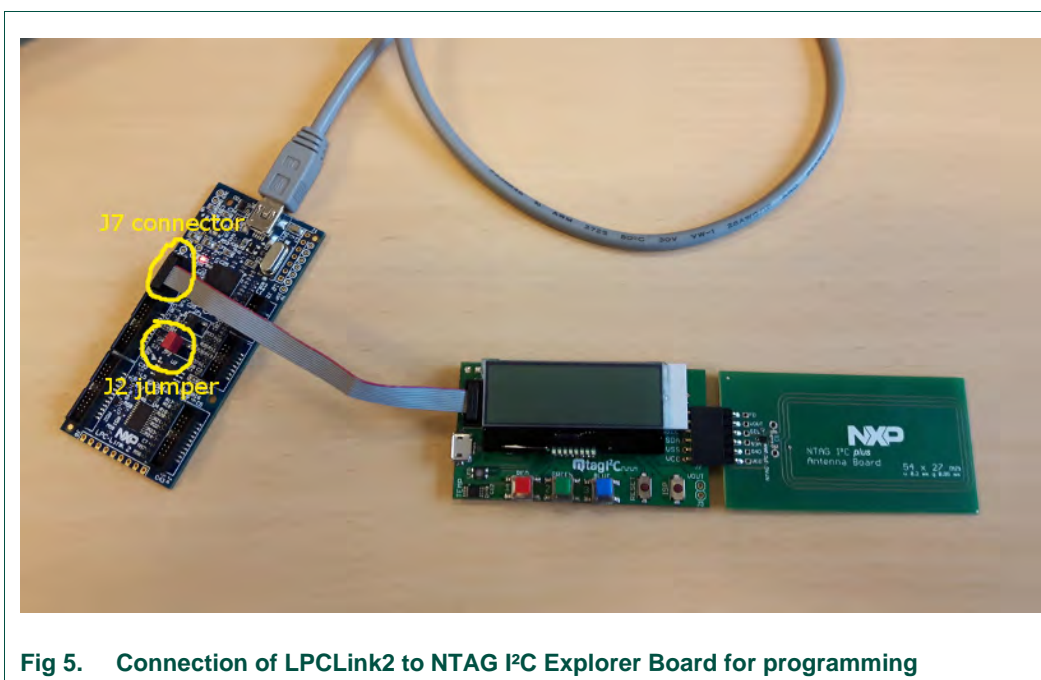


Fig 5. Connection of LPCLink2 to NTAG I²C Explorer Board for programming

4.1 Flash BOOTLOADER

All Demo Boards should be flashed with “NTAG_I2C_LED_Bootloader” firmware before using any Demo firmware and/or latest Android app.

Select the Project “NTAG_I2C_LED_Bootloader” and click on “Program Flash”:

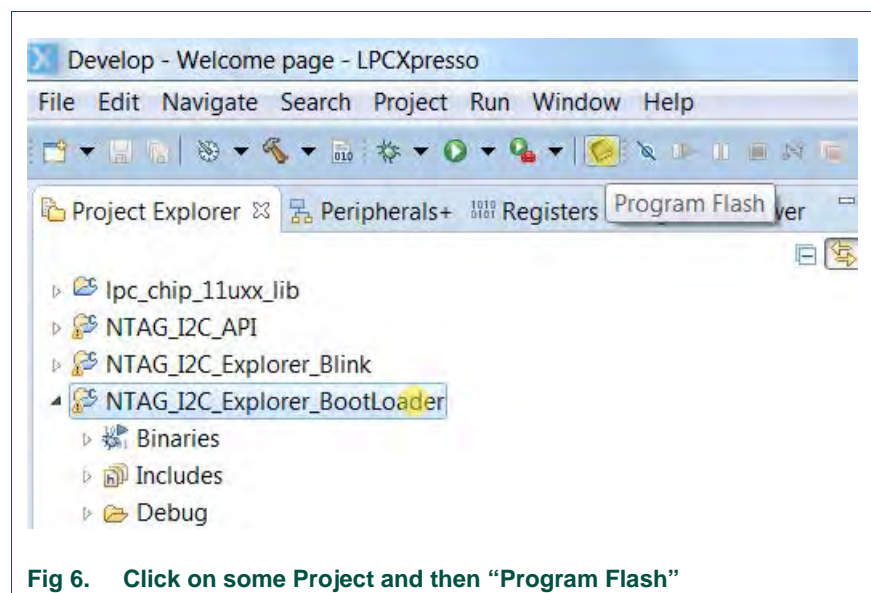


Fig 6. Click on some Project and then “Program Flash”

Check if the right target is selected LPC11xx (NXP LPC11U24/401). If not, please see [Troubleshoot](#) section.

Click “Browse...” and search for latest built binary file (.axf).

Press “OK”.

Flashing should start. In case of issues please see [Troubleshoot](#) section.

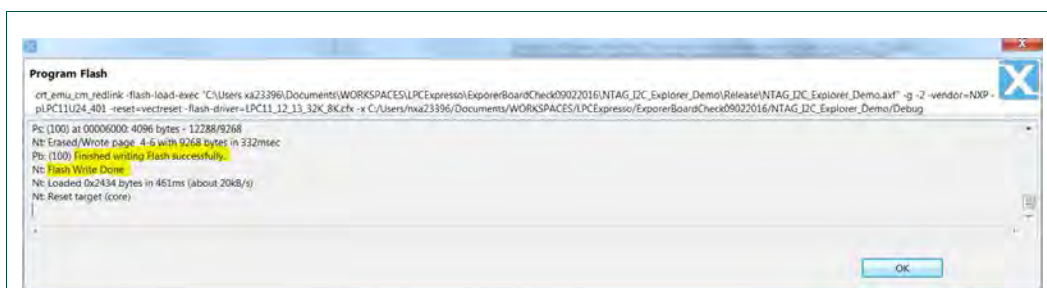


Fig 7. Successful flashing status window pops-up.

4.2 Flash DEMO APPLICATION

Flashing DEMO APP can be done in two ways:

1. Using LPCLink2 and LPCXpresso as described in [chapter 4.1](#)
2. Using Android app (NTAG I²C Demo 1.7.6). Detailed procedure is described on page 32 of [\[UM10966\]](#) - User manual NTAG I²C Demo app.

4.3 Test flashed firmware

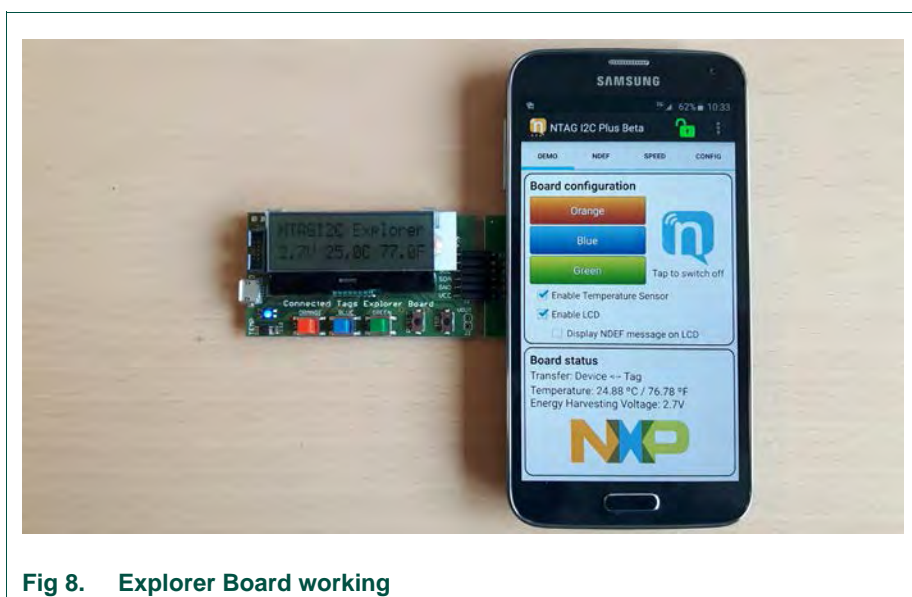


Fig 8. Explorer Board working

5. Troubleshoot

5.1 No compatible emulators found

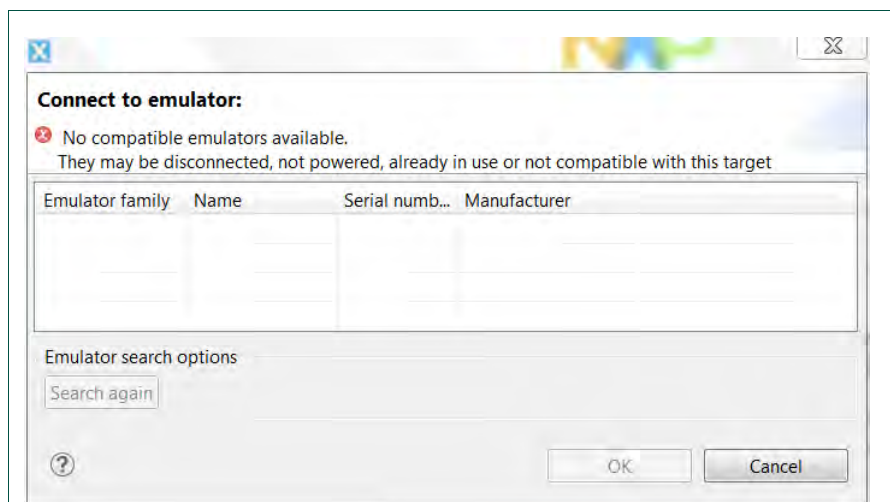


Fig 9. No compatible emulators found

→ Reconnect the LPCLink2 USB to PC

5.2 Could not connect to core

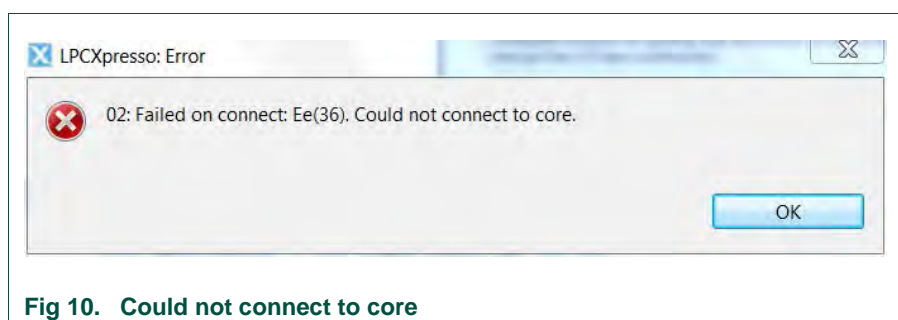


Fig 10. Could not connect to core

→ Check your flat cable connection

→ Restart redlinksrv.exe

5.3 Firmware size is too big

```
c:/nxp/lpcxpresso_7.9.0_455/lpcxpresso/tools/bin/./lib/gcc/
/arm-none-eabi/4.9.3/././././././arm-none-eabi/bin/ld.exe:
NTAG_I2C_Explorer_Demo.axf section `.text' will not fit in
region `MFlash32'
c:/nxp/lpcxpresso_7.9.0_455/lpcxpresso/tools/bin/./lib/gcc/
/arm-none-eabi/4.9.3/././././././arm-none-eabi/bin/ld.exe:
region `MFlash32' overflowed by 200 bytes
collect2.exe: error: ld returned 1 exit status
make: *** [NTAG_I2C_Explorer_Demo.axf] Error 1
```

Fig 11. Firmware size is too big.

- ➔ Right click Project you are trying to flash. Left click “Properties”. Under “C/C++ Build” – “Settings”, in the tab “Tool Settings” – “Optimization, set “Optimization Level” to “Optimize (-O0)”.

Re-build sources and flash again.

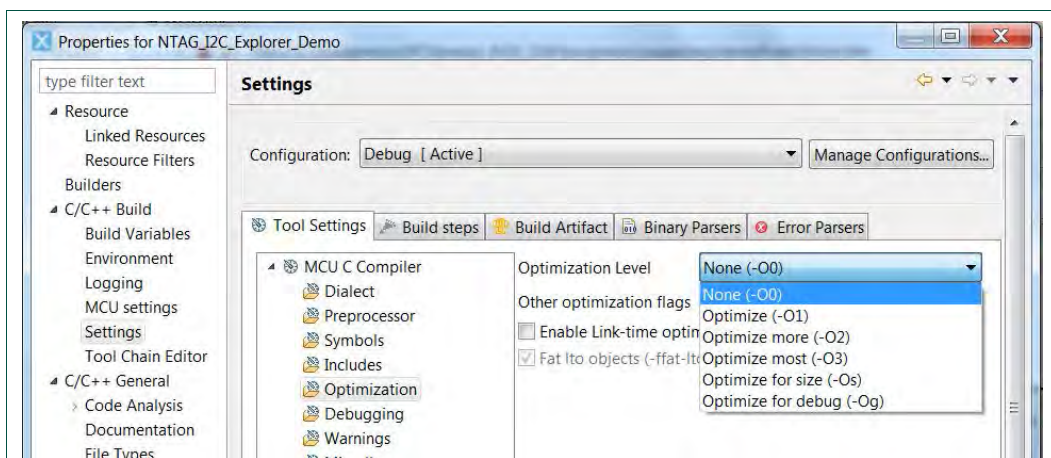


Fig 12. Set optimization level

6. References

[NTAGI2Cplus] NT3H2111/NT3H2211, NTAG I²C *plus*, NFC Forum Type 2 Tag compliant IC with I²C interface

http://www.nxp.com/documents/data_sheet/NT3H2111_2211.pdf

[UM10966] NTAG I²C Demo app

www.nxp.com/documents/user_manual/UM10966.pdf

7. Legal information

7.1 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

7.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of non-infringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out of the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

7.3 Licenses

Purchase of NXP ICs with NFC technology

Purchase of an NXP Semiconductors IC that complies with one of the Near Field Communication (NFC) standards ISO/IEC 18092 and ISO/IEC 21481 does not convey an implied license under any patent right infringed by implementation of any of those standards. Purchase of NXP Semiconductors IC does not include a license to any NXP patent (or other IP right) covering combinations of those products with other products, whether hardware or software.

7.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are property of their respective owners.

MIFARE — is a trademark of NXP Semiconductors N.V.

PC-bus — logo is a trademark of NXP Semiconductors N.V.

8. List of figures

Fig 1.	Activate LPCxpresso (Free Edition)	3
Fig 2.	Create new workspace	4
Fig 3.	Insert Existing Project into Workspace.....	5
Fig 4.	Browse for the project .zip file	6
Fig 5.	Connection of LPCLink2 to NTAG I ² C Explorer Board for programming	7
Fig 6.	Click on some Project and then "Program Flash"	7
Fig 7.	Successful flashing status window pops-up.	8
Fig 8.	Explorer Board working.....	8
Fig 9.	No compatible emulators found	9
Fig 10.	Could not connect to core	9
Fig 11.	Firmware size is too big.	10
Fig 12.	Set optimization level	10

9. Contents

1.	Object	3
2.	Download and install latest LPCXpresso	3
3.	Importing source files	4
3.1	Create new workspace for new template.	4
3.2	Import project	5
4.	Programming (flashing) Explorer Board	6
4.1	Flash BOOTLOADER	7
4.2	Flash DEMO APPLICATION	8
4.3	Test flashed firmware	8
5.	Troubleshoot	9
5.1	No compatible emulators found.....	9
5.2	Could not connect to core	9
5.3	Firmware size is too big	10
6.	References	11
7.	Legal information	12
7.1	Definitions	12
7.2	Disclaimers.....	12
7.3	Licenses.....	12
7.4	Trademarks.....	12
8.	List of figures.....	13
9.	Contents.....	14

Please be aware that important notices concerning this document and the product(s) described herein, have been included in the section 'Legal information'.
