## Aluno: João Dowsley

Sinais	sub rd, rt, rs	bne rs, rt, label	sw rt, x(rs)	or rd, rt, rs
RegDst	1	0	0	1
RegWrite	1	0	0	1
ALUSrc	0	0	1	0
Branch	0	1	0	0
MemRead	0	0	0	0
MemWrite	0	0	1	0
MemToReg	0	0	0	0
ALUControl	0110	0110	0010	0001

Sinais	li rt, x	sra rd, rt, rs	srl rd, rt, x	sll rd, rt, x
RegDst	0	1	1	1
RegWrite	1	1	1	1
ALUSrc	1	0	1	1
Branch	0	0	0	0
MemRead	0	0	0	0
MemWrite	0	0	0	0
MemToReg	0	0	0	0
ALUControl	1000	0101	0100	0011