

UNISONIC TECHNOLOGIES CO., LTD

LM393

LINEAR INTEGRATED CIRCUIT

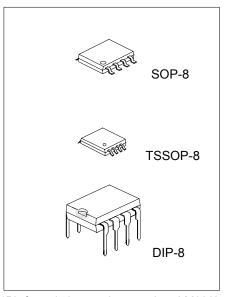
DUAL DIFFERENTIAL COMPARATOR

■ DESCRIPTION

The UTC LM393 consists of two independent voltage comparators, designed specifically to operate from a single power supply over a wide voltage range.

■ FFATURES

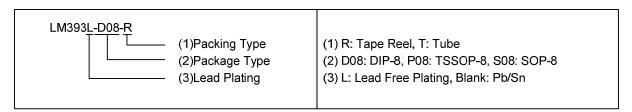
- * Single or dual supply operation.
- * Wide operating supply range $(V_{CC}=2V \sim 36V \text{ or } \pm 1 \sim \pm 18V)$
- * Input common-mode voltage includes ground.
- * Low supply current drain I_{CC}=0.8mA (Typical).
- * Low input bias current I_{BIAS}=25nA (Typical).
- * Output compatible with TTL, DTL, and CMOS logic system.



*Pb-free plating product number: LM393L

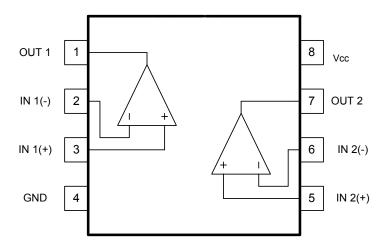
ORDERING INFORMATION

Ordering Number		Package	Dacking	
Normal	Lead Free Plating	Fackage	Packing	
LM393-D08-T	LM393L-D08-T	DIP-8	Tube	
LM393-P08-R	LM393L-P08-R	TSSOP-8	Tape Reel	
LM393-P08-T	LM393L-P08-T	TSSOP-8	Tube	
LM393-S08-R	LM393L-S08-R	SOP-8	Tape Reel	
LM393-S08-T	LM393L-S08-T	SOP-8	Tube	

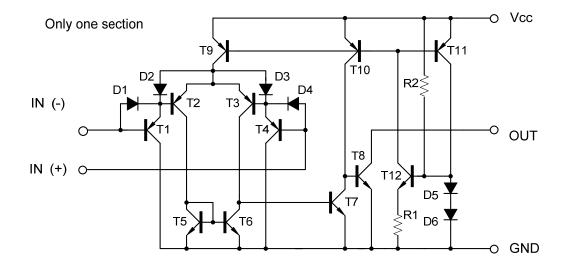


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■ PIN DESCRIPTION



■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	Vcc	±18 or 36	٧	
Differential Input Voltage	V _{I(DIFF)}	36	٧	
Input Voltage	V _{IN}	-0.3 ~ +36	٧	
Power Dissipation	P_D	570	mW	
Operating Temperature Range	T _{OPR}	0 ~ +70	ç	
Storage Temperature Range	T _{STG}	-65 ~ +150	ç	

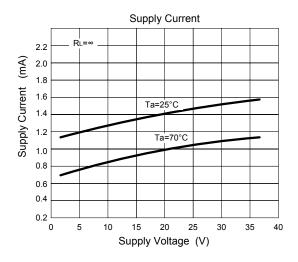
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

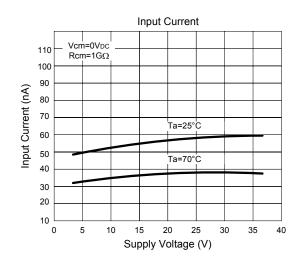
■ ELECTRICAL CHARACTERISTICS

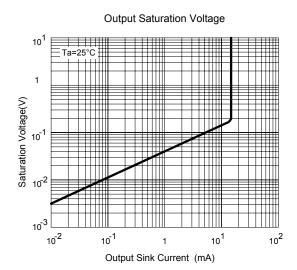
(V_{CC} =5.0V, Ta=25°C, All voltage referenced to GND unless otherwise specified)

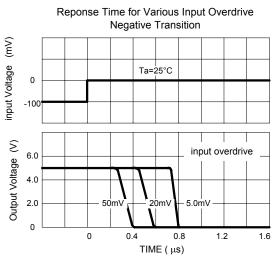
PARAMETER	SYMBOL	TEST COND	MIN	TYP	MAX	UNIT	
Input Offset Voltage	V _{I(OFF)}	$V_{CM}=0V$ to $V_{CC}-1.5V$ $V_{O(P)}=1.4V$, $R_S=0\Omega$		1.0	5.0	mV	
Output Saturation Voltage	V_{SAT}	$V_{I}(-)>1V, V_{I}(+)=0V,$		160	400	mV	
Input Common Mode Voltage	$V_{I(CM)}$	V _{CC} =30V		0		V _{CC} -1.5	V
Large Signal Voltage Gain	G _V	V_{CC} =15V, $R_L \ge 15K\Omega$		50	200		V/mV
Power Supply Current	Icc	R _L =∞, V _{CC} =30V			0.8	2.5	mA
		R _L =∞			0.6	1.0	mA
Input Offset Current	I _{I(OFF)}				5	50	nA
Input Bias Current	I _{I(BIAS)}				65	250	nA
Output Sink Current	I _{O(SINK)}	V _I (-)>1V, V _I (+)=0V, Vo(p)<1.5V		6	18		mA
Output Leakage Current	I _{O(LEAK)}	V _I (+)=1V, V _I (-)=0	Vo(p)= 5V		0.1		nA
			Vo(p)=30V			1.0	μA
Large Signal Response Time	t _R	V_{IN} =TTL logic wing V_{REF} =1.4V, V_{RL} =5V, R_L =5.1k Ω			350		ns
Response Time	t _R	V_{RL} =5 V , R_L =5.1 $k\Omega$			1400		ns

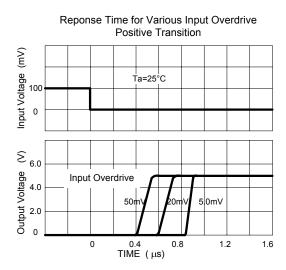
■ TYPICAL CHARACTERISTICS



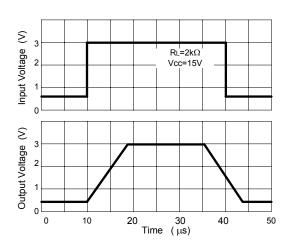


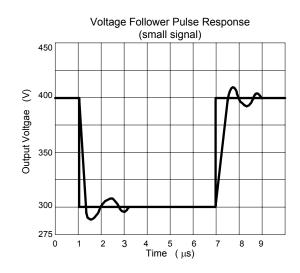


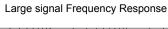


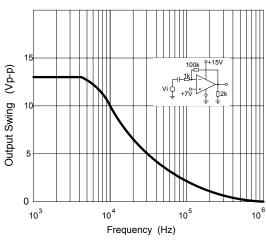


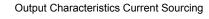
■ TYPICAL CHARACTERISTICS(Cont.)

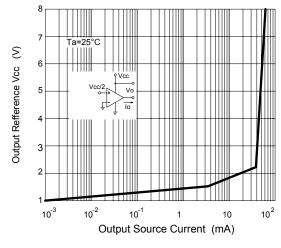




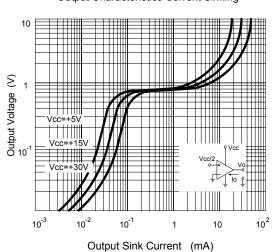


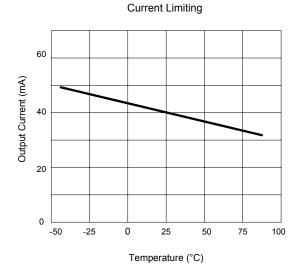






Output Characteristics Current Sinking





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