



The diagram illustrates the TARGET INTERFACE PCB layout. It features five 74LVC2T45DC buffers (U7-U11) connected to various data and control lines. The layout includes status LEDs (D1), a power flag indicator (D2), and a reset circuit (U12A, U12B). The board is populated with various passive components like capacitors (C24-C37) and resistors (R9, R10, RN7-RN10).

**Component List:**

- U7, U8, U9, U10, U11: 74LVC2T45DC
- U12A, U12B: MCP6002-xMS
- D1: LED
- D2: LED
- C24, C25, C26, C27, C28, C29, C30, C31, C32, C33, C34, C35, C36, C37: 100nF
- R9: 5.1k
- R10: 1k
- RN7, RN8, RN9, RN10: 470, 100, 220k

**Pin Connections:**

- U7: VCCA, VCCB, 1A, 1B, 2A, 2B, DIR, GND
- U8: VCCA, VCCB, 1A, 1B, 2A, 2B, DIR, GND
- U9: VCCA, VCCB, 1A, 1B, 2A, 2B, DIR, GND
- U10: VCCA, VCCB, 1A, 1B, 2A, 2B, DIR, GND
- U11: VCCA, VCCB, 1A, 1B, 2A, 2B, DIR, GND
- U12A: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100
- U12B: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58, 59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76, 77, 78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96, 97, 98, 99, 100

**Legend:**

- GREEN: +3V3
- RED: -3V3
- WHITE: GND

**Revision History:**

Rev	Description
0.1	Initial release

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