

Document Revision History

Rev A : Initial Release

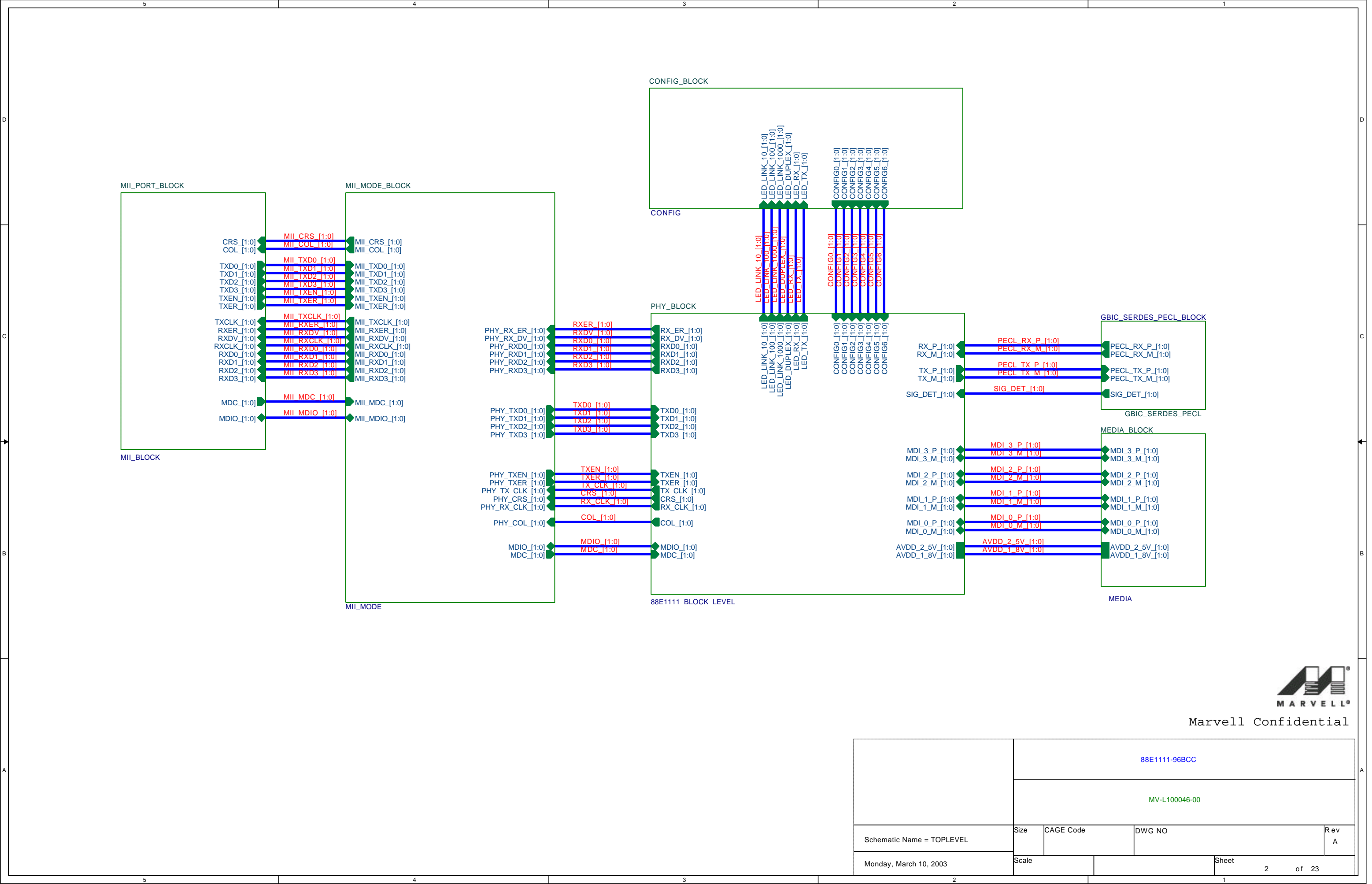
Overview

This is the evaluation board designed to validate the functionalities of 88E1111-96BCC Integrated 10/100/1000 Transceiver. There are 2 88E1111-96BCC placed back-to-back for the MAC interface i.e. GMII, RGMII, etc.



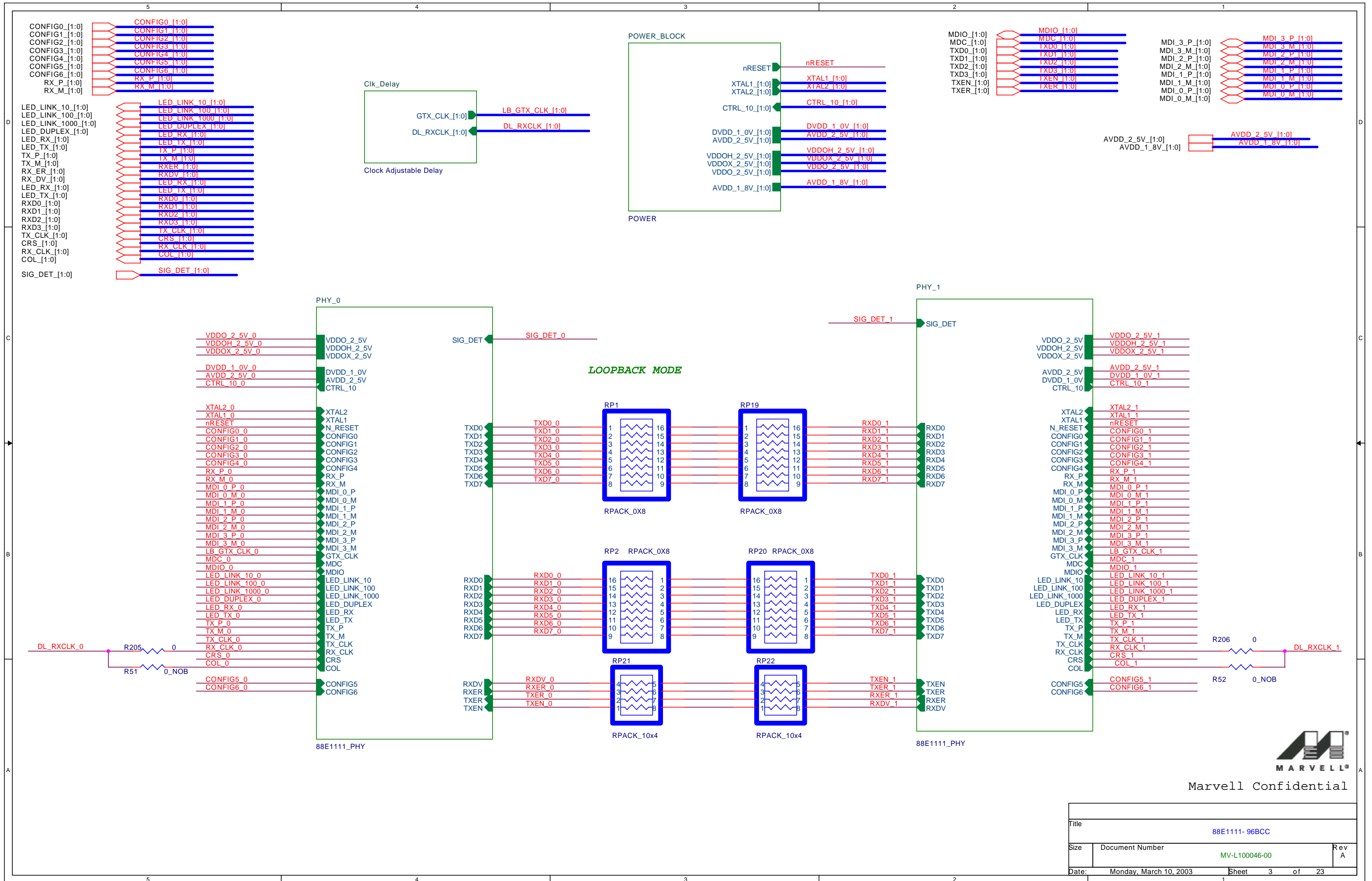
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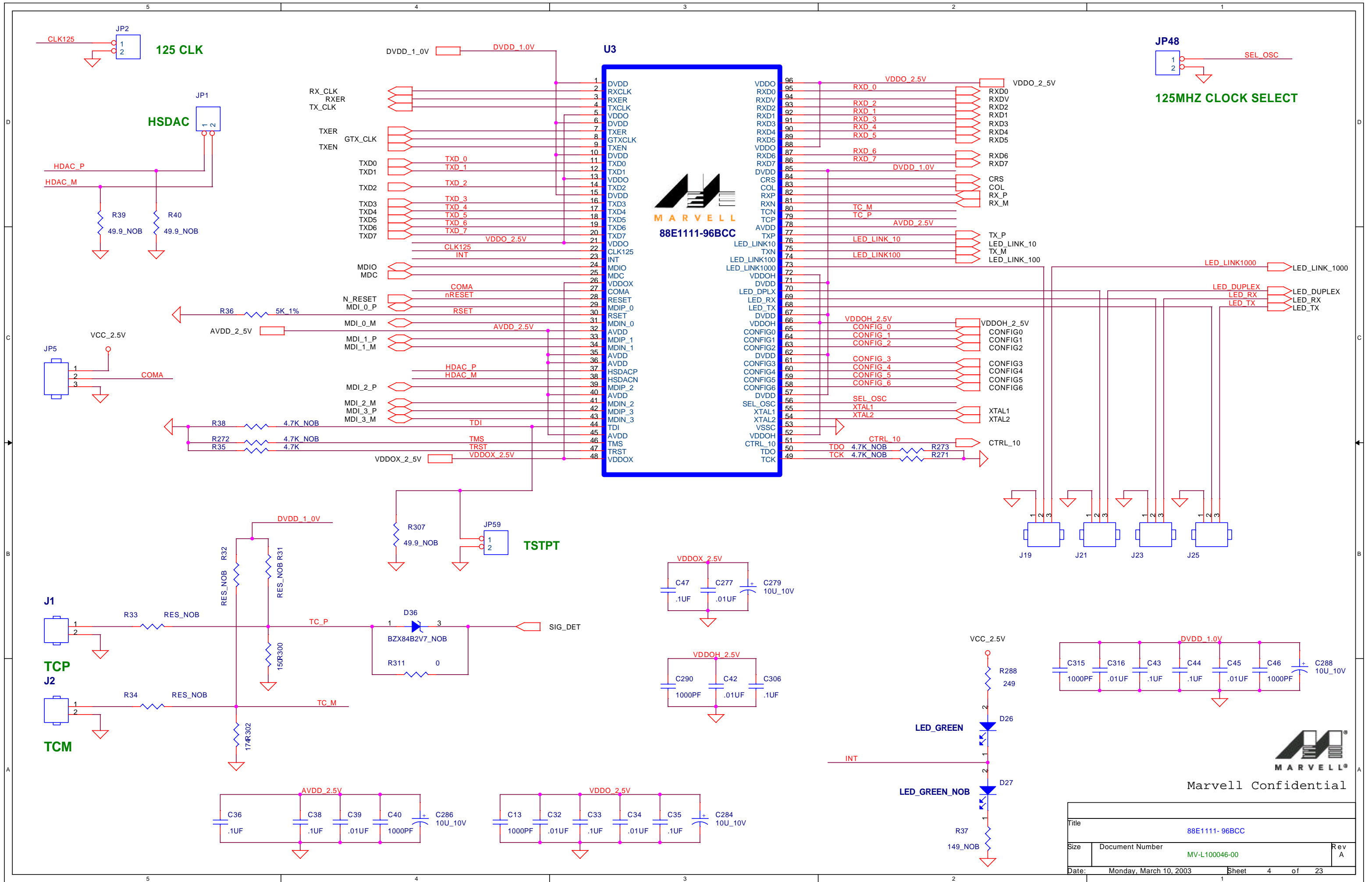
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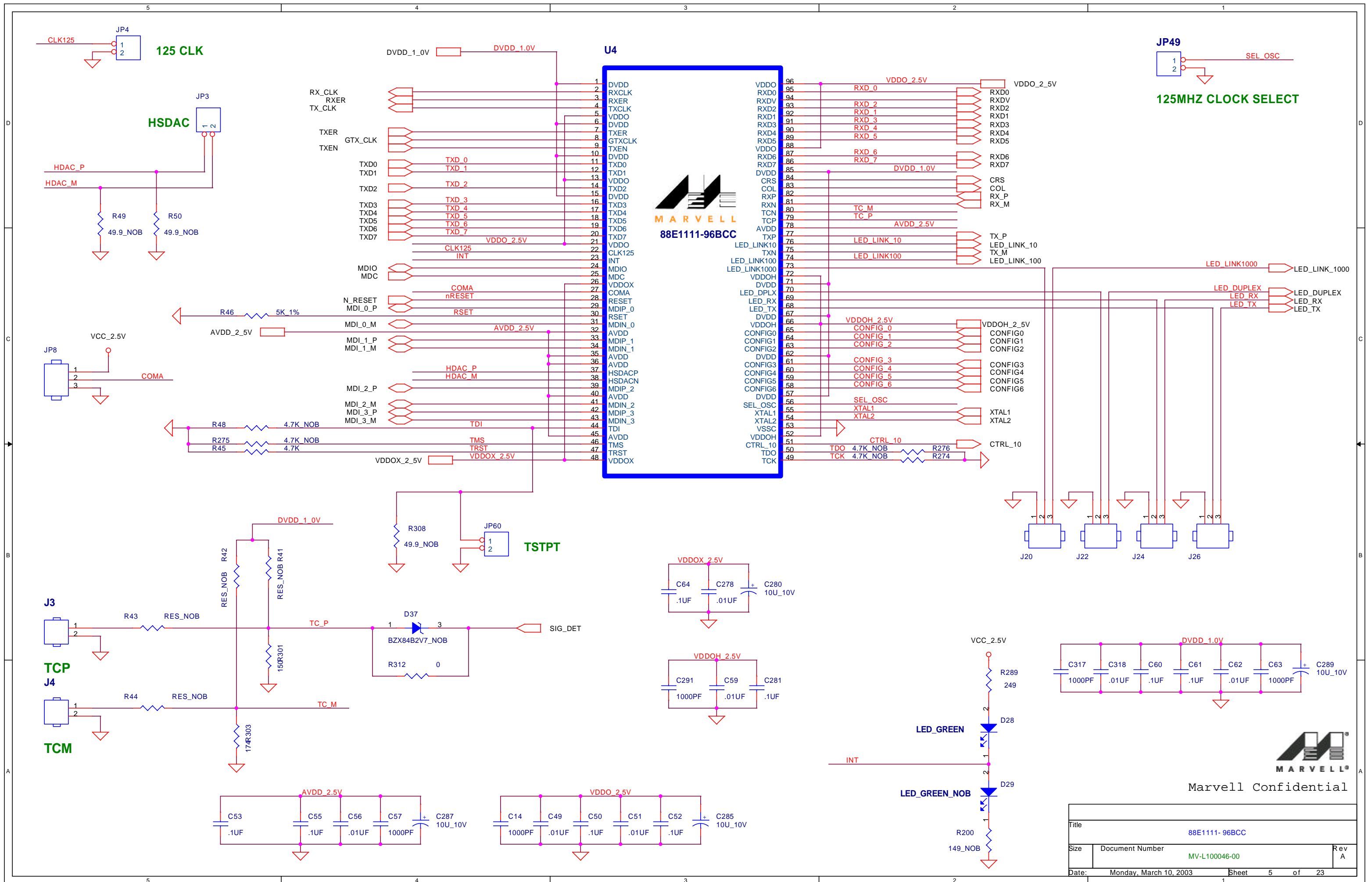
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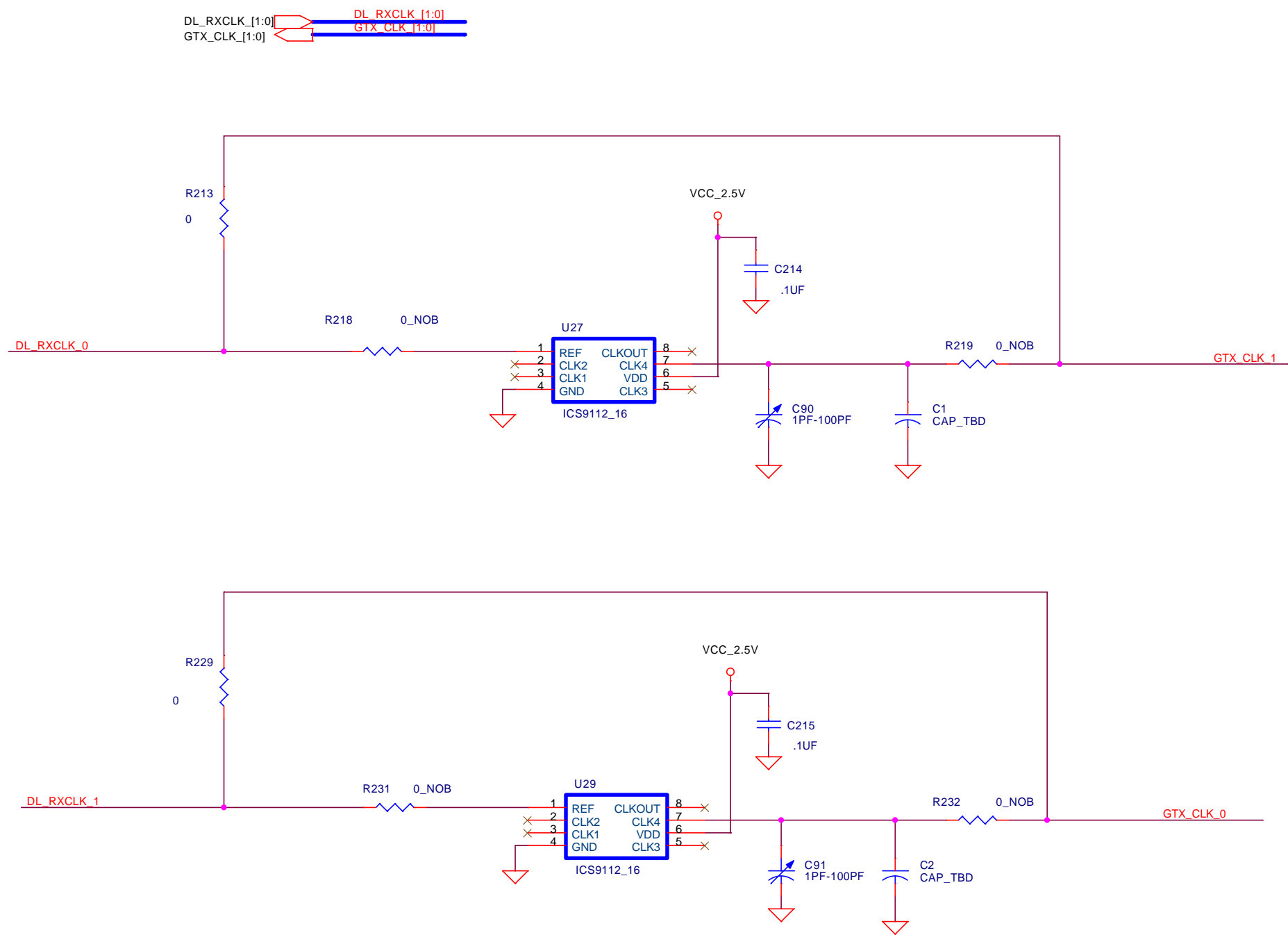
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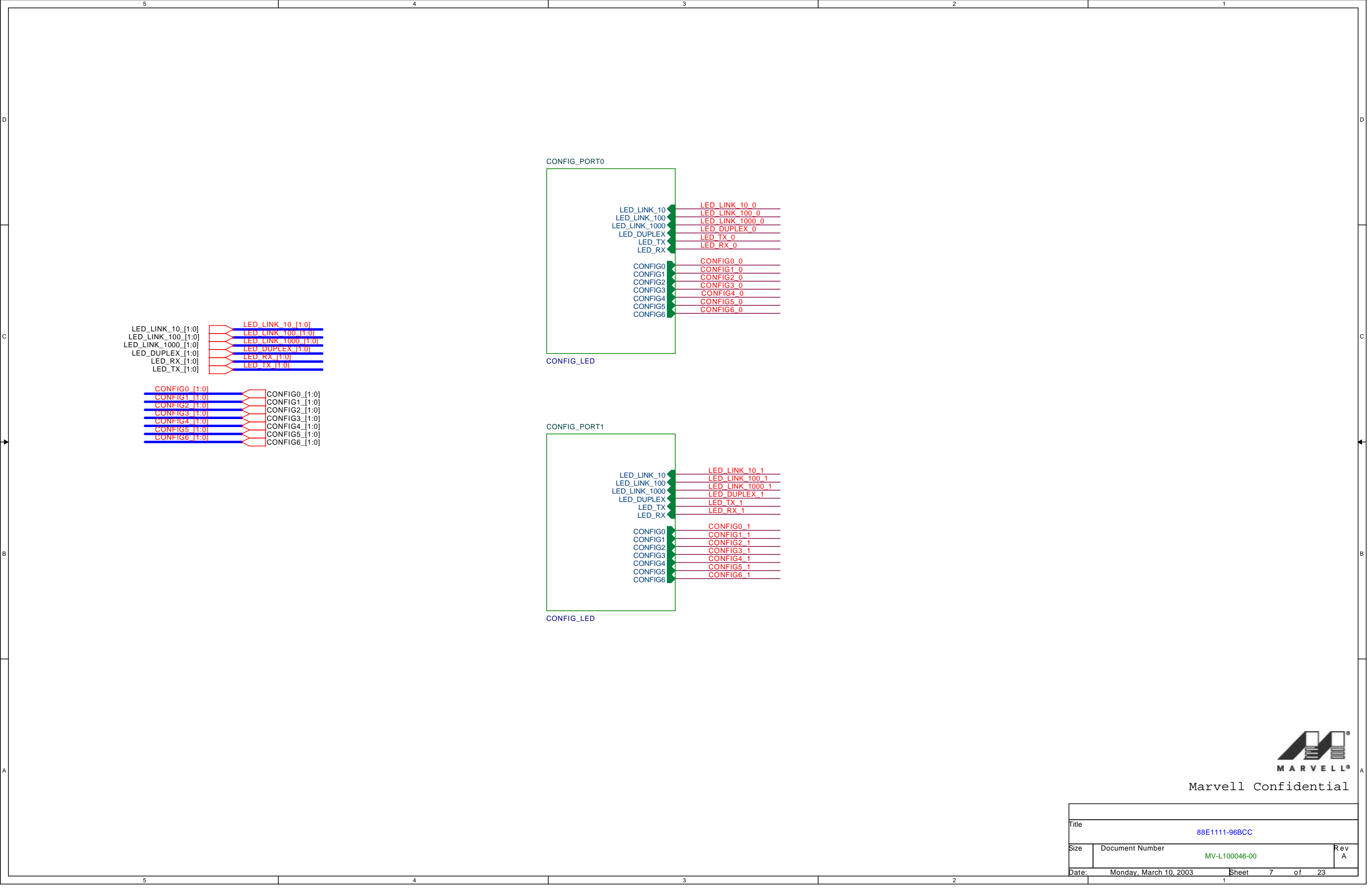
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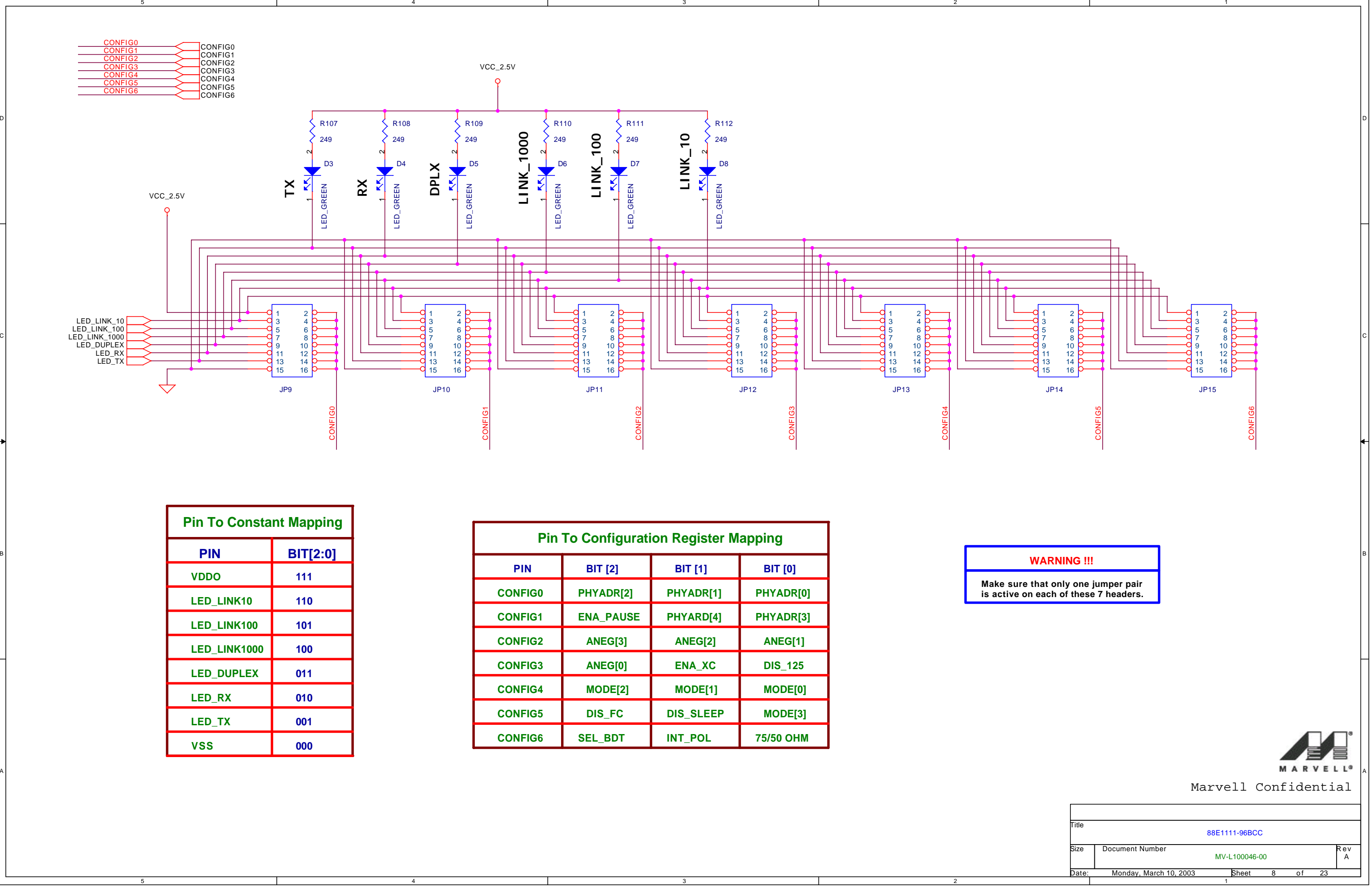
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Pin To Constant Mapping	
PIN	BIT[2:0]
VDDO	111
LED_LINK10	110
LED_LINK100	101
LED_LINK1000	100
LED_DUPLEX	011
LED_RX	010
LED_TX	001
VSS	000

Pin To Configuration Register Mapping			
PIN	BIT [2]	BIT [1]	BIT [0]
CONFIG0	PHYADR[2]	PHYADR[1]	PHYADR[0]
CONFIG1	ENA_PAUSE	PHYARD[4]	PHYADR[3]
CONFIG2	ANEG[3]	ANEG[2]	ANEG[1]
CONFIG3	ANEG[0]	ENA_XC	DIS_125
CONFIG4	MODE[2]	MODE[1]	MODE[0]
CONFIG5	DIS_FC	DIS_SLEEP	MODE[3]
CONFIG6	SEL_BDT	INT_POL	75/50 OHM

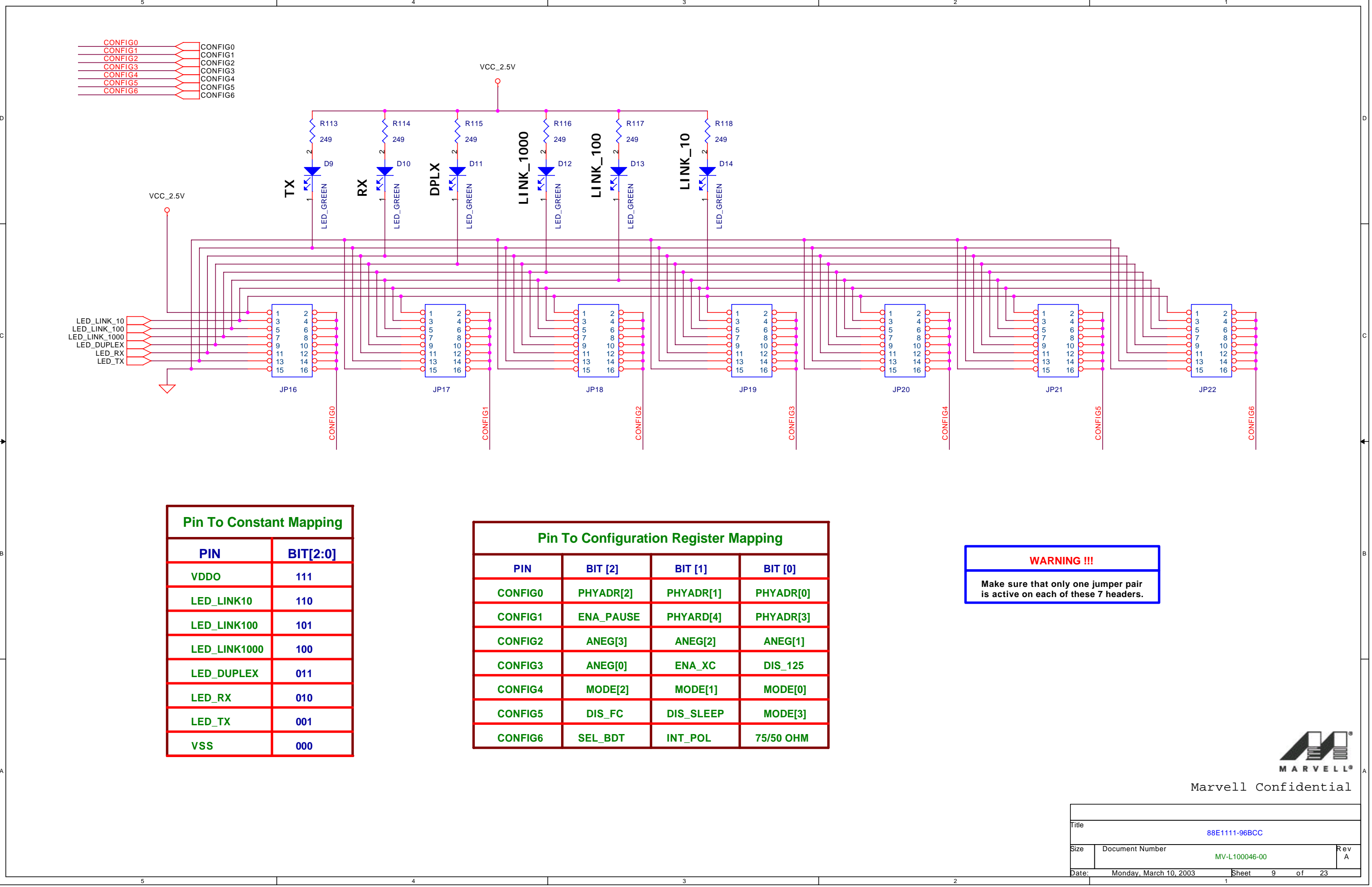
WARNING !!!

Make sure that only one jumper pair is active on each of these 7 headers.



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Pin To Constant Mapping	
PIN	BIT[2:0]
VDDO	111
LED_LINK10	110
LED_LINK100	101
LED_LINK1000	100
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Pin To Configuration Register Mapping			
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CONFIG0	PHYADR[2]	PHYADR[1]	PHYADR[0]
CONFIG1	ENA_PAUSE	PHYARD[4]	PHYADR[3]
CONFIG2	ANEG[3]	ANEG[2]	ANEG[1]
CONFIG3	ANEG[0]	ENA_XC	DIS_125
CONFIG4	MODE[2]	MODE[1]	MODE[0]
CONFIG5	DIS_FC	DIS_SLEEP	MODE[3]
CONFIG6	SEL_BDT	INT_POL	75/50 OHM

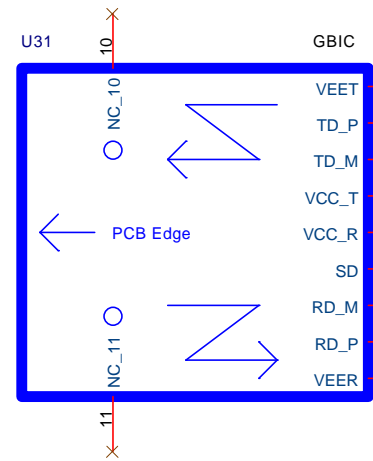
WARNING !!!

Make sure that only one jumper pair is active on each of these 7 headers.

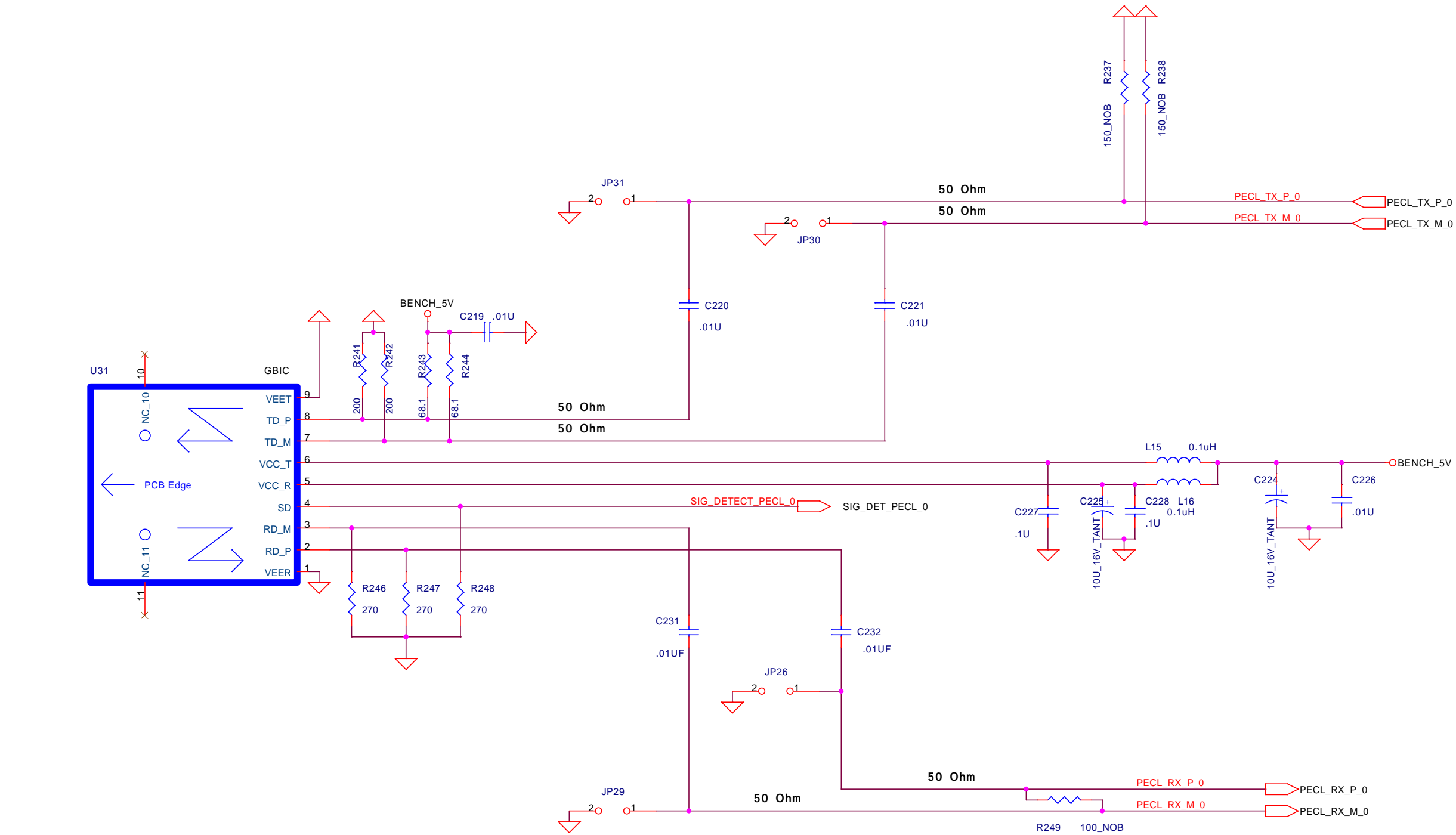


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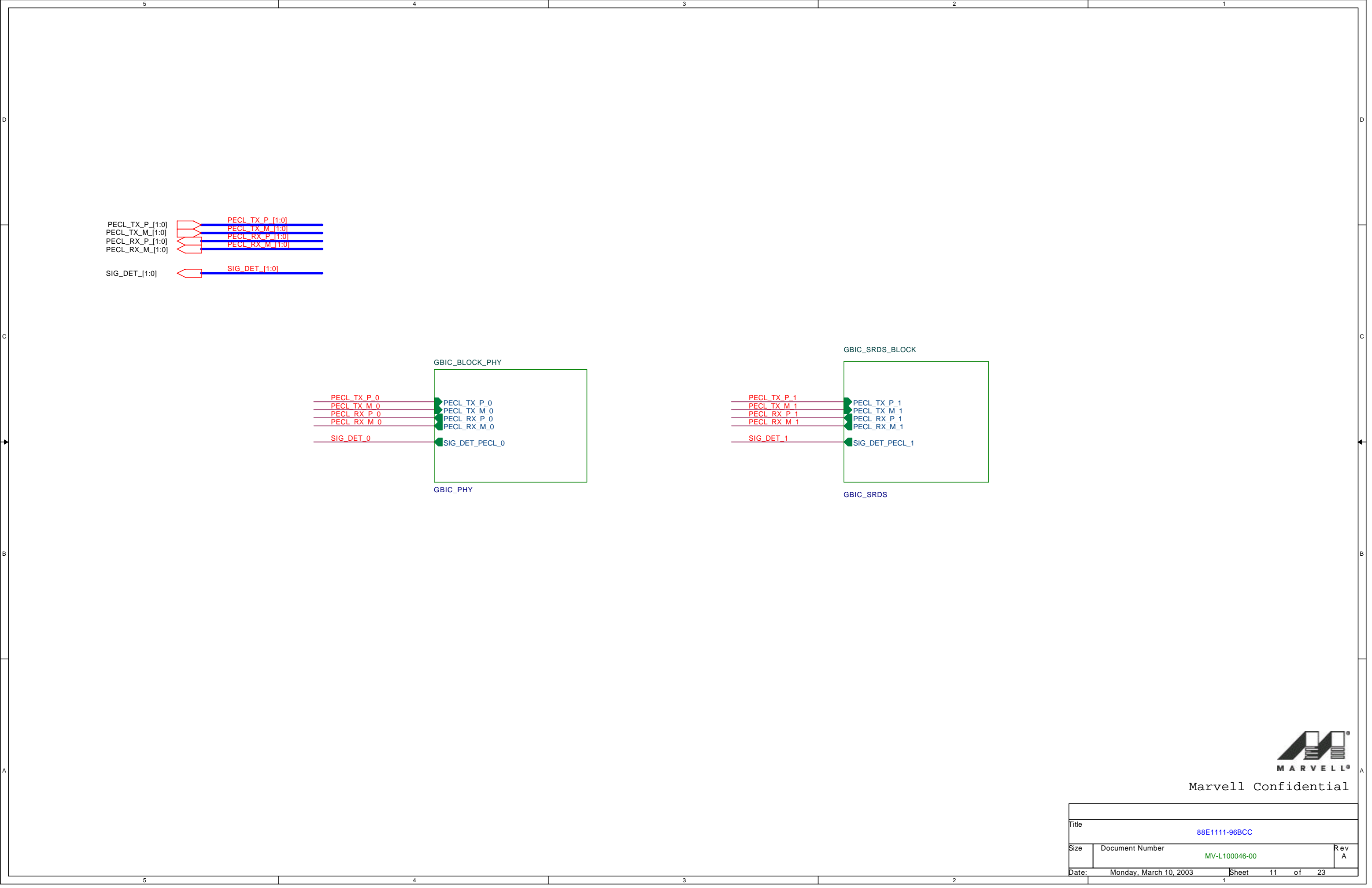


Diff PECL to TTL converter



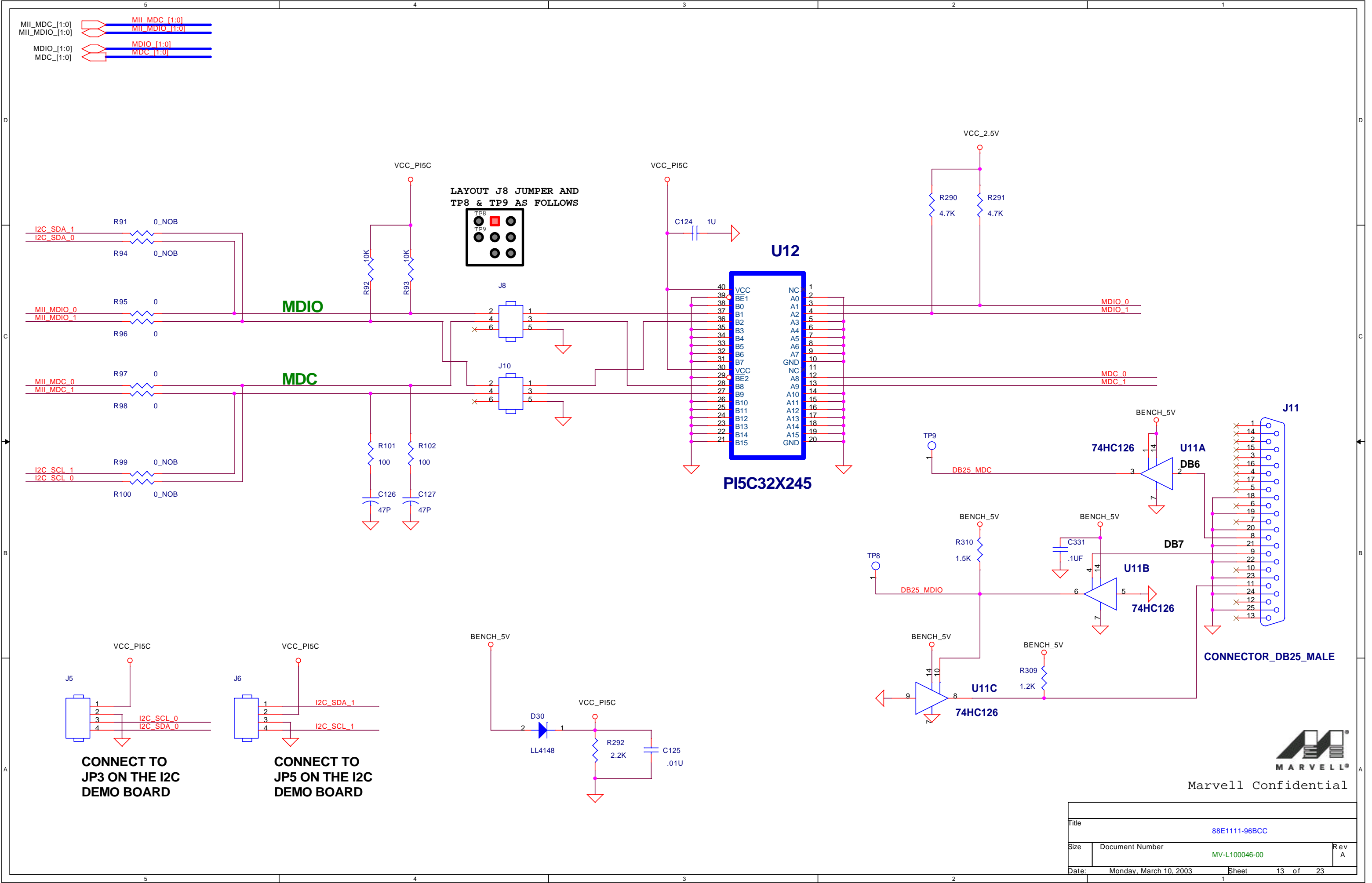
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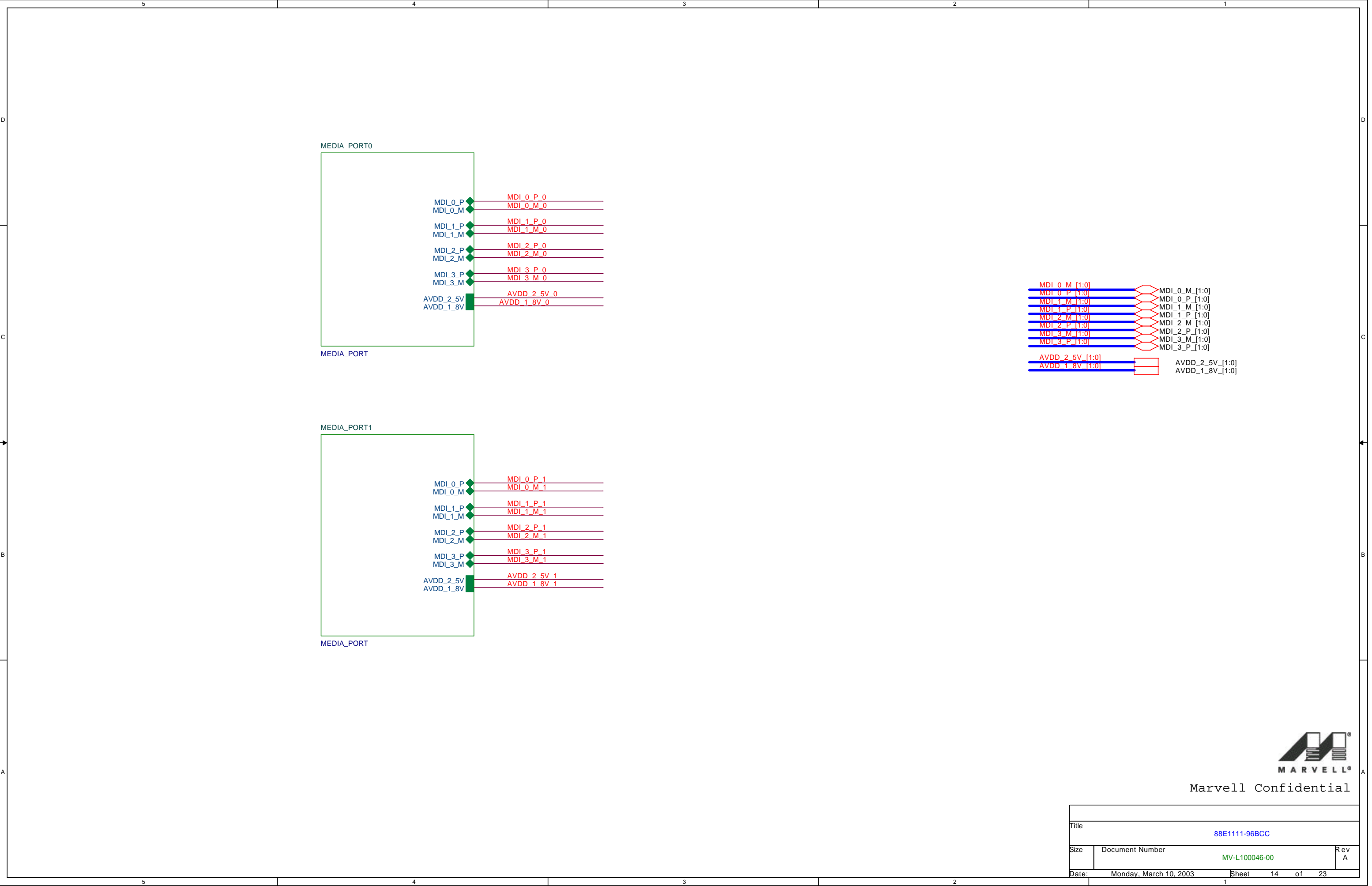
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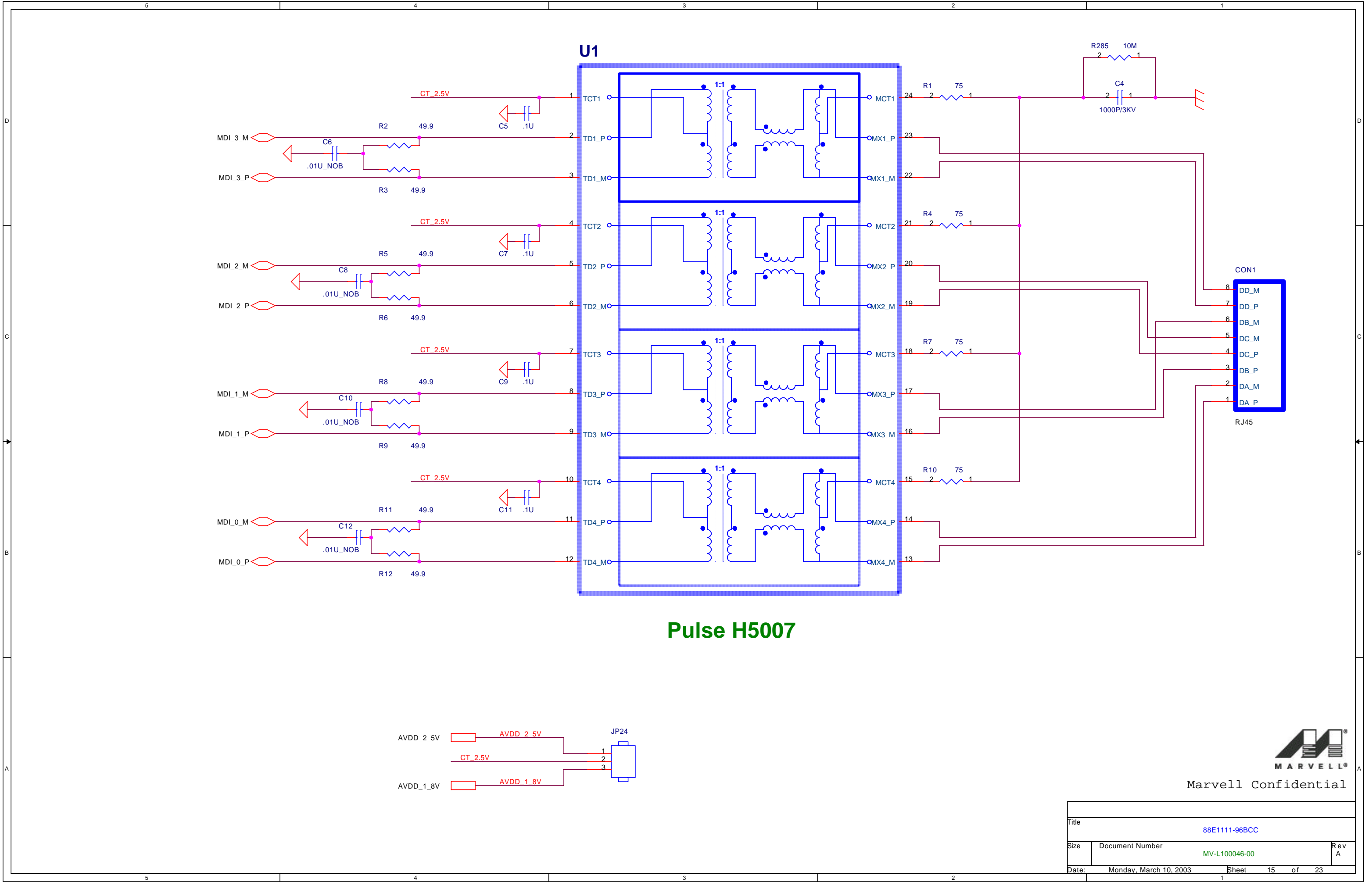
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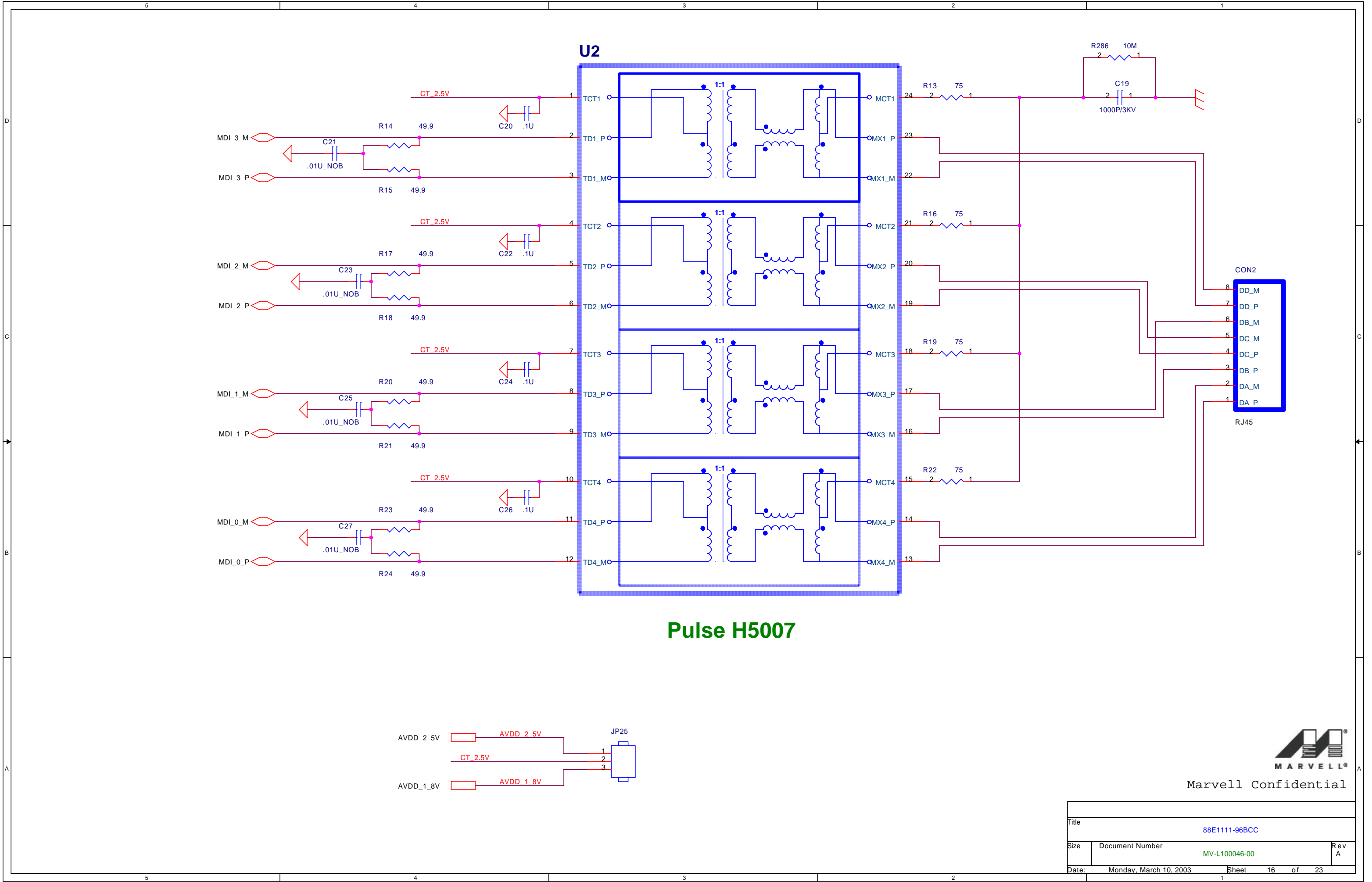
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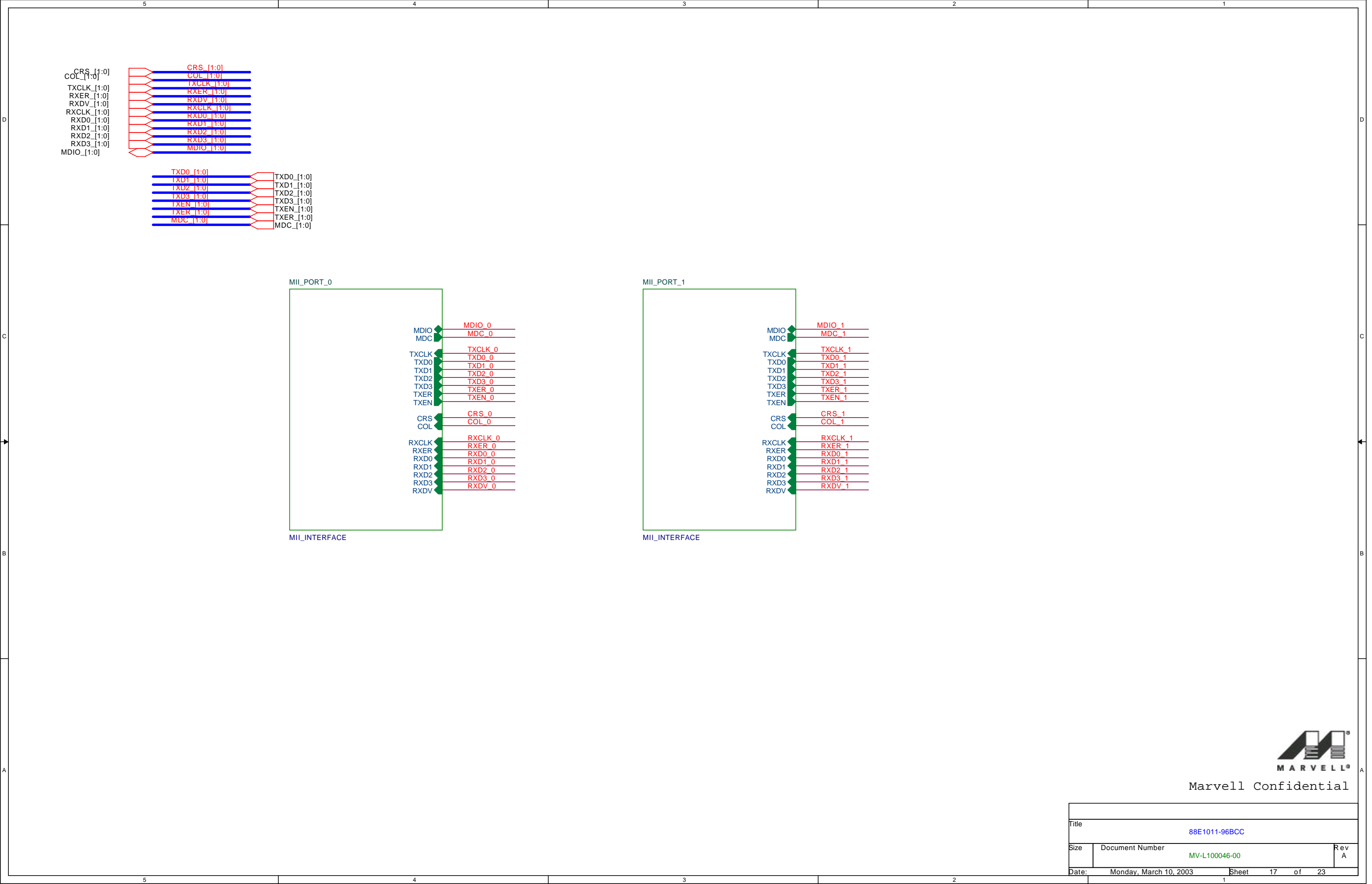
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88E1011-96BCC				
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This header to be placed close to the MII connector in straight line from the FPGA. The pads for this connectors should fall on the traces from FPGA. DO NOT USE SEPARATE TRACES

TEST_MII_PORT

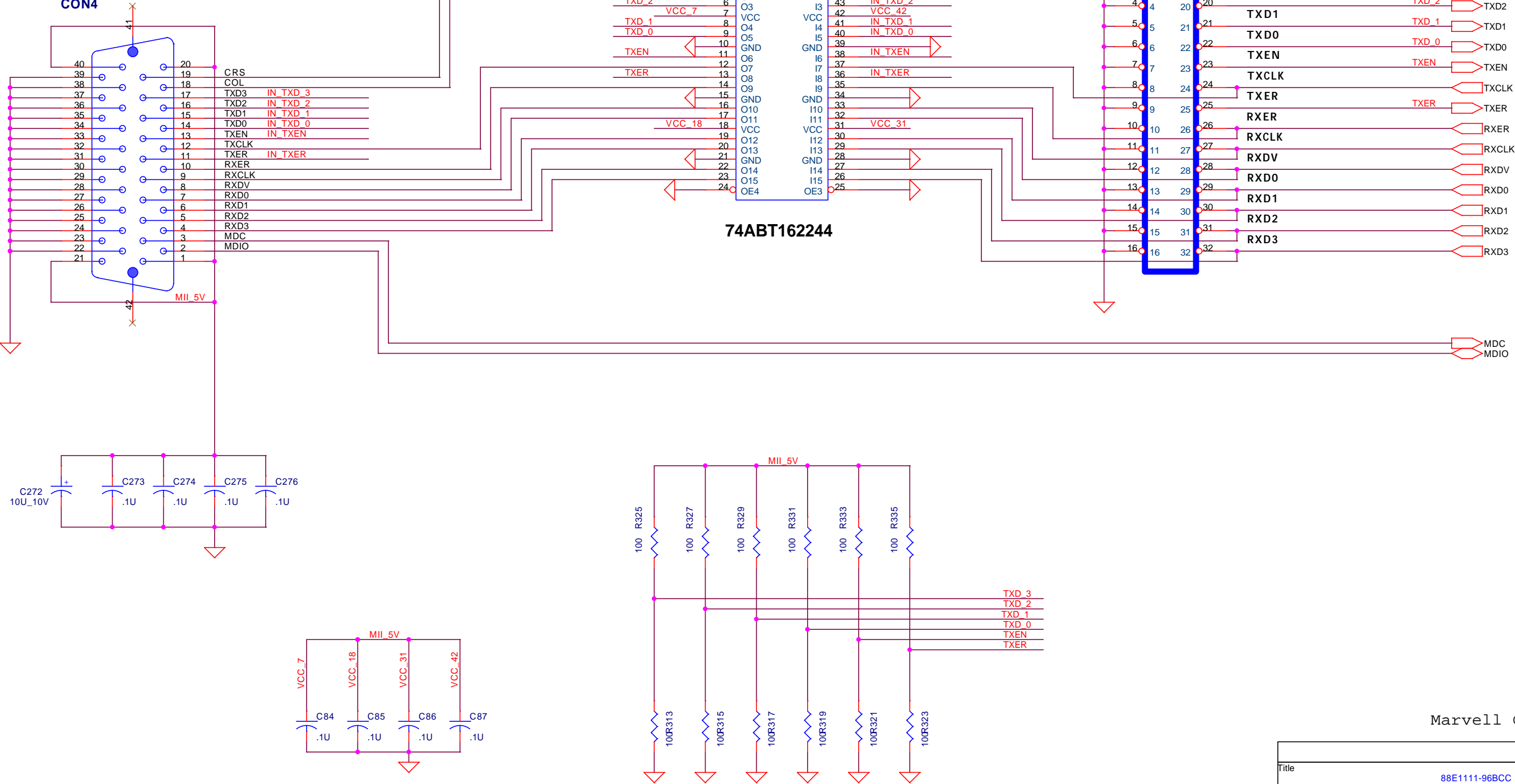
JP7

U8

74ABT162244

AMP_787170-4

CON4



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This header to be placed close to the MII connector in straight line from the FPGA. The pads for this connectors should fall on the traces from FPGA. DO NOT USE SEPARATE TRACES

TEST_MII_PORT

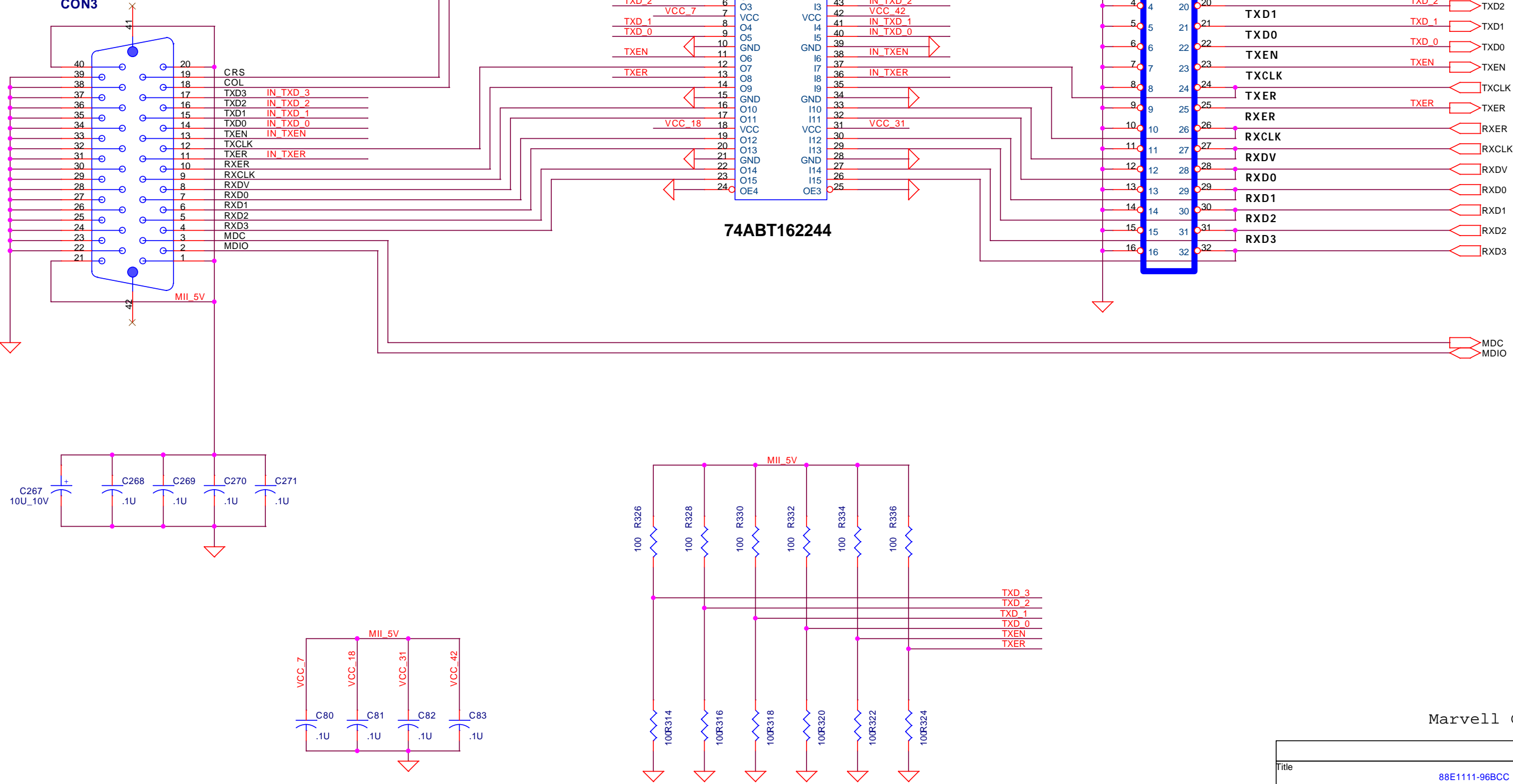
JP6

U7

74ABT162244

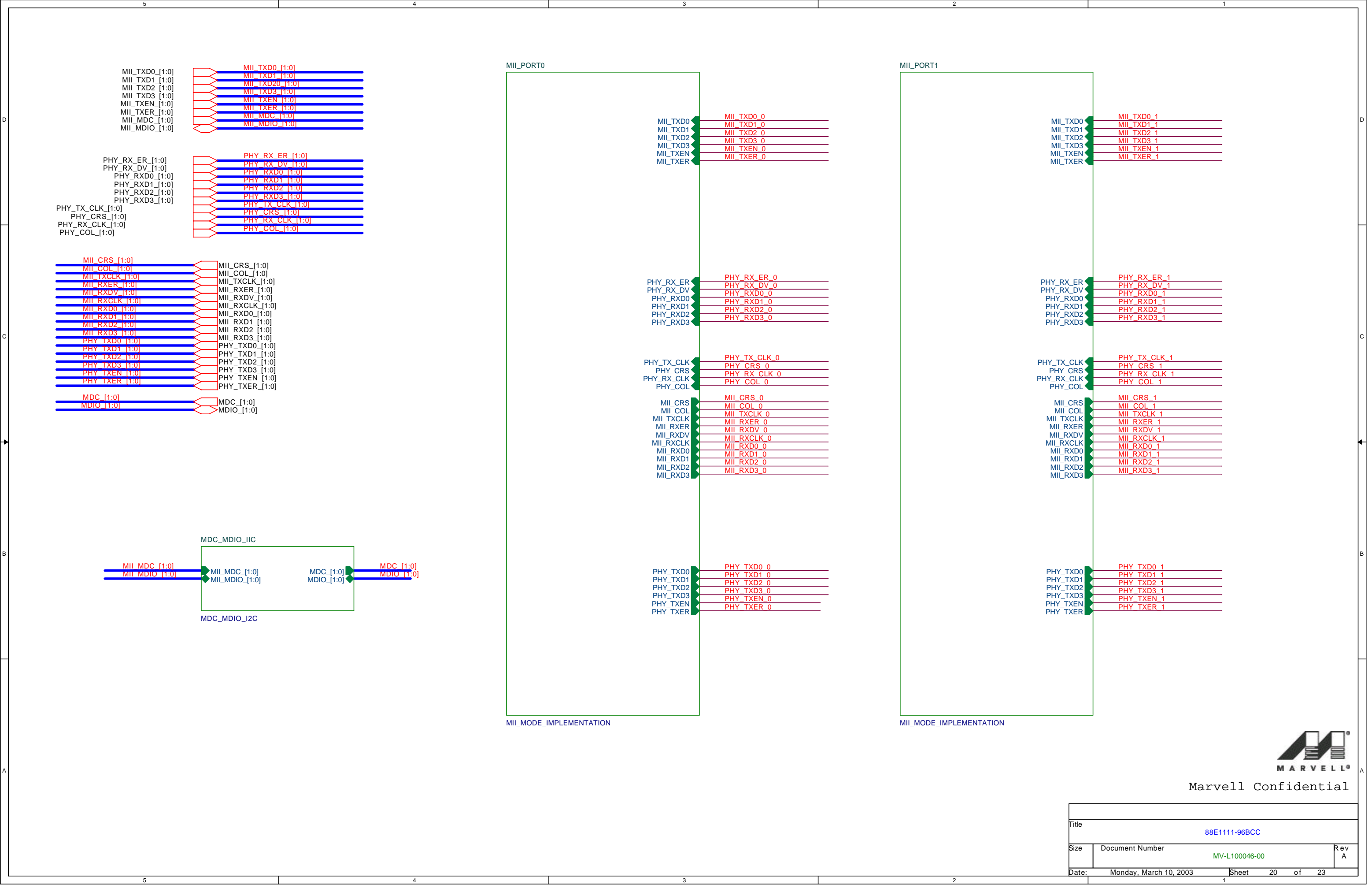
AMP_787170-4

CON3



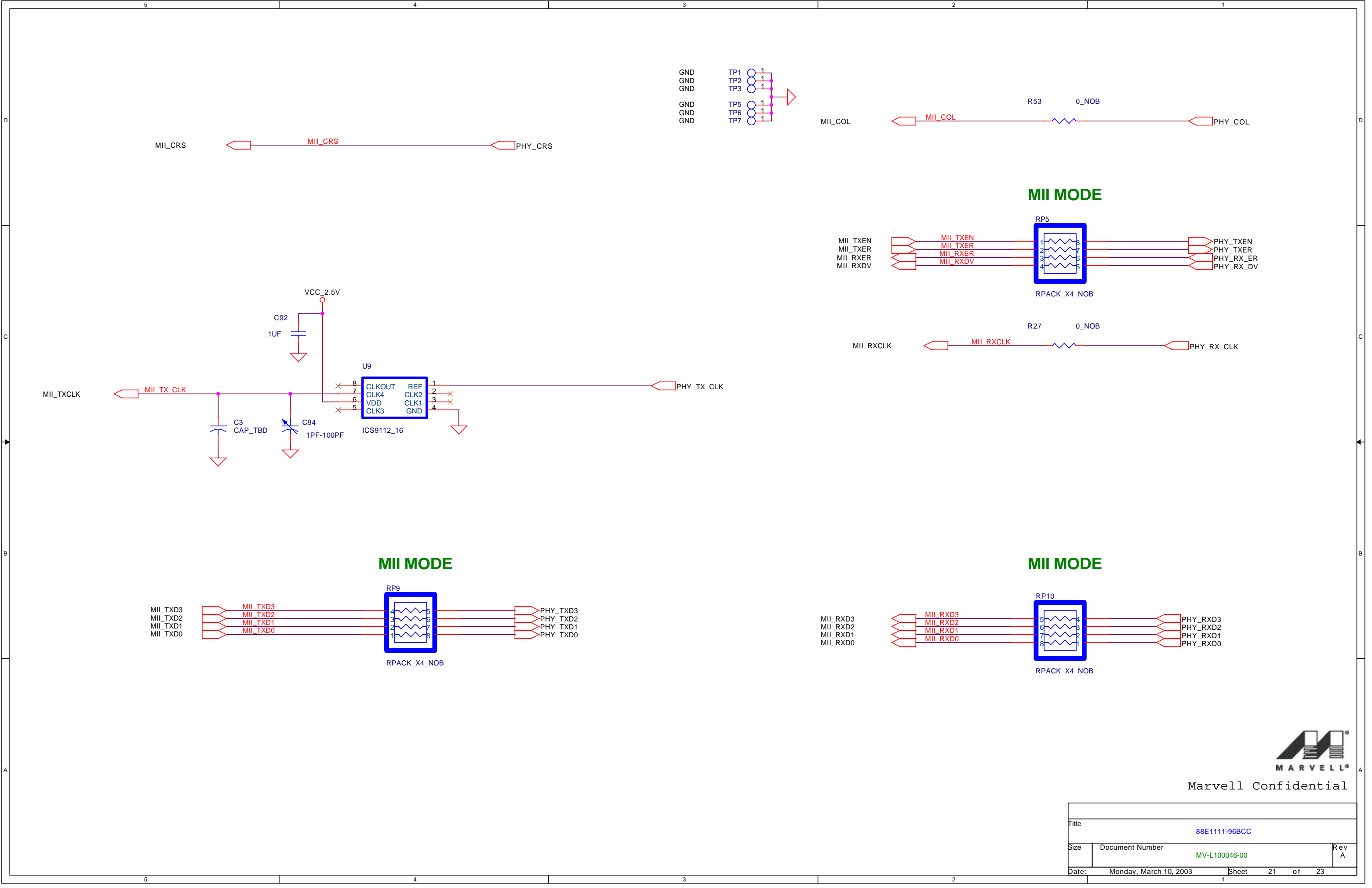
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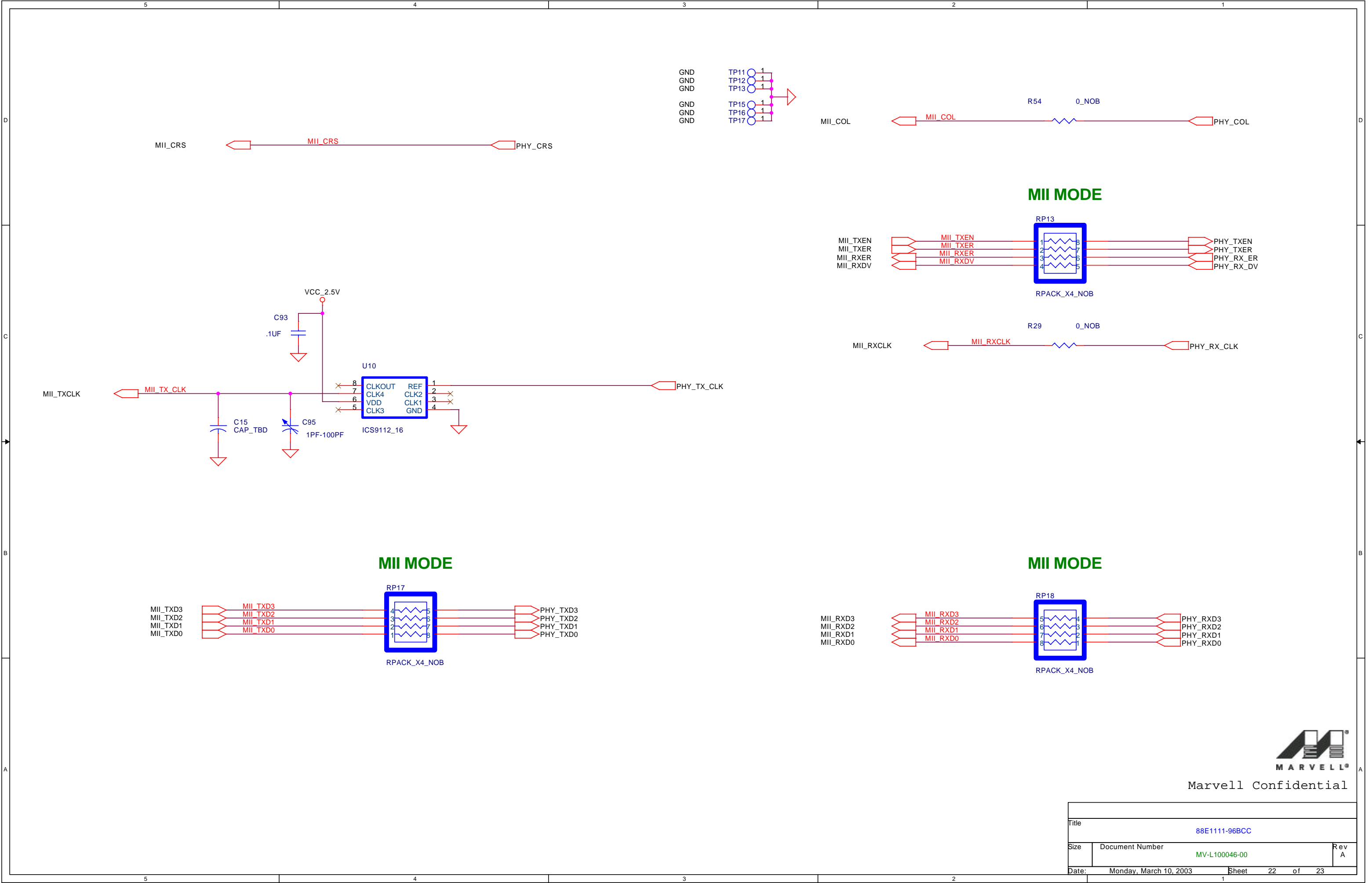
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