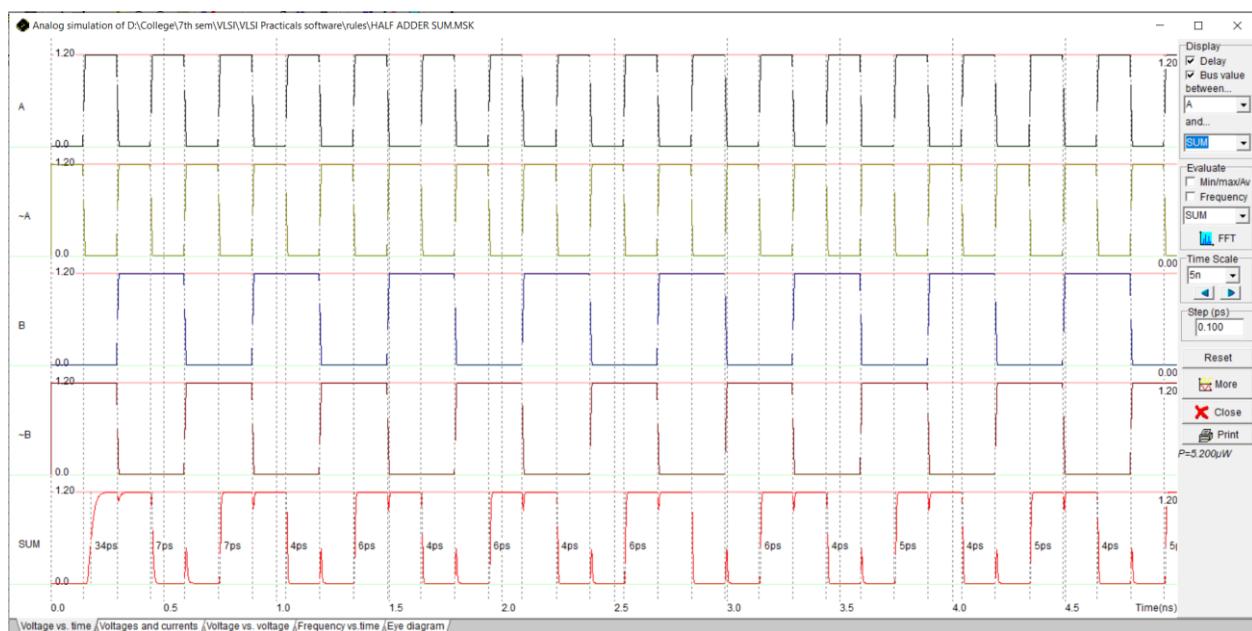
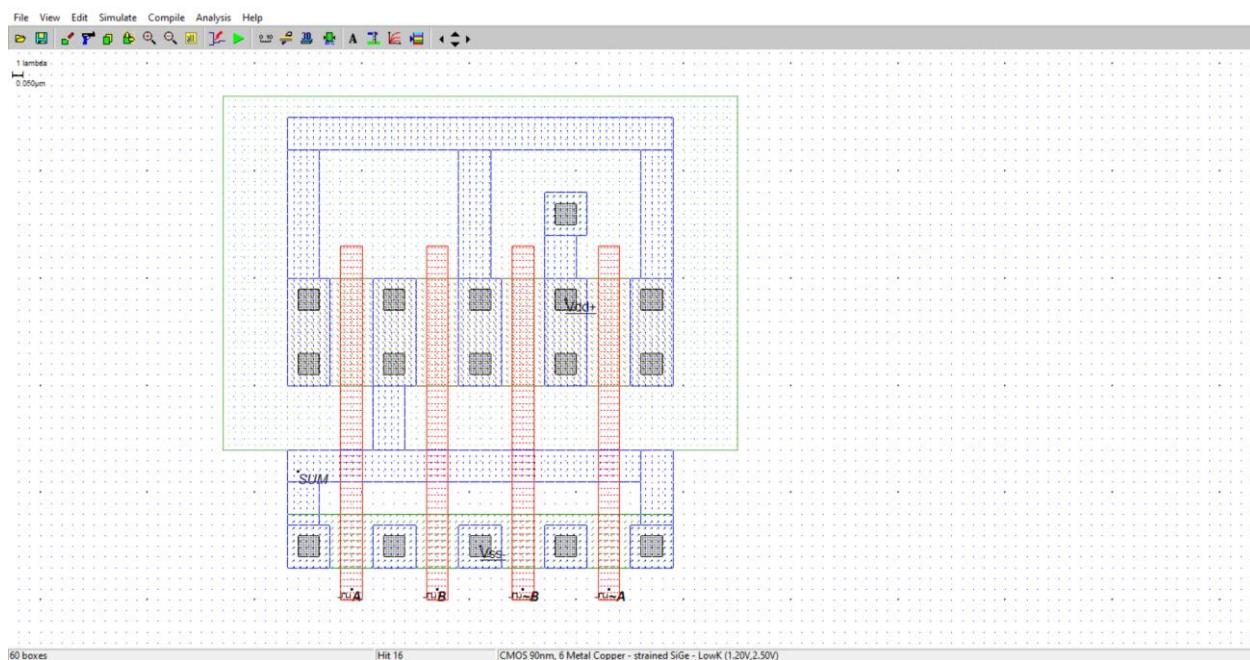


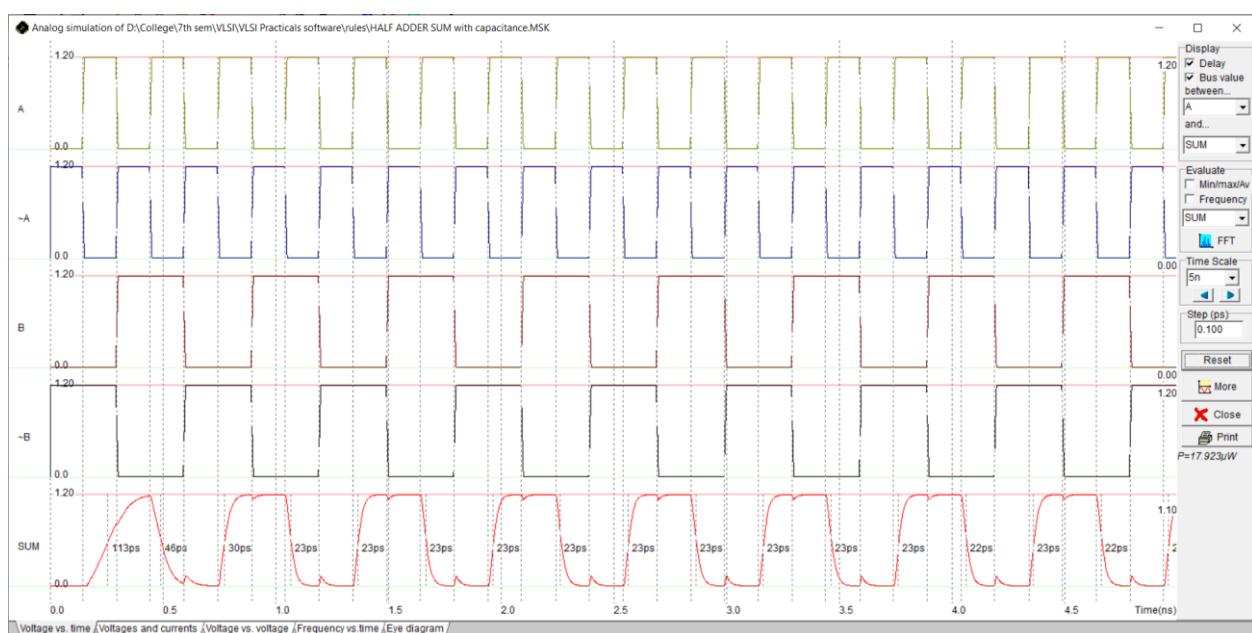
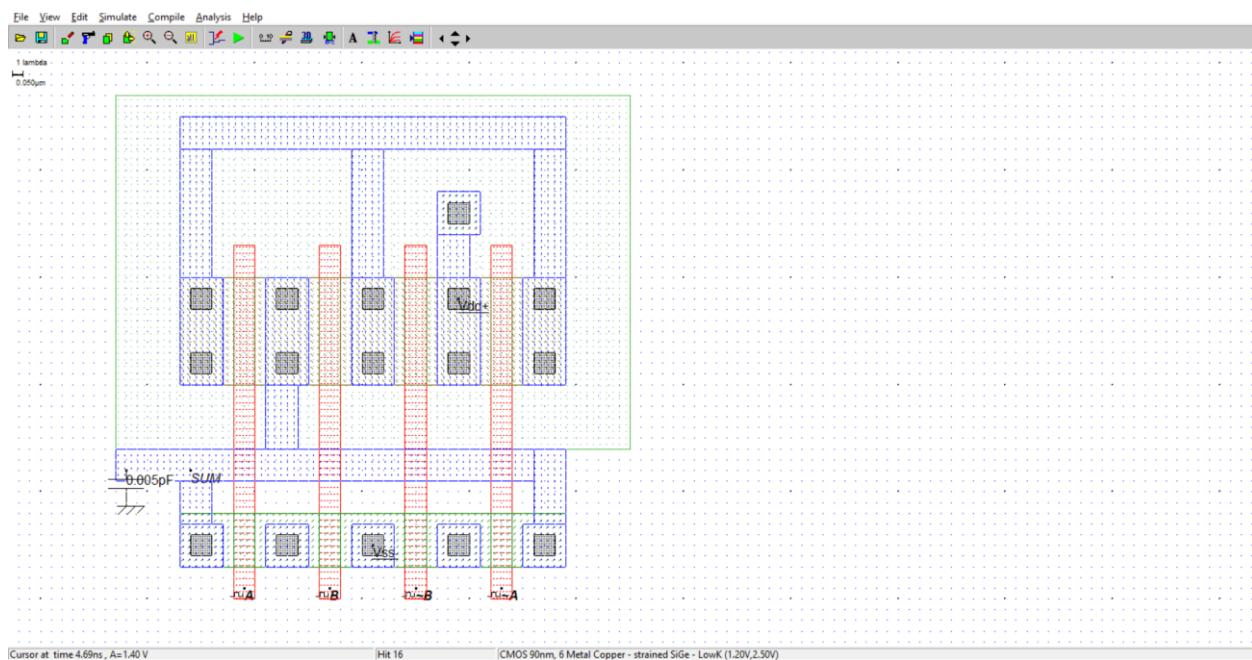
Outputs:

CMOS HALF ADDER - SUM



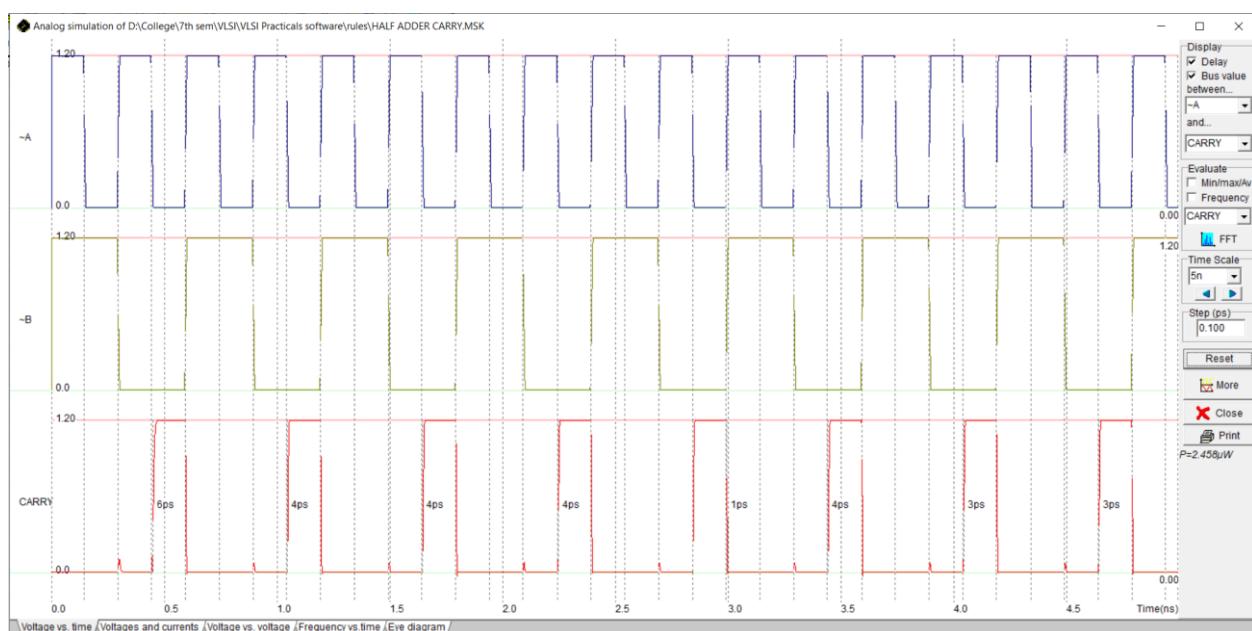
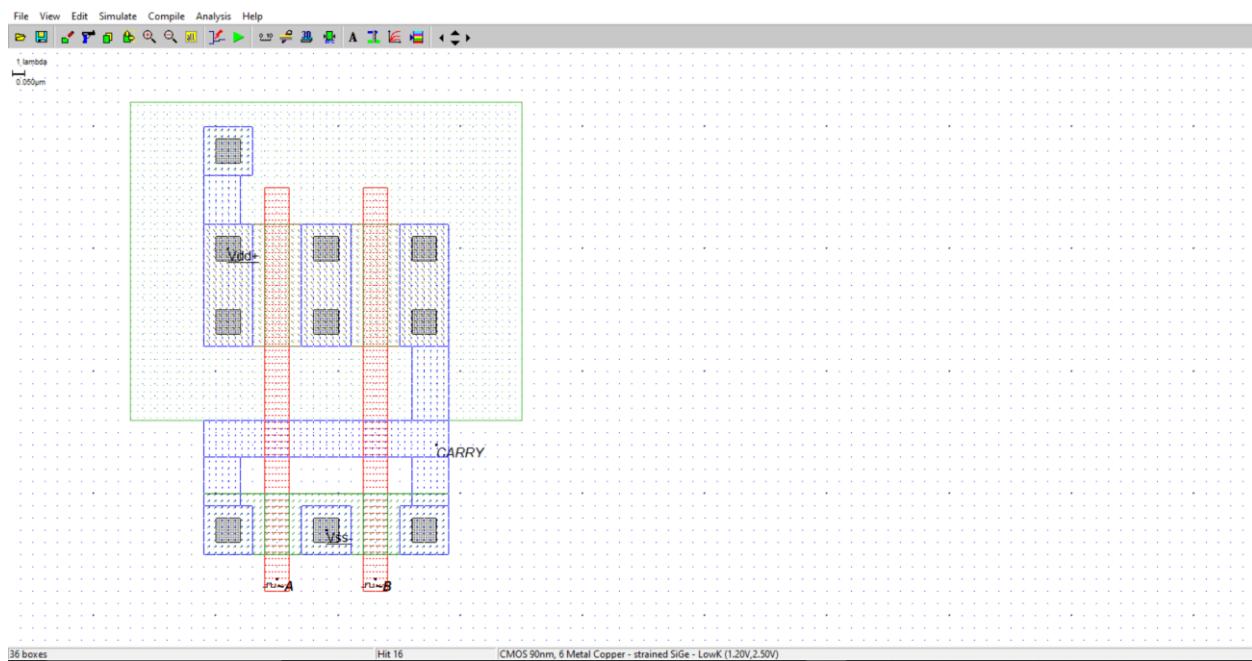
Output

SUM WITH CAPACITANCE



Output

CMOS HALF ADDER - CARRY



Output

CARRY WITH CAPACITANCE

