

## 1. Description

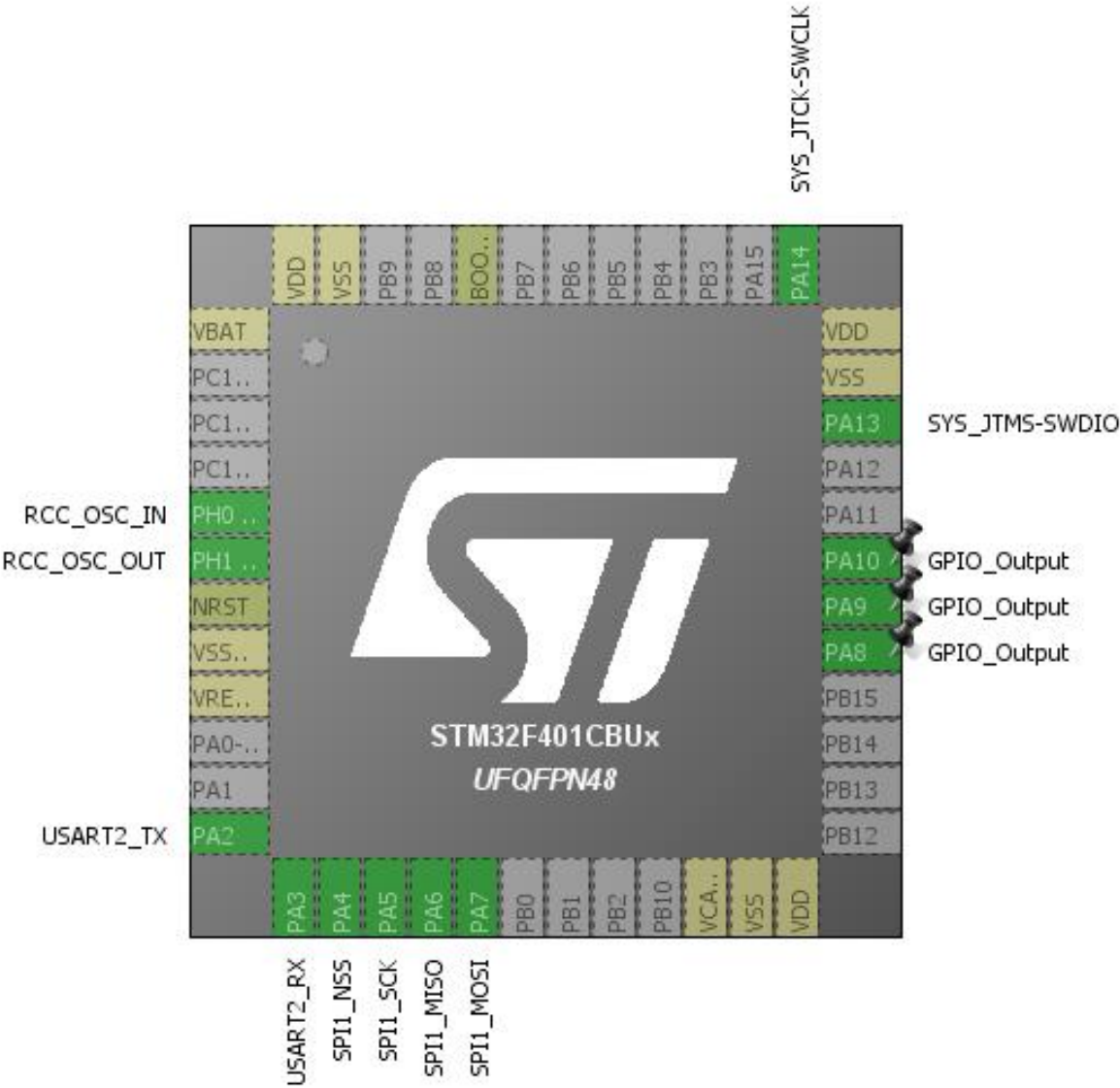
### 1.1. Project

Project Name	F401SWDTest
Board Name	F401SWDTest
Generated with:	STM32CubeMX 4.22.1
Date	09/20/2017

### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F401
MCU name	STM32F401CBUx
MCU Package	UFQFPN48
MCU Pin number	48

2. Pinout Configuration

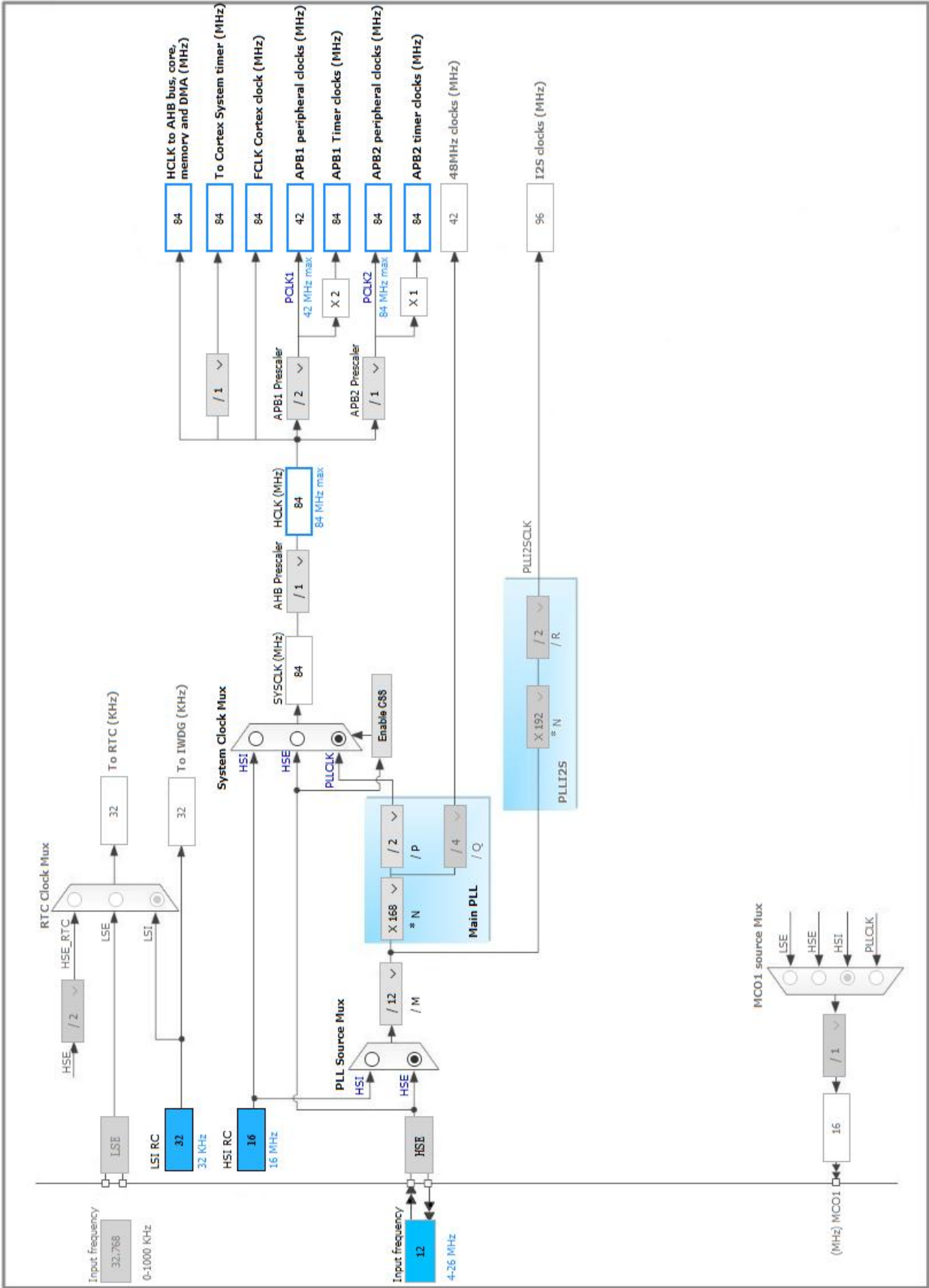


### 3. Pins Configuration

Pin Number UFQFPN48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PH0 - OSC_IN	I/O	RCC_OSC_IN	
6	PH1 - OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA/VREF-	Power		
9	VREF+	Power		
12	PA2	I/O	USART2_TX	
13	PA3	I/O	USART2_RX	
14	PA4	I/O	SPI1_NSS	
15	PA5	I/O	SPI1_SCK	
16	PA6	I/O	SPI1_MISO	
17	PA7	I/O	SPI1_MOSI	
22	VCAP1	Power		
23	VSS	Power		
24	VDD	Power		
29	PA8 *	I/O	GPIO_Output	
30	PA9 *	I/O	GPIO_Output	
31	PA10 *	I/O	GPIO_Output	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function

4. Clock Tree Configuration



## 5. IPs and Middleware Configuration

### 5.1. CRC

mode: Activated

### 5.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

#### 5.2.1. Parameter Settings:

##### System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

##### RCC Parameters:

HSI Calibration Value	16
TIM Prescaler Selection	Disabled
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

##### Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 2
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### 5.3. SPI1

Mode: Full-Duplex Master

Hardware NSS Signal: Hardware NSS Output Signal

#### 5.3.1. Parameter Settings:

##### Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

##### Clock Parameters:

Prescaler (for Baud Rate)	2
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Baud Rate	42.0 MBits/s *
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge
<b>Advanced Parameters:</b>	
CRC Calculation	Disabled
NSS Signal Type	Output Hardware

5.4. SYS

Debug: Serial Wire  
Timebase Source: SysTick

5.5. USART2

Mode: Asynchronous

5.5.1. Parameter Settings:

<b>Basic Parameters:</b>	
Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1
<b>Advanced Parameters:</b>	
Data Direction	Receive and Transmit
Over Sampling	16 Samples

\* User modified value

## 6. System Configuration

### 6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PH0 - OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1 - OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA4	SPI1_NSS	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
USART2	PA2	USART2_TX	Alternate Function Push Pull	Pull-up	Very High *	
	PA3	USART2_RX	Alternate Function Push Pull	Pull-up	Very High *	
GPIO	PA8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

### 6.2. DMA configuration

nothing configured in DMA service

### 6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
SPI1 global interrupt	unused		
USART2 global interrupt	unused		
FPU global interrupt	unused		

\* User modified value



## ***7. Power Consumption Calculator report***

### 7.1. Microcontroller Selection

Series	STM32F4
Line	STM32F401
MCU	STM32F401CBUx
Datasheet	024738_Rev8

### 7.2. Parameter Selection

Temperature	25
Vdd	3.3

## 8. Software Project

### 8.1. Project Settings

Name	Value
Project Name	F401SWDTest
Project Folder	E:\syncting\ElectricalEngineering\STM32Code\F401SWDTest
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F4 V1.16.0

### 8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No