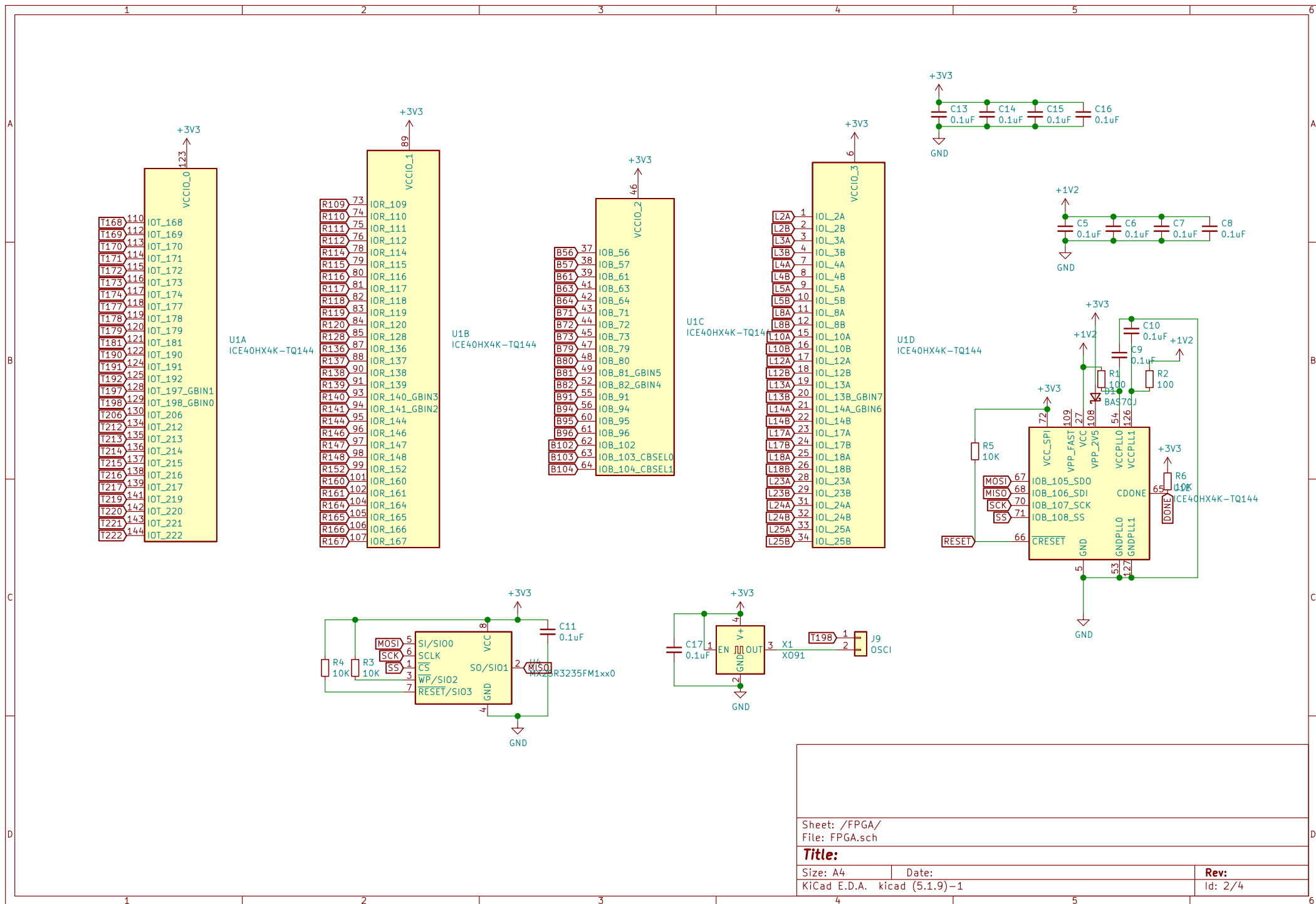


	1	2	3	4	5	6
A	Sheet: FPGA File: FPGA.sch					A
B	Sheet: POWER File: Power.sch					B
C	Sheet: IO File: IO.sch					C
D	<div></div> <div>Sheet: / File: ice40_dev.sch</div> <div>Title:</div> <div>Size: A4Date:KiCad E.D.A. kicad (5.1.9)–1</div> <div>Id: 1/4</div>					D
	1	2	3	4	5	6



Sheet: /FPGA/
File: FPGA.sch

Title:

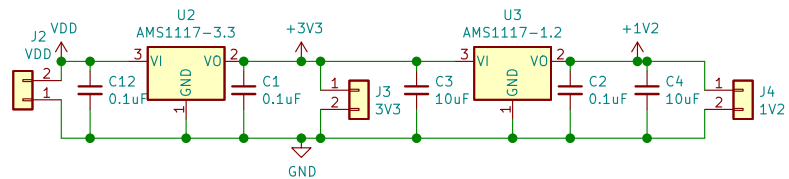
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Sheet: /POWER/
File: Power.sch

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