Parallel Congruence Closure SAT solver

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Abstract—In this report is presented a parallel implementation of a satisfability solver for the congruence closure, able to solve a set of clauses in the quantifiers free fragment of first order logic, based on equality among variables, constants, function applications, recursive data structures with their elements and elements of arrays.

node	find	ccpar
0 x	0	23
1y	0	_
1y 2f->0 3f->1	2	_
3f->1	3	_

I. Introduction

The first theory considered is the theory of equality with uninterpreted functions (EUF). It is the most important theory because its congruence closure algorithm is the core of the entire alghoritm.

MERGE 2 3 UNION 2 3

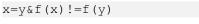
II. METHODOLOGY

A. Algorithm

```
#include <iostream>
int main() {
    // print hello to the console
    std::cout << "Hello,_world!" << std::endl;
    return 0;
}</pre>
```

node find ccpar 0x 0 23 1y 0 2f->0 2 3f->1 2

B. Equality theory congruence closure example



CONGRUENT 2 3 = 1

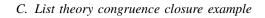


(f)	f
(x)	(y)

node	find	ccpar
0 x	0	2
1y	1	3
1y 2f->0 3f->1	2	_
3f->1	3	_

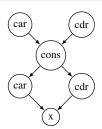
```
3f->1 3 -

MERGE 0 1
UNION 0 1
MERGE 2 3 ?
```



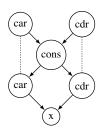
UNSAT

atom(x) &cons(car(x), cdr(x)) = x



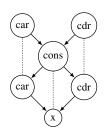
MERGE	4	1
UNION	4	1
MERGE	5	2
UNION	5	2



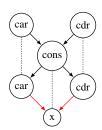


node	find	ccpar
0 x	0	12
1car->0	4	_
2cdr->0	5	_
3cons->12	3	45
4car->3	4	3
5cdr->3	5	3

MERGE 3 0
UNION 3 0
MERGE 4 1 ?
MERGE 4 2 ?
CONGRUENT 4 2 = 0
MERGE 5 1 ?
CONGRUENT 5 1 = 0
MERGE 5 2 ?



node	find	ccpar
0 x	3	_
1car->0	4	_
2cdr->0	5	_
3cons->12	3	4512
4car->3	4	3
5cdr->3	5	3



Euality theory passed UNSAT

D. Array theory congruence closure example

e=select(store(a,i,e),j)&select(a,j)!=e

detected store

1: e=e&j=i&select(a,j)!=e

2: e=select(a,j)&j!=i&select(a,j)!=e

e=e&j=i&select(a,j)!=e e=select(a,j)&j!=i&select(a,j)!=e

node	find	ccpar
0e	0	_
0e 1j	1	4
2i	2	_
3a	3	4
4select->31	4	_

MERGE 0 0 MERGE 1 2 UNION 1 2

node	find	ccpar
0e	0	
0e 1j	1	4
2i	1	_
3a	3	4
4select->31	4	_

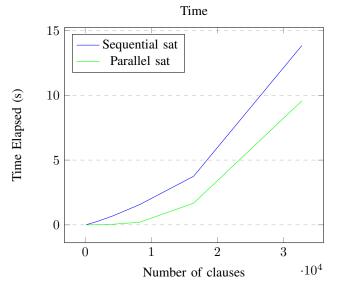
Euality theory passed SAT

III. VALIDATION

IV. BENCHMARKS

Test	#Formulas	Sequential (s)	Parallel (s)	Speedup
7	128	0,0117	0,0001	82,1
8	256	0,0290	0,0003	100,1
9	512	0,0612	0,0008	78,8
10	1024	0,1374	0,0026	52,1
11	2048	0,2988	0,0103	29,1
12	4096	0,6736	0,0442	15,3
13	8192	1,5526	0,1968	7,9
14	16384	3,7465	1,6690	2,2
15	32768	13,8530	9,5786	1,4

V. PERFORMANCE ANALYSIS



VI. CONCLUSION