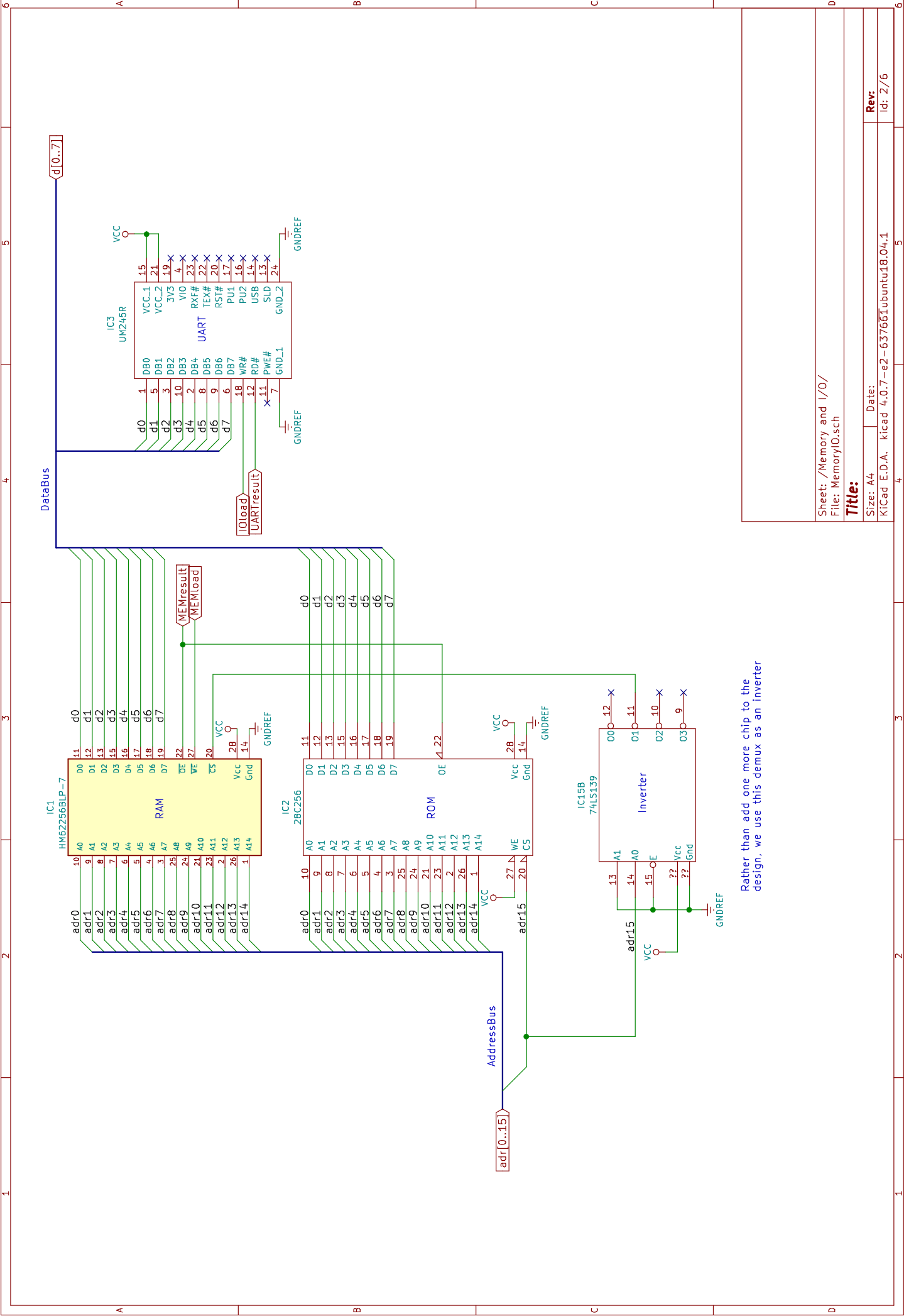


1	2	3	4	5	6
A	Sheet: Memory and I/O <div></div> <div>File: MemoryIO.sch</div>		Sheet: ALU and Data Registers <div></div> <div>File: ALU_DataRegs.sch</div>		
B	Sheet: Addressing <div></div> <div>File: Addressing.sch</div>		Sheet: Instruction Decode <div></div> <div>File: IR_Decode.sch</div>		
C					
D					
<div>Sheet: / File: Schematic.sch</div> <div>Title:</div> <div>Size: A4Date:KICad E.D.A. kicad 4.0.7-e2-637661ubuntu18.04.1</div> <div>Rev: Id: 1/6</div>					
1	2	3	4	5	6



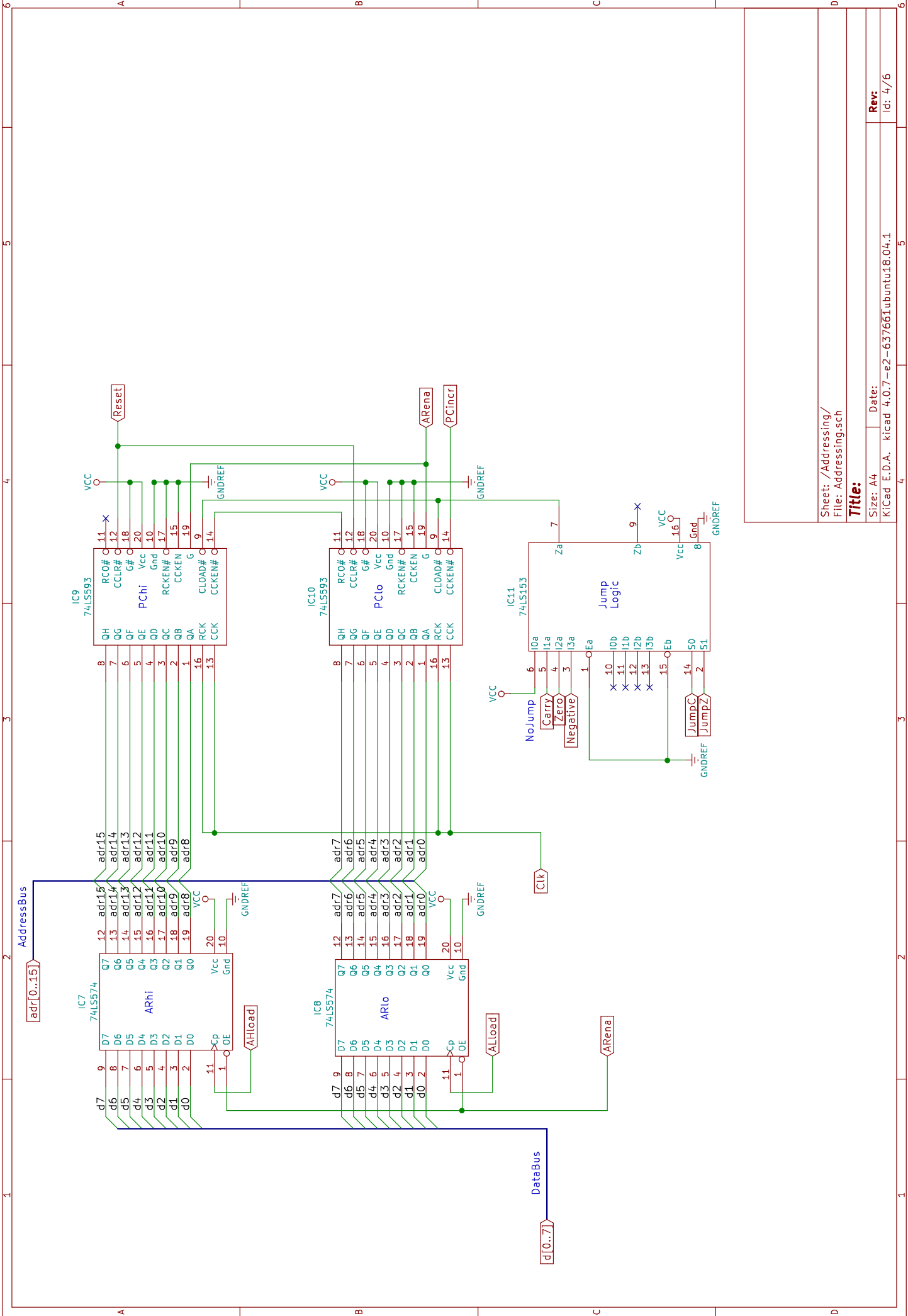
Rather than add one more chip to the design, we use this demux as an inverter

Sheet: /Memory and I/O/
File: MemoryO.sch

Title:

Size: A4 Date:

KiCad E.D.A. kicad 4.0.7-e2-637661ubuntu18.04.1 Id: 2/6



Sheet: /Addressing/
File: Addressing.sch

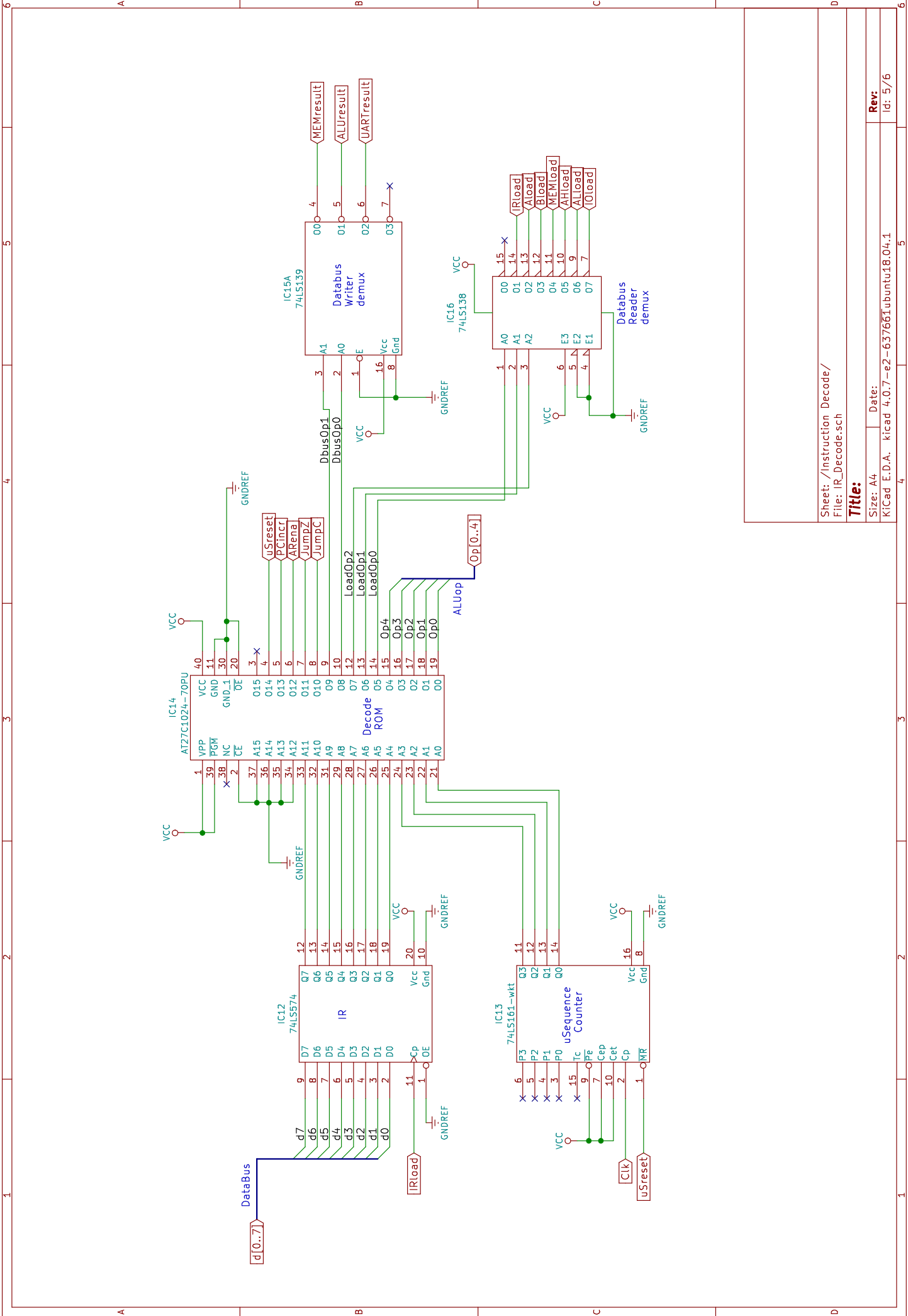
Title:

Size: A4 Date:

KiCad E.D.A. kicad 4.0.7-e2-637661ubuntu18.04.1

Rev:

Id: 4/6



Sheet: /Instruction Decode/
File: IR_Decode.sch

Title:

Size: A4 Date:

KiCad E.D.A. kicad 4.0.7-e2-637661ubuntu18.04.1

Rev:

Id: 5/6

