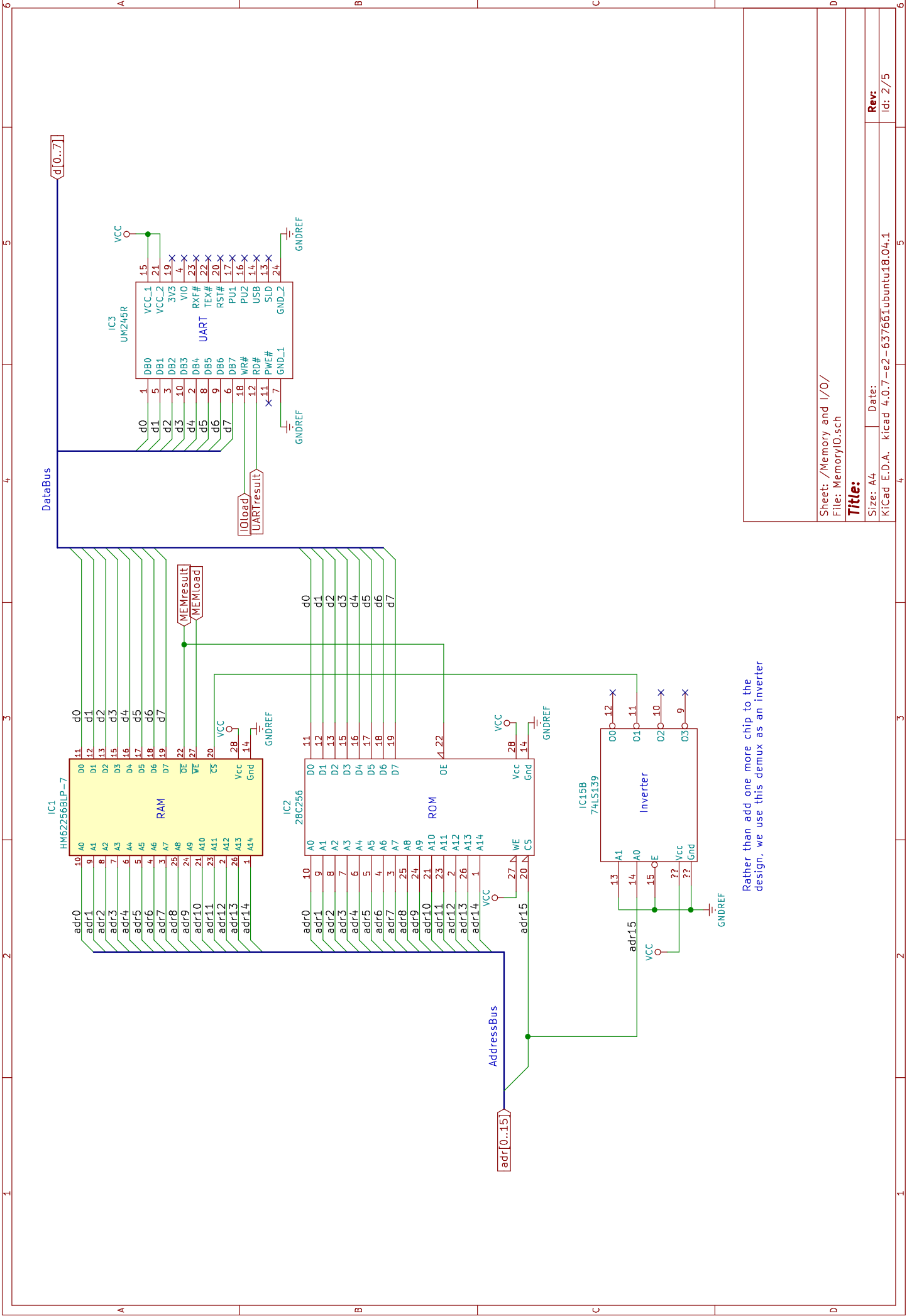


1	2	3	4	5	6			
A	Sheet: Memory and I/O		Sheet: ALU and Data Registers					
B	File: MemoryIO.sch		File: ALU_DataRegs.sch					
C	Sheet: Addressing		Sheet: Instruction Decode					
D	File: Addressing.sch		File: IR_Decode.sch					
Sheet: /								
File: Schematic.sch								
Title:								
Size: A4		Date:		Rev:				
KiCad E.D.A. kicad 4.0.7-e2-637661		tubuntu18.04.1		Id: 1/5				
1	2	3	4	5	6			



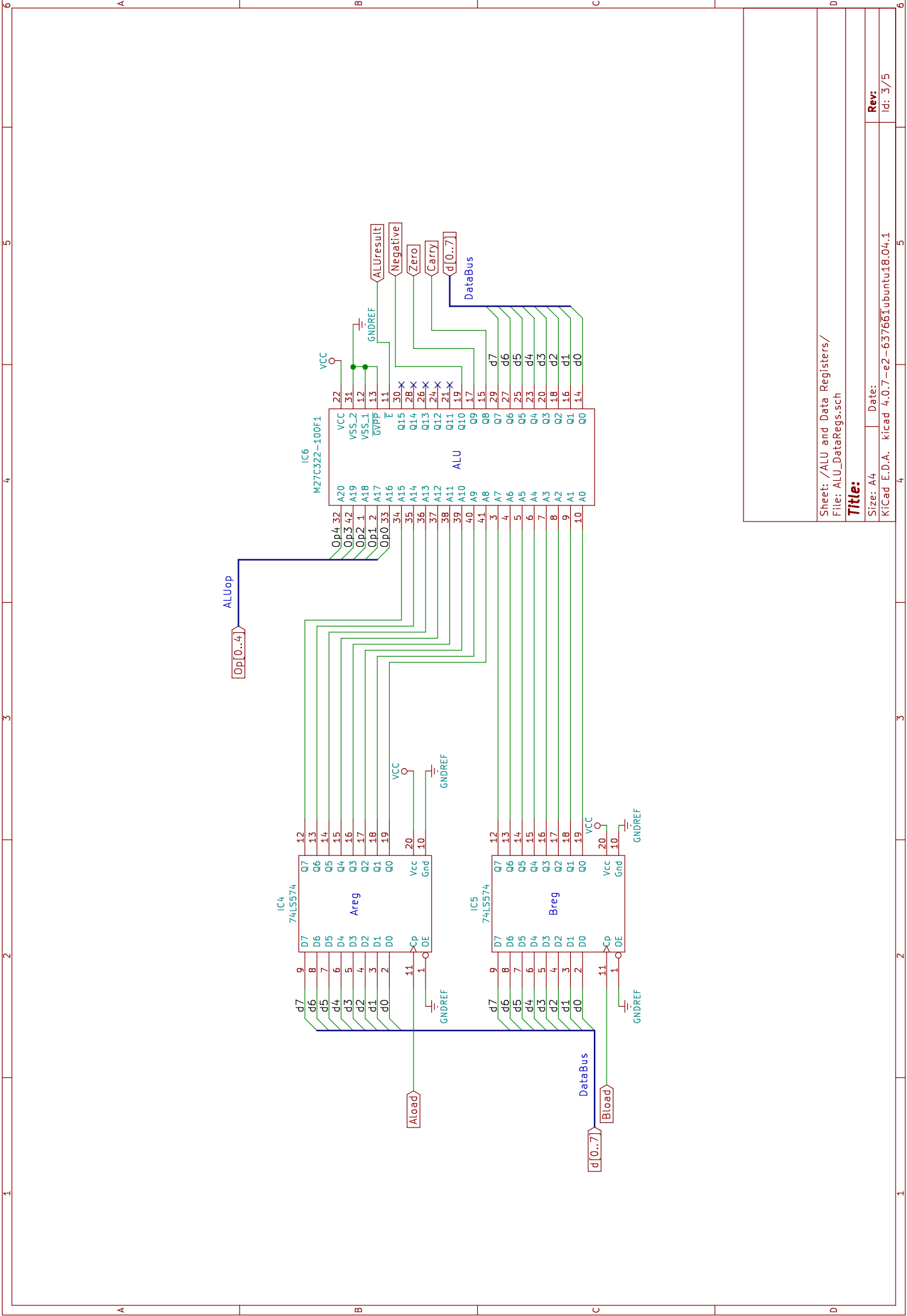
Rather than add one more chip to the design, we use this demux as an inverter

Sheet: /Memory and I/O/
File: MemoryIO.sch

Title:

Size: A4 Date:

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Sheet: /ALU and Data Registers/
File: ALU_DataRegs.sch

Title:

Size: A4 Date:

KiCad E.D.A. kicad 4.0.7-e2-637661ubuntu18.04.1

Rev:

Id: 3/5

