

	1	2	3	4	5	6
A	<div>CPU</div> <div>File: cpu.kicad_sch</div>		<div>MEM</div> <div>File: mem.kicad_sch</div>		<div>UART</div> <div>File: uart.kicad_sch</div>	
B	<div>Storage_RTC</div> <div>File: storage.kicad_sch</div>		<div>Power and Debugging</div> <div>File: power.kicad_sch</div>			
C						
D						
	1	2	3	4	5	6

MMU09 Schematic dated 1 Sep 2023.
Warning: completely untested. Do
not build using this schematic.

Sheet: /
File: MMU09.kicad_sch

Title:

Size: A4

Date: 2023-09-01

Rev:

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Id: 1/6

MMU09 Schematic dated 1 Sep 2023.
Warning: completely untested. Do
not build using this schematic.

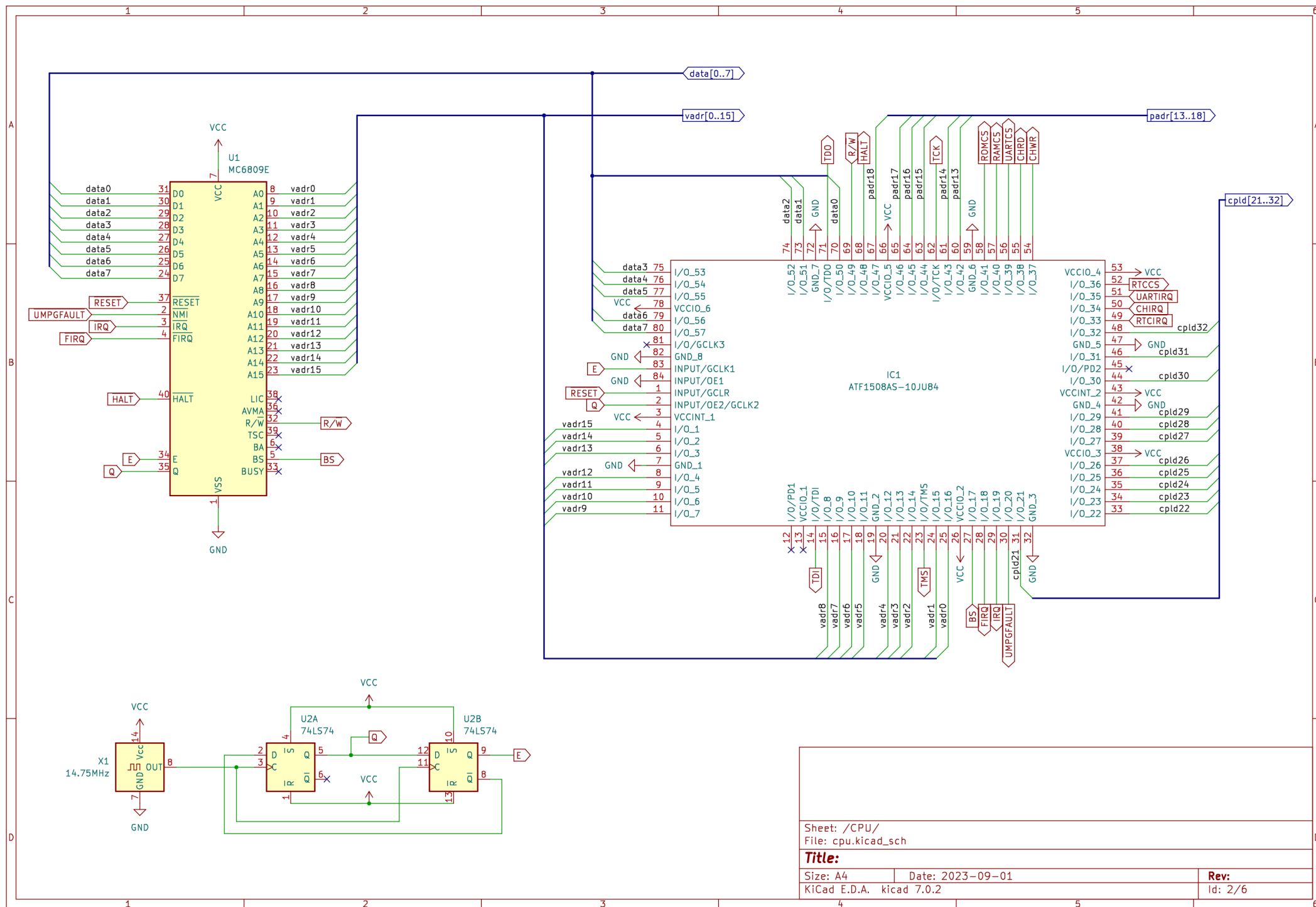
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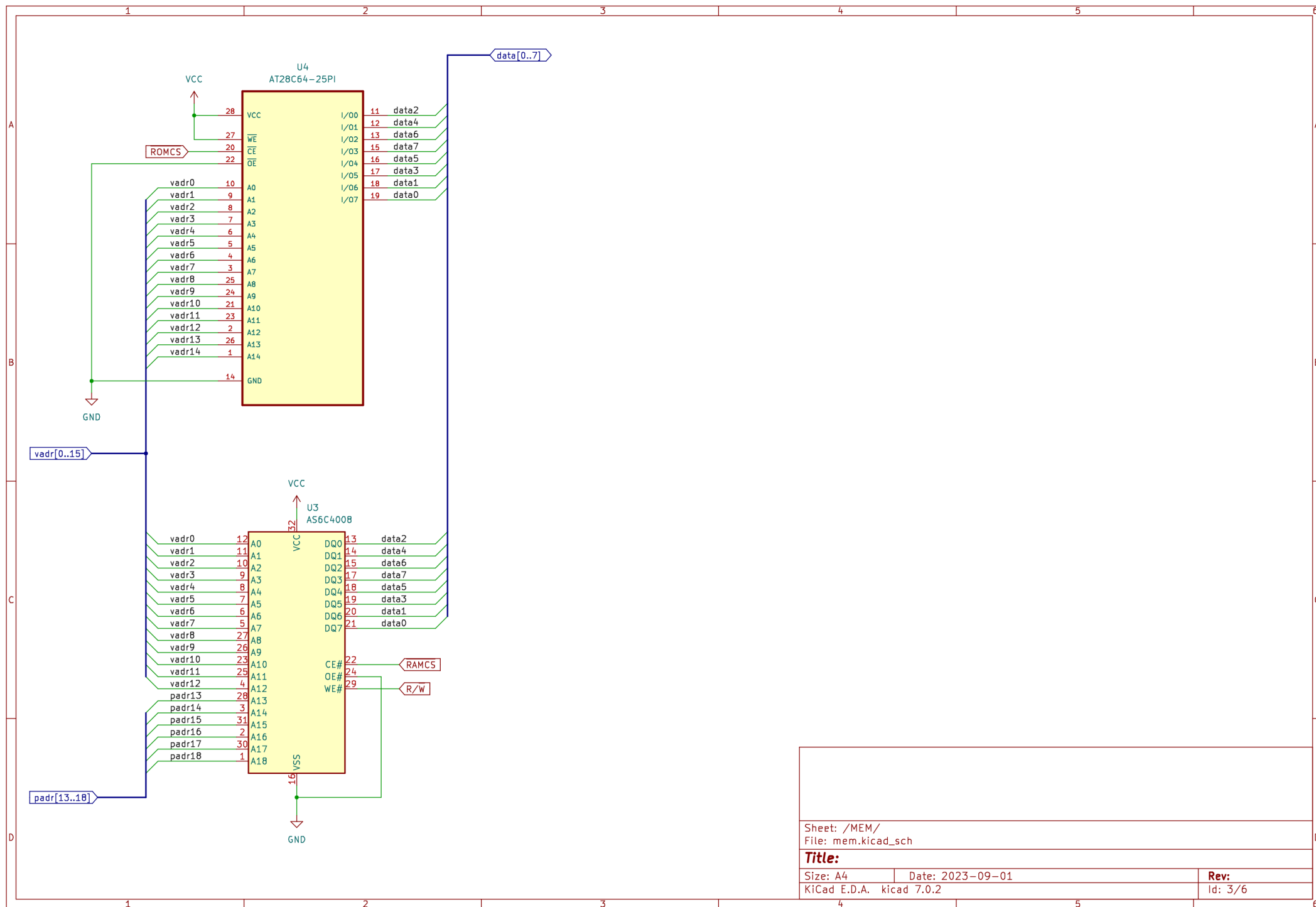
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Size: A4Date: 2023-09-01

KiCad E.D.A. kicad 7.0.2

Rev:
Id: 1/6





Sheet: /MEM/
File: mem.kicad_sch

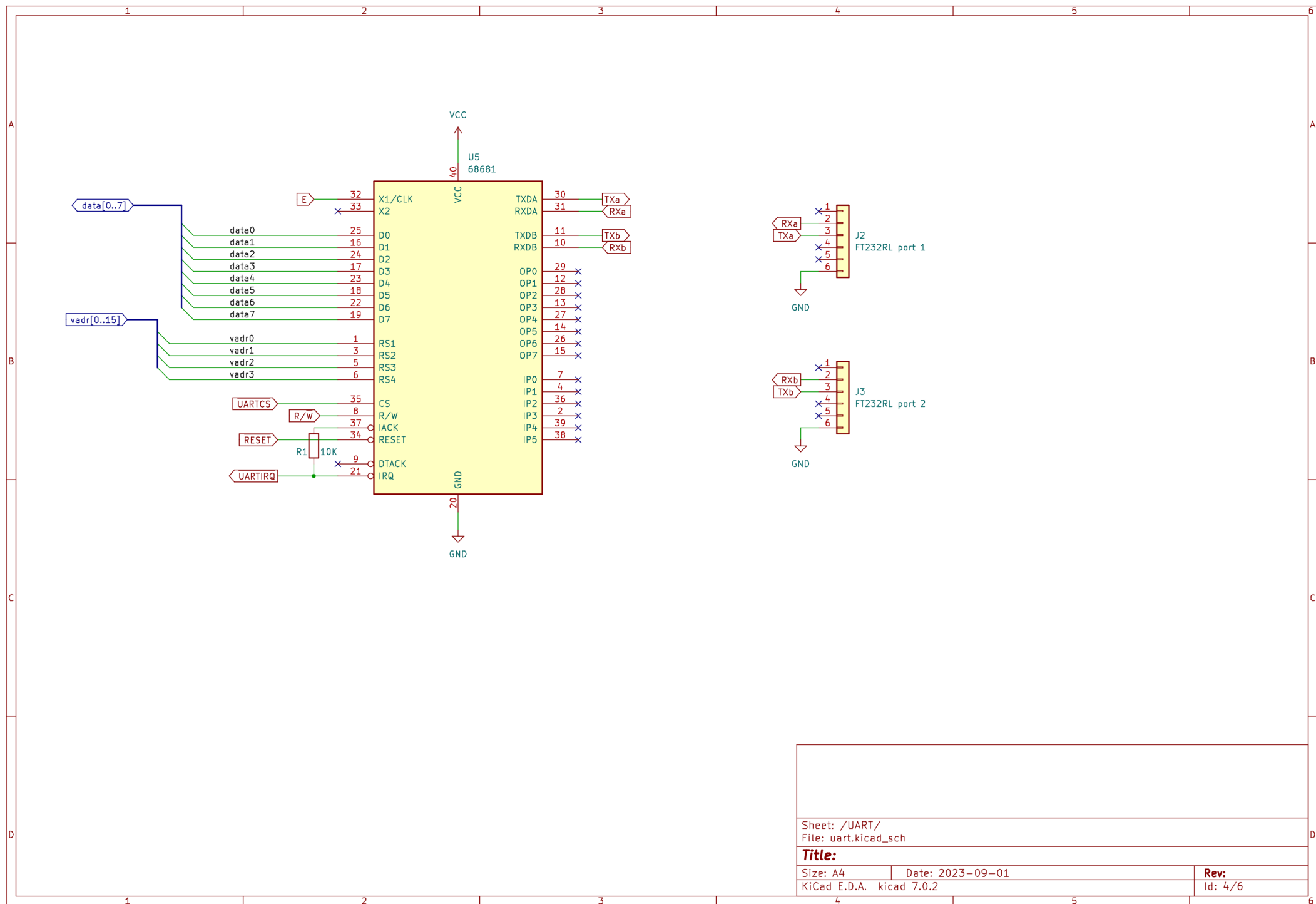
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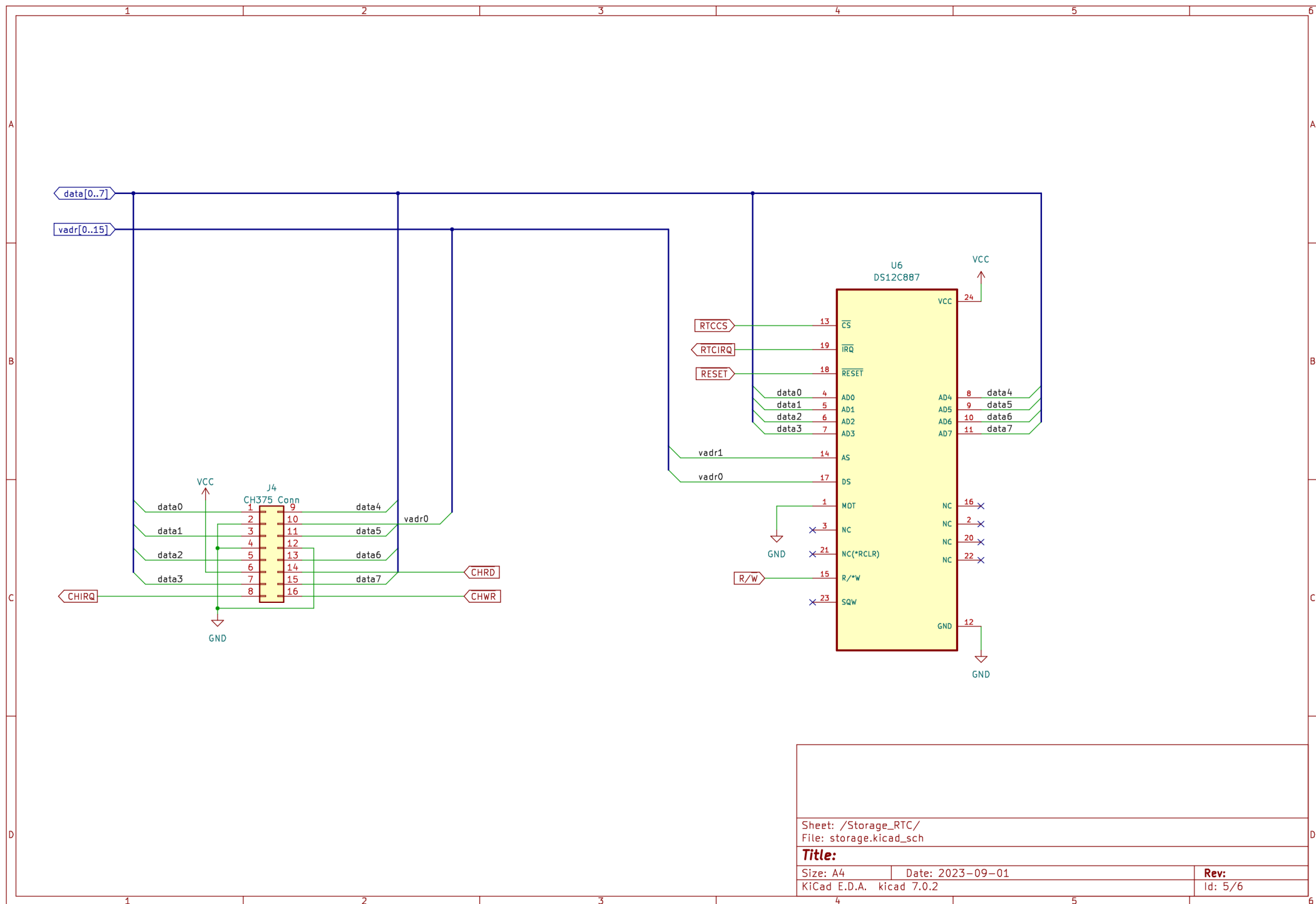
Size: A4 Date: 2023-09-01

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Rev:

Id: 3/6





Sheet: /Storage_RTC/
File: storage.kicad_sch

Title:

Size: A4 Date: 2023-09-01
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Rev:
Id: 5/6

