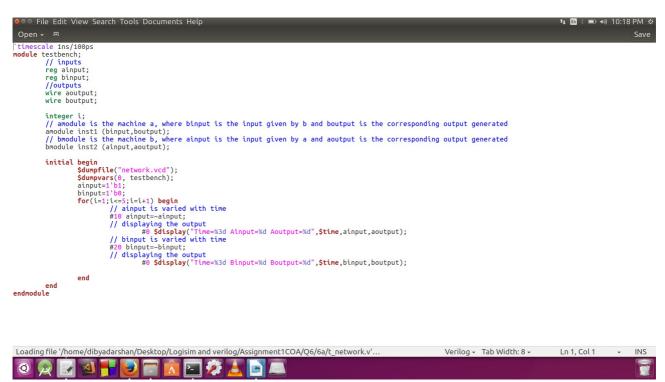
## **Interconnection Network**

```
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//Module for machine a module amodule(input binput,output reg boutput);
//Whenever b sends an input, the output boutput is generated which is later printed in the testbench along with the timestamp always@(binput) begin boutput=binput;
end
endmodule
//Module for machine b module(input ainput,output reg aoutput);
//Whenever a sends an input, the output aoutput is generated which is later printed in the testbench along with the timestamp always@(ainput) begin aoutput=ainput;
end
endmodule
```

Machine A sends message to machine B and vice versa, periodically. Each of the machine prints the received message with the time stamp



The time is printed using \$time function