A Relatively Small Turing Machine Whose Behavior Is Independent of Set Theory

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Abstract

Since the definition of the Busy Beaver function by Radó in 1962, an interesting open question has been what the smallest value of n for which $BB(n)$ is independent of ZFC set theory. Is this n approximately 10, or closer to 1,000,000, or is it even larger? In this paper, we show that it is at most 8,013 by presenting an explicit description of a 8,013-state Turing machine Z with 1 tape and a 2-symbol alphabet that cannot be proved to run forever in ZFC (even though it presumably does), assuming ZFC is consistent. The machine is based on work of Harvey Friedman on independent statements involving order-invariant graphs. In doing so, we give the first known upper bound on the highest provable Busy Beaver number in ZFC. We also present an explicit description of a 4,888-state Turing machine G that halts if and only if there is a counterexample to Goldbach's conjecture, and an explicit description of a 5,372-state Turing machine R that halts if and only if the Riemann hypothesis is false. To create G, R, and Z, we develop and use a higher-level language, Laconic, which is much more convenient than direct state manipulation.

1 Introduction

1.1 Background and Motivation

Zermelo-Fraenkel set theory with the axiom of choice, more commonly known as ZFC, is an axiomatic system invented in the twentieth which has since been used as the foundation of most of modern mathematics. It encodes arithmetic by describing natural numbers as increasing sets of sets.

Like any axiomatic system capable of encoding arithmetic, ZFC is constrained by Gödel's two incompleteness theorems. The first incompleteness theorem states that if ZFC is *consistent* (it never proves both a statement and its opposite), then ZFC cannot also be *complete* (able to prove every true statement). The second incompleteness theorem states that if ZFC is consistent, then ZFC cannot prove its own consistency. Because we have built modern mathematics on top of ZFC, we can reasonably be said to have assumed ZFC's consistency. This means that we must also believe that ZFC cannot prove its own consistency. This fact carries with it certain surprising conclusions.

In particular, consider a Turing machine Z that enumerates, one after the other, each of the provable statements in ZFC. To describe how such a machine might be constructed, Z could iterate over the axioms and inference rules of ZFC, applying each in every possible way to each conclusion

or pair of conclusions that had been reached so far. We might ask Z to halt if it ever reaches a contradiction; in other words, Z will halt if and only if it ever finds a proof of $0 = 1$. Because we know that this machine will enumerate *every* provable statement in ZFC, we know that it will run forever if and only if ZFC is consistent.

It follows that Z is a Turing machine for which the question of its behavior (whether or not it halts when run indefinitely) is equivalent to the consistency of ZFC. While we will talk about ZFC throughout this paper, rather than simple Zermelo-Fraenkel set theory, this is simply convention brought about by the fact that ZFC is a more powerful and more commonly-used set of axioms. In more detail, for the purposes of this paper, the Axiom of Choice is irrelevant: the consistency of ZFC is equivalent to the consistency of simple ZF set theory, [8] and ZFC and ZF prove exactly the same arithmetical statements (which include, among other things, statements about whether Turing machines halt). [17] Therefore, just as ZFC cannot prove its own consistency (assuming ZFC is consistent), ZFC also cannot prove that Z will run forever.

This is interesting because, while the undecidability of the halting problem tells us that there cannot exist an algorithmic method for determining whether an *arbitrary* Turing machine loops or halts, Z is an example of a *specific* Turing machine whose behavior cannot be proven one way or the other using the foundation of modern mathematics. Mathematicians and computer scientists think of themselves as being able to determine how a given algorithm will behave if we are given enough time to stare at it; despite this intuition, Z is a machine whose behavior we can never prove without assuming axioms more powerful than those generally assumed in most of modern mathematics.

1.2 Turing Machines

There are many definitions for Turing machines, each differing slightly from the other. For example, some definitions allow the machine to have multiple tapes; others only allow it to have one. Some definitions allow an arbitrarily large alphabet, while others allow only two symbols. Some definitions allow the tape head to remain in place, while others require it to move at every time-step. In most research regarding Turing machines, mathematicians don't concern themselves with which of these models to use, because any one of them can simulate the others. However, because this work is concerned with upper-bounding the exact number of states required to perform certain tasks, it is important to define precisely what model of Turing machine is being used.

Formally, a k-state Turing machine is a 7-tuple $M = (Q, \Gamma, b, \Sigma, \delta, q_0, F)$, where:

Q is the set of k *states* $\{q_0, q_1, \ldots, q_{k-2}, q_{k-1}\}$
$\Gamma = \{a, b\}$ is the set of *tape alphabet symbols*
$\mathtt{a}$ is the *blank symbol*
$\Sigma =$ is the set of *input symbols*
$\delta = Q \times \Gamma \to (Q \cup F) \times \Gamma \times \{L, R\}$ is the *transition function*
q_0 is the *start state*
$F = \{\text{ACCEPT}, \text{REJECT}, \text{ERROR}\}$ is the set of *halting transitions.*

A Turing machine's *states* make up the Turing machine's easily-accessible, finite memory. The Turing machine's state is initialized to q_0.

The *tape alphabet symbols* correspond to the symbols that can be written on the Turing machine's infinite tape.

In this work, all Turing machines discussed are run on the all-`a` input.

The *transition function* encodes the Turing machine's behavior. It takes two inputs: the current state of the Turing machine (an element of Q) and the symbol read off the tape (an element of Γ). It outputs three separate instructions: what state to enter (an element of Q), what symbol to write onto the tape (an element of Γ) and what direction to move the head in (an element of $\{L, R\}$). A transition function specifies the entire behavior of the Turing machine in all cases.

The *start state* is the state that the Turing machine is in at initialization.

A *halting transition* is a transition that causes the Turing machine to halt. While having three possible halting transitions is not necessary for our purposes, being able to differentiate between three different types of halting (ACCEPT, REJECT, and ERROR) is useful for testing.

1.3 The Busy Beaver Function

Consider the set of all Turing machines with k states, for some positive integer k. We call a Turing machine B a *k-state Busy Beaver* if when run on the empty tape as input, B halts, and also runs for at least as many steps before halting as all other halting k-state Turing machines. [16]

In other words, a Busy Beaver is a Turing machine that runs for at least as long as all other halting Turing machines with as many states as it. Another common definition for a Busy Beaver is a Turing machine that writes as many 1's on the tape as possible; because the number of 1's written is a somewhat arbitrary measure, it is not used in this work.

The *Busy Beaver function*, written $BB(k)$, equals the number of steps it takes for a k-state Busy Beaver to halt. The Busy Beaver function has many striking properties. To begin with, it is not *computable*; in other words, there does not exist an algorithm that takes k as input and returns $BB(k)$, for arbitrary values of k. This follows directly from the undecidability of the halting problem. Suppose an algorithm existed to compute the Busy Beaver function; then given a k-state Turing machine M as input, we could compute $BB(k)$ and run M for $BB(k)$ steps. If, after $BB(k)$ steps, M had not yet halted, we could safely conclude that M would never halt. Thus, we could solve the halting problem, which we know is impossible.

By the same argument, $BB(k)$ must grow faster than any computable function. (To check this, assume that some computable function $f(k)$ grows faster than $BB(k)$, and substitute $f(k)$ for $BB(k)$ in the rest of the proof.) In particular, the Busy Beaver grows even faster than (for instance) the Ackermann function, a well-known fast-growing function.

Because finding the value of $BB(k)$ for a given k requires so much work (one must fully explore the behavior of all k-state Turing machines), few explicit values of the Busy Beaver function are known. The known values are [10] [3]:

$$BB(1) = 1$$

$$BB(2) = 6$$

$$BB(3) = 21$$

$$BB(4) = 107$$

For $BB(5)$ and $BB(6)$, only lower bounds are known: $BB(5) \geq$ 47,176,870, and $BB(6) \geq 7.4 \times 10^{36,534}$. Researchers are currently working on pinning down the value of $BB(5)$ exactly, and some consider it to be possibly within reach. A summary of the current state of human knowledge about Busy Beaver values can be found at [13].

Another way to discuss the Busy Beaver sequence is to say that modern mathematics has established a *lower bound* of 4 on the highest provable Busy Beaver value. In this paper, we prove the first known *upper bound* on the highest provable Busy Beaver value in ZFC; that is, we give a value of k, namely 8,013, such that the value of $BB(k)$ cannot be proven in ZFC.

Intuitively, one might expect that while no algorithm may exist to compute $BB(k)$ for *all* values of k, we could find the value of $BB(k)$ for any *specific* k using a procedure similar to the one we used to find the value of $BB(k)$ for $k \leq 4$. The reason this is not so is closely tied to the existence of a machine like the Gödelian machine Z, as described in Section 1.1. Suppose that Z has k states. Because Z's behavior (whether it halts or loops) cannot be proven in ZFC, it follows that the value of $BB(k)$ also cannot be proven in ZFC; if it could, then a proof would exist of Z's behavior in ZFC. Such a proof would consist of a *computation history* for Z, which is an explicit step-by-step description of Z's behavior for a certain number of steps. If Z halts, a computation history leading up to Z's halting would be the entire proof; if Z loops, then a computation history that takes $BB(k)$ steps, combined with a proof of the value of $BB(k)$, would constitute a proof that Z will run forever.

In this paper we construct a machine like Z, for which a proof that Z runs forever would imply that ZFC was consistent. In doing so, we give an explicit upper bound on the highest Busy Beaver value provable in ZFC assuming the consistency of a slightly stronger set theory. Our machine, which we shall refer to as Z hereafter, contains 8,013 states. Therefore, we will never be able to prove the value of $BB(8{,}013)$ without assuming more powerful axioms than those of ZFC. This upper bound is presumably very far from tight, but it is a first step.

Nontrivial ideas are needed to achieve such a small state count, without which the size of Z might range in the hundreds of thousands or even millions of states. We briefly introduce these ideas in the subsection following this one, and explore them in much greater detail later in the paper. These ideas constitute this paper's main technical contribution.

1.4 Parsimony

In most algorithmic study, efficiency is the primary concern. In designing Z, however, parsimony is the only thing that matters. One historical analogue is the practice of "code-golfing": a recreational pursuit adopted by some programmers in which the goal is to produce a piece of code in a given programming language, using as few characters as possible. Many examples of code-golfing can be found at [19]. The goal of designing a Turing machine with as few states as possible to accomplish a certain task, without concern for the machine's efficiency or space usage, can be thought of as code-golfing with a particularly low-level programming language.

Part of the charm of Turing machines is that they give us a "standard reference point" for measuring complexity, unencumbered by the details of more sophisticated programming languages. Also, with Turing machines, there can be no suspicion that we engineered a programming formalism just for the purpose of code-golfing, or for making the concepts we want expressed artificially simple to describe. This is why we prefer Turing machines as a tool for measuring complexity; not because they are particularly special, but simply because they are so primitive and so minimal that their specifics will interfere minimally with what we mean by an algorithm being "complicated."

In this paper, we use three ideas for generating parsimonious Turing machines: Harvey Friedman's mathematical statements, *on-tape processing*, and *introspective* Turing machines. The last of these ideas was proposed, under a different name and with some variations, by Ben-Amram and Petersen in 2002. [2] These three ideas are explained in more detail in Subsections 3.1, 8.1, and 8.3,

respectively, but we will summarize them very briefly here.

The first idea, that of using Friedman's mathematical statements, is simply to use the research done by Friedman into finding simple-to-express statements that are equivalent to the consistency of various axiomatic systems. In particular, we make use of a statement discovered by Friedman to be equivalent to the consistency of a set theory known to be stronger than ZFC (and whose consistency, therefore, would imply the consistency of ZFC). [6]

The second idea, on-tape processing, is a way to encode high-level commands into a Turing machine parsimoniously. Instead of converting commands to groups of states directly, which incurs a multiplicative overhead based on how large these groups need to be, on-tape processing begins by writing the commands onto the tape, using as efficient an encoding as possible. Then, once the commands are on the tape, the commands are processed by a single group of states that understands how to interpret them.

The third idea, introspective Turing machines, is a way to write long strings onto the tape using as few states as possible. The idea is to encode information one of each state's transitions, instead of encoding information in each state's write field. This is advantageous because there are many choices for which state to point a transition to, but only two choices for what bit to write. Therefore, more information can be encoded in each state using this method.

1.5 Implementation Overview

To generate descriptions of Turing machines with nice mathematical properties entirely by hand is a daunting task. Rather than approach the problem directly, we created tools for generating parsimonious Turing machines while presenting an interface that is comfortably familiar to most programmers (and to us!)

We created two tools. At the top level is the Laconic programming language, whose syntax and capabilities are similar to those of most programming languages, such as Java or Python. Beneath it we created a lower-level language called Turing Machine Descriptor (TMD). TMD is quite unlike most programming languages, and is better thought of as a convenient way to describe a multi-tape, 3-symbol Turing machine plus a function stack. The style of multi-tape Turing machine used in TMD is the commonly used "one-tape-at-a-time" abstraction: only one tape at a time can be interacted with, for reading, writing, and moving the head. Laconic compiles down to a TMD program, and TMD compiles down to a description of a single-tape, 2-symbol Turing machine. This process is illustrated in Figure 1.

We recommend that programmers hoping to use our tools to generate their own encodings of mathematical statements or algorithms as Turing machines use Laconic. Laconic's interface is perfect for somebody hoping to write in a "traditional" language. On the other hand, if the programmer wishes to improve upon Laconic's compilation process, writing code directly in TMD is likely to be the better option.

2 Related Work

This paper is not the first to attempt to quantify the complexity of arithmetical statements. Calude and Calude [5] define a register machine of their own design, and provide quantifications of the complexity of Legendre's conjecture, Fermat's last theorem, Goldbach's conjecture, Dyson's conjecture,

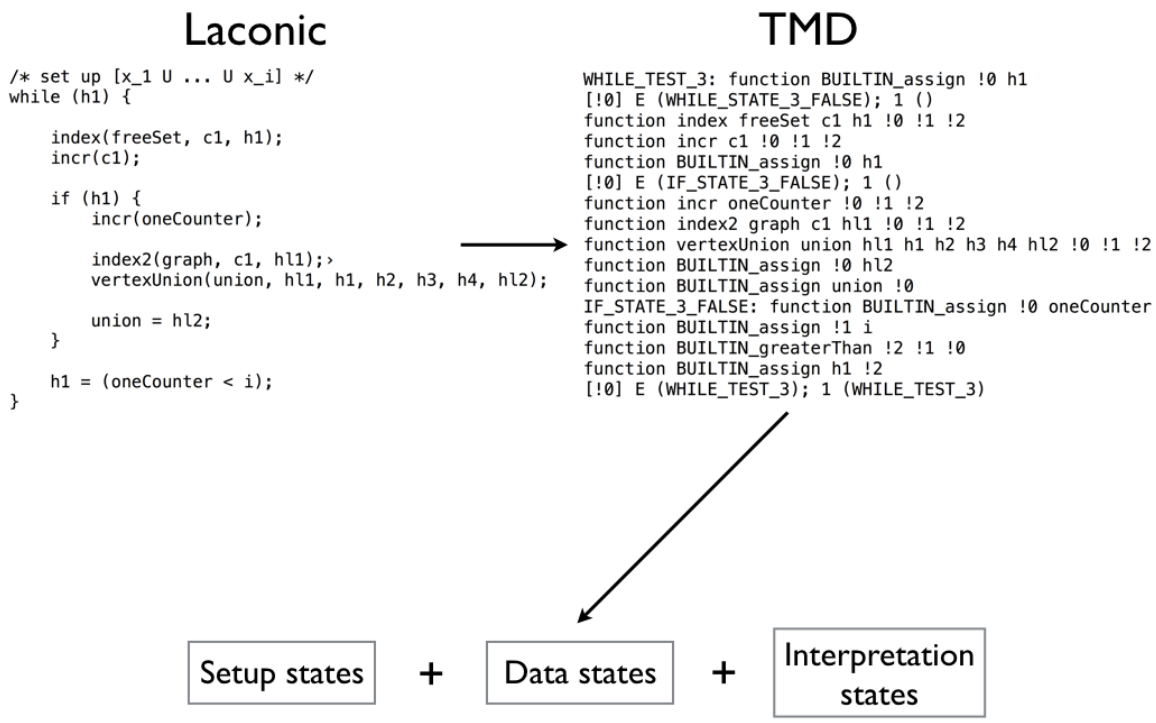

Figure 1: A visual overview of the compilation process.

the Riemann hypothesis, and the four color theorem.[1] In addition, Koza [9] and Pargellis [15] each invent instruction sets that are particularly well-suited to representing self-reproducing programs simply, and show that starting from a "primordial soup" of such instructions distributed about a large memory, along with an increasing number of program threads, a rich ecosystem of increasingly efficient self-reproducing programs start to dominate the "landscape."

This paper differs from the previous work in two ways: firstly, it is the first to give explicit, relatively small machines whose behavior is provably independent of the standard axioms of modern mathematics. Secondly, to our knowledge, this paper is the first concrete study of parsimony to use Turing machines as the model of computation—rather than (for example) a new programming language proposed by the authors! We consider it important to use the weakest and most common model of computation for complexity comparisons across different mathematical statements. This is because the more powerful and complex the model of computation used, the more of the complexity of the algorithm can be "shunted" onto the model of computation, and the greater the potential distortion created by the choice of model. As a reductio ad absurdum, we could imagine a programming language that included "test the Riemann hypothesis" and "test the consistency of ZFC" as primitive operations. By using the "weakest" model of computation that is commonly known, and one which is generally accepted as the mathematical basis of algorithms, we hope to avoid this pitfall and make it easier to interpret our results in a model-independent way.

3 A Turing Machine that Cannot Be Shown to Run Forever Using ZFC

We present a 8,013-state Turing machine whose behavior is *independent of ZFC*; it would not be possible to prove that this machine would halt or wouldn't halt using the axioms of ZFC, assuming a slightly stronger set theory is consistent. It is therefore impossible to prove the value of $BB(8{,}013)$ to be any given value without assuming axioms more powerful than ZFC, assuming that ZFC is consistent.

For an explicit listing of this machine, see Appendix C.

We call this machine Z. One way to build this machine would be to start with the axioms of ZFC and apply the inference rules of first-order logic repeatedly in each possible way so as to enumerate every statement ZFC could prove, and to halt if ever a contradiction was found. While this method is conceptually simple, to actually construct such a machine would lead to a huge number of states, because it would require writing a program to manipulate the axioms of ZFC and the inference rules of first-order logic, and then compiling that program all the way down to Turing machine states.

3.1 Friedman's Mathematical Statement

Thankfully, a simpler method exists for creating Z. Friedman [6] was able to derive a graph-theoretic statement whose truth implies the consistency of ZFC, and which will be false if ZFC is inconsistent.[2] Here is Friedman's statement (the notation will be explained in the rest of this

[1] Because Fermat's last theorem and the four color theorem have been proved, their complexity is now known to be 0.

[2] In fact, Friedman's statement is equivalent to the consistency of SRP ("stationary Ramsey property"), which is a system of axioms more powerful than ZFC. Because SRP is strictly more powerful than ZFC (it in fact consists of

section):

Statement 1. *For all $k, n, r > 0$, every order invariant graph on $[\mathbb{Q}]^{\leq k}$ has a free $\{x_1, \ldots, x_r,$ $ush(x_1), ..., ush(x_r)\}$ of complexity $\leq (8knr)!$, each $\{x_1, \ldots, x_{(8kni)!}\}$ reducing $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$. [6]*

A number of *complexity* at most c refers to a number that can be written as a fraction a/b, where a and b are both integers less than or equal to c. A set has complexity at most c if all the numbers it contains have complexity at most c.

An *order invariant graph* is a graph containing a countably infinite number of nodes. In particular, it has one node for each finite set of rational numbers. The only numbers relevant to the statement are numbers of complexity $(8knr)!$ or smaller. In every description of nodes that follows, the term *node* refers both to the object in the order invariant graph and to the set of numbers that it represents.

In an order invariant graph, two nodes (a, b) have an edge between them if and only if each other pair of nodes (c, d) that is *order equivalent* with (a, b) has an edge between them. Two pairs of nodes (a, b) and (c, d) are *order equivalent* if a and c are the same size and b and d are the same size and if for all $1 \leq i \leq |a|$ and $1 \leq j \leq |b|$, the i-th element of a is less than the j-th element of b if and only if the i-th element of c is less than the j-th element of d.

To give some trivial examples of order invariant graphs: the graph with no edges is order invariant, as is the complete graph. A less trivial example is a graph on $[\mathbb{Q}]^2$, in which each node corresponds to a set of two rational numbers of a given complexity, and there is an edge between two nodes if and only if their corresponding sets a and b satisfy $a_1 < b_1 < a_2 < b_2$. (Because edges are undirected in order invariant graphs, such an edge will exist if *either* assignment of the vertices to a and b satisfies the inequality above).

The *ush()* function takes as input a set and returns a copy of that set with all non-negative numbers in that set incremented by 1.

For vertices x and y, $x \leq_{rlex} y$ if and only if $x = y$ or $x_i < y_i$ where i is least such that $x_i \neq y_i$.

Finally, a set of vertices X *reduces* a set of vertices Y if and only if for all $y \in Y$, there exists $x \in X$ such that $x \leq_{rlex} y$ and an edge exists between x and y.

3.2 Implementation Methods

To create Z, we needed to design a Turing machine that halts if Statement 1 is false, and loops if Statement 1 is true. Such a Turing Machine's behavior would necessarily be independent of ZFC, because the truth or falsehood of Statement 1 is independent of ZFC, assuming the consistency of SRP, a slightly more powerful set theory. [6]

To design such a Turing machine, we wrote a Laconic program which encoded Friedman's statement, then compiled the program down to a description of a single-tape, 2-symbol Turing machine. What follows is an extremely brief description of the design of the Laconic program; for the documented Laconic code itself, along with a detailed explanation of the full compilation process, please see [18].

Our Laconic program begins by looping over all non-negative values for k, n, and r. For each trio (k, n, r), our program generates a list N of all numbers of complexity at most $(8knr)!$. These

ZFC plus some additional axioms), the consistency of SRP implies the consistency of ZFC, and the inconsistency of ZFC implies the inconsistency of SRP.

numbers represent the vertices in our putative order invariant graph. Because Laconic does not support floating-point numbers, the list is entirely composed of integers; it is a list of all numbers that can be written in the form $(((8knr)!)!)((8kni)!)/((8knj)!)$, where i and j are integers satisfying $-(8knr)! \leq i \leq (8knr)!$ and $1 \leq j \leq (8knr)!$. (Note that any number that can be expressed in this form is necessarily an integer, because of the large scaling factor in front.)

After we generate N, we generate the nodes in a potential order invariant graph by adding to N all possible lists of k or fewer numbers from N. We call this list of lists V.

We iterate over all binary lists of length $|V|^2$. Any such list E represents a possible set of edges in the graph. To be more precise, we say that an edge exists between node i and node j (represented by V_i and V_j respectively) if and only if $E_{i|V|+j}$ is 1.

For any graph (V, E), we say that it is "valid" if the following three conditions hold:

1. No node has an edge to itself.
2. If an edge exists between node i and node j, an edge also exists between node j and node i.
3. The graph has a free $\{x_1, \ldots, x_r, \mathrm{ush}(x_1), ..., \mathrm{ush}(x_r)\}$, each $\{x_1, \ldots, x_{(8kni)!}\}$ reducing $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$.

For each list of nodes V, we loop over every possible binary list E, and if no pair (V, E) yields a valid graph, we halt.

When verifying the validity of a graph, checking the first two conditions is trivial, but the third merits further explanation. In order to verify that a given graph (V, E) has a free $\{x_1, \ldots, x_r, ush(x_1), ..., ush(x_r)\}$, each $\{x_1, \ldots, x_{(8kni)!}\}$ reducing $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$, we look at every possible subset of the nodes in V. For each subset, we verify that it has length r, that $\mathrm{ush}(x_1), ..., \mathrm{ush}(x_r)$ all exist in V, and for each i such that $(8kni)! \leq r$, that $\{x_1, \ldots, x_{(8kni)!}\}$ reduces $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$. Once we have found such a subset, we know that the third conditon is satisfied.

4 A Turing Machine that Encodes Goldbach's Conjecture

We present a 4,888-state Turing machine that *encodes Goldbach's conjecture*; in other words, to know whether this machine halts is to know whether Goldbach's conjecture is true. It is therefore impossible to prove the value of $BB(4{,}888)$ without simultaneously proving or disproving Goldbach's conjecture.

Recall that Goldbach's conjecture is as follows:

Statement 2. Every even integer greater than 2 can be expressed as the sum of two primes.

Because Goldbach's conjecture is so simple to state, the Laconic program encoding the statement is also quite simple. It can be found in Appendix A. A detailed explanation of the compilation process, documentation for the Laconic language, and an explicit description of this Turing machine are available at [18].

5 A Turing Machine that Encodes Riemann's Hypothesis

We present a 5,372-state Turing machine that *encodes Riemann's hypothesis*; in other words, to know whether this machine halts is to know whether Riemann's hypothesis is true. An explicit description of this machine can be found at [18]

Riemann's hypothesis is traditionally stated as follows:

Statement 3. The Riemann zeta function has its zeros only at the negative even integers and the complex numbers with real part 1/2.

5.1 Equivalent Statement

Instead of encoding the Riemann zeta function into a Laconic program, it is simpler to use the following statement, which was shown by Lagarias to be equivalent to the Riemann hypothesis [4]:

Statement 4. *For all integers $n \geq 1$,*

$$\left(\left(\sum_{k \leq \delta(n)} \frac{1}{k}\right) - \frac{n^2}{2}\right)^2 < 36n^3$$

The function $\delta(n)$ used in Statement 4 is defined as follows:

$$\eta(j) = p \text{ if } j = p^k, \ p \text{ is prime, } k \text{ is a positive integer}$$
$$\eta(j) = 1 \text{ otherwise}$$
$$\delta(x) = \prod_{n<x} \prod_{j \leq n} \eta(j)$$

5.2 Implementation Methods

This statement is equivalent to the following statement, which contains only positive integers[3]:

$$l(n) < r(n)$$

for all positive integers n, where

$$l(n) = (a(n))^2 + (b(n))^2$$
$$r(n) = 36n^3(\delta(n)!)^2 + 2a(n)b(n)$$

.

$$a(n) = \sum_{k \leq \delta(n)} \frac{\delta(n)!}{k}$$

$$b(n) = n^2 \frac{\delta(n)!}{2}$$

To check the Riemann hypothesis, our program computes $a(n)$, $b(n)$, $l(n)$, and $r(n)$, in that order, for each possible value of n. If $l(n) \geq r(n)$, our program halts.

[3]Although it is not immediately obvious at first glance, $\frac{\delta(n)!}{k}$ is necessarily an integer for all $k \leq \delta(n)$, and $\frac{\delta(n)!}{2}$ is an integer for all $n > 1$.

6 Laconic

Laconic is a programming language designed to be both user-friendly and easy to compile down to parsimonious Turing machine descriptions.

Laconic is a strongly-typed language that supports recursive functions. Laconic compiles to an intermediate language called TMD. TMD programs are spread across multiple files and grouped into directories. TMD directories are meant to represent a sequence of commands that could be given to a multi-tape, 3-symbol Turing machine, using the Turing machine abstraction that allows the machine to read and write from one head at a time.

For an example of a Laconic program, see Appendix A. For a visual illustration of the compilation process, see Figure 1.

7 TMD

TMD is a programming language designed to help the user describe the behavior of a multi-tape, 3-symbol Turing machine with a function stack. Each tape is infinite in one direction and supports three symbols: _, `1`, and `E`. The blank symbol is _: that is, _ is the only symbol that can appear on the tape an infinite number of times. The tape must always have the form $\_?(1|E)^+\_^\infty$; in other words, each tape must always contain a string of `1`'s and `E`'s of size at least 1, possibly preceded by a _ symbol, and necessarily followed by an infinite number of copies of the _ symbol.

What is the purpose of having a language like TMD as an intermediary between Laconic and a description of a single-tape machine? The concept of tapes in a multi-tape Turing machine and the concept of variables in standard imperative programming languages map to one another very nicely. The idea of the Laconic-to-TMD compiler is to encode the value of each variable on one tape. Then, each Laconic command that manipulates the value of one or more variables compiles down to a TMD function call that manipulates the tapes that correspond to those variables appropriately.

As an example, consider the following Laconic command:

```
a=b*c;
```

This Laconic command assigns the value of `a` to the value of `b*c`. It compiles down to the following TMD function call:

```
function BUILTIN_multiply a b c
```

This function call will result in `BUILTIN_multiply` being run on the three tapes `a`, `b`, and `c`. This will cause the symbols on tape `a` to take on a representation of an integer whose value is equal to bc.

In turn, the TMD code compiles directly to a string of bits that are written onto the tape at the start of the Turing machine's execution.

A TMD directory consists of three types of files:

1. The `functions` file. This file contains a list of the names of all the functions used by the TMD program. The top function in the file is pushed onto the stack at initialization. Moreover, when this top function returns, the Turing machine halts.

2. The `initvar` file. This file contains the non-_ symbols that start in each register at initialization.

3. Any files used to describe TMD functions. These files all end in a `.tfn` extension and only have any relevance to the compiled program if they show up in the functions file.

8 Compilation and Processing

When discussing the layout of the tape symbols and patterns, there are two ways to think about it: one is with a 4-symbol alphabet ($\{\_, \texttt{1}, \texttt{H}, \texttt{E}\}$, blank symbol _), and one is with a 2-symbol alphabet ($\{\texttt{a}, \texttt{b}\}$, blank symbol `a`). The 2-symbol alphabet version is the one that's ultimately used for the results in this paper, since we advertised a Turing machine that used only two symbols. However, in nearly all parts of the Turing machine, the 2-symbol version of the machine is a direct translation of the 4-symbol version, according to the following mapping:

- _ $\leftrightarrow$ `aa`
- `1` $\leftrightarrow$ `ab`
- `H` $\leftrightarrow$ `ba`
- `E` $\leftrightarrow$ `bb`

Additionally, the sections that follow may make reference to the `ERROR` state. Transitions to the `ERROR` state are stand-ins for transitions that should never be taken under any circumstances and are useful for debugging purposes.

8.1 Concept

A directory of TMD functions is converted at compilation time to a string of bits to be written onto the tape, along with other states designed to interpret these bits. The resulting Turing machine has three main components, or *submachines*:

1. The *initializer* sets up the basic structure of the variable registers and the function stack.

2. The *printer* writes down the binary string that corresponds to the compiled TMD code.

3. The *processor* interprets the compiled binary, modifying the variable registers and the function stack as necessary.

The Turing machine's control flow proceeds from the initializer to the the printer to the interpreter. In other words, initializer states point only to initializer states or to printer states, printer states point only to printer states or to interpreter states, and interpreter states point only to interpreter states or the `HALT` state.

This division of labor, while seemingly straightforward, actually constitutes a very important and non-obvious idea. The problem of the compiler is to convert a higher-level representation—a machine with many tapes, a larger alphabet, and a function stack—to the lower-level representation of a machine with a single tape, a 2-symbol alphabet and no function stack. The immediately

obvious solution, and the one taught in every computability theory class as a proof of the equivalence of different kinds of Turing machines, is to have every "state" in the higher-level machine compile down to many states in the lower-level machine.

While simple, this approach is suboptimal in terms of the number of states. As is nearly always true when designing systems to be parsimonious, the clue that improvement is possible lies in the presence of repetition. Each state transition in the higher-level machine is converted to a group of lower-level states with the same basic structure. Why not instead explain how to perform this conversion exactly once, and then apply the conversion many times?

This idea is at the core of the division of labor described previously. We begin by writing a description of the higher-level machine onto the tape, and then "run" the higher-level machine by reading what is on the tape with a set of states that understands how to interpret the encoded higher-level machine. We refer to this idea as *on-tape processing*.

In this paper, we use TMD as the representation of the higher-level machine.[4] The printer writes the TMD program onto the tape, and the processor executes it. As a result of using this scheme, we incur a constant *additive* overhead—we have to include the processor in our final Turing machine—but we avoid the constant *multiplicative* overhead required for the naïve scheme.

Is this additive overhead small enough to be worth it? We found that it is. Our implementation of the processor requires 3,860 states. (See Section 8.5 for a detailed breakdown of the state cost by submachine.) In contrast to this additive overhead of 3,860, the naïve approach incurs a large multiplicative overhead that depends in part on how many states must be used to represent each higher-level state transition, and in part on how efficient an encoding scheme can be devised for the on-tape approach. The following table compares the performance of on-tape processing to the performance of an implementation that used the naïve approach. The comparison is shown for three kinds of machines: a machine that halts if and only if Goldbach's conjecture is false, a machine that halts if and only if the Riemann hypothesis is false, and a machine whose behavior is independent of ZFC.

Program	States (Naïve)	States (On-Tape Processing)
Goldbach	7,902	4,888
Riemann	36,146	5,372
ZFC	340,943	8,013

As can be seen from this table, on-tape interpretation results in huge gains, particularly in large and complex programs.

The subsections that follow describe each of the three submachines—the initializer, the printer, and the processor—in greater detail.

8.2 The Initializer

The initializer starts by writing a counter onto the tape which encodes how many registers there will be in the program. Using the value in that counter, it creates each register, with demarcation

[4]Note that instead of TMD, the on-tape processing scheme could be used for any language, assuming the designer provides both a processor and an encoding for that language. We chose TMD because it made the interpreter easy to write, but other minimalist languages, like Unlambda [11], Brainf*ck [14], or Iota and Jot [1], might be good candidates for parsimonious designs, with the additional advantage of being already known to some programmers! Thanks to Luke Schaeffer for this point.

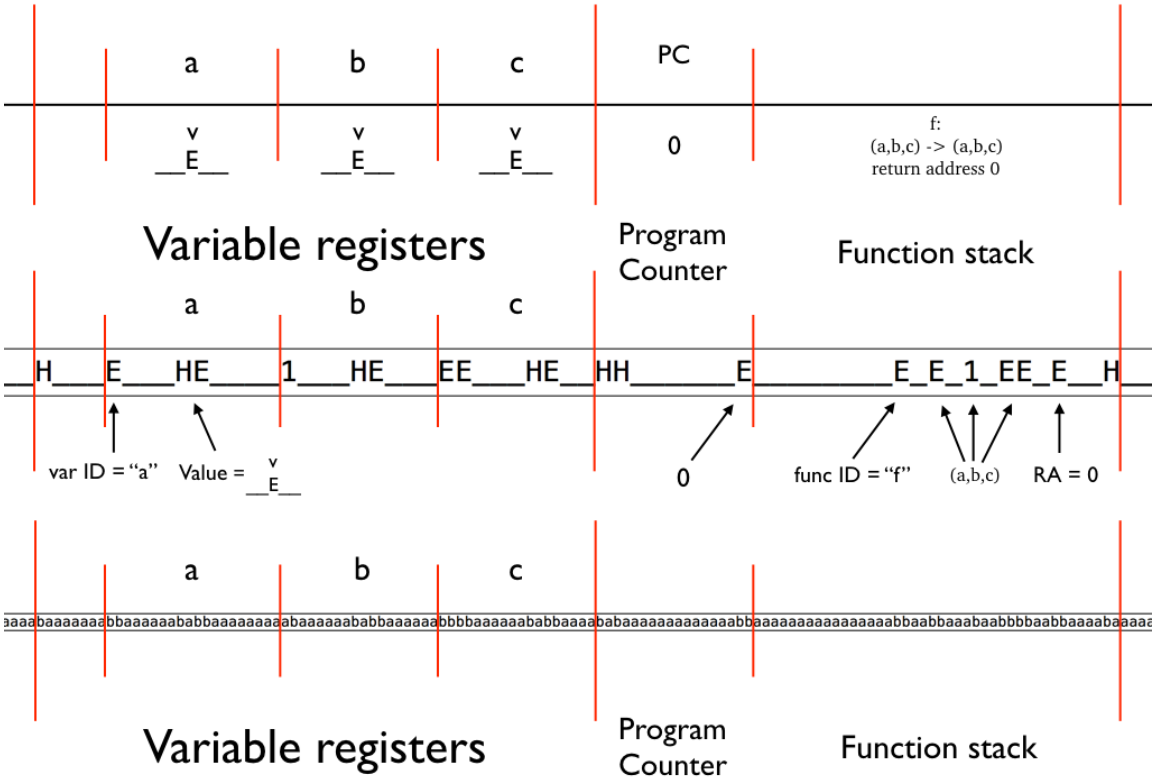

Figure 2: The state of the Turing machine tape after the initializer completes. The TMD program being expressed in Turing machine form is described in full in Appendix B. The top bar is a high-level description of what each part of the Turing machine tape represents. The middle bar is an encoding of the tape in the standard 4-symbol alphabet; the bottom bar is simply the translation of that tape into the 2-symbol alphabet. For a more detailed explanation of how to interpret the tape patterns, see [18].

patterns between registers, and unique identifiers for each register. Each register's value begins with the pattern of non-_ symbols laid out in the `initvar` file. The initializer also creates the program counter, which starts at 0, and the function stack, which starts out with only a single function call to the top function in the `functions` file.

Figure 2 is a detailed diagram describing the tape's state when the initializer passes control to the printer.

8.3 The Printer

8.3.1 Specification

The printer writes down a long binary string which encodes the entirety of the TMD program onto the tape.

Figure 3 is a detailed diagram describing the tape's state when the printer passes control to the processor.

8.3.2 Introspection

Writing down a long binary string onto a Turing machine tape in a parsimonious fashion is not as straightforward as it might initially appear. The first idea that comes to mind is simply to use one state per symbol, with each state pointing to the next, as shown in Figure 4.

Upon closer examination, however, it is apparent that this approach is quite wasteful for all but the smallest binary files. Every `a` transition points to the next state in the sequence, and none of the `b` transitions are used at all! Indeed, the only information-bearing part of the state is the single bit contained in the choice of which symbol to write. But in theory, far more information than that could be encoded with each state. In a machine that contains n states, each state could contain $2(\log(n) + 1)$ bits of information, because each of its two transitions could point to any of the n states, and write either an `a` or a `b` onto the tape. Of course, this is only in theory; in practice, to extract the information contained in the Turing machine's states and translate it into bits on the tape is nontrivial.

What we propose here is a scheme originally conceived by Ben-Amram and Petersen [2] and refined further and suggested to us by Luke Schaeffer. It does not achieve the optimal theoretical encoding described above, but is relatively simple to implement and understand, and is within a factor of 2 of optimal for large binary strings. Schaeffer named Turing machines that use this idea *introspective*.

Introspection works as follows. If the binary string contains k bits, then let w be the *word size*. w takes the largest value it can such that $w2^w \leq k$. We can split the binary string into $n_w = \left\lceil \frac{k}{w} \right\rceil$ different *words* of size w bits each (we can pad the last word with copies of the blank symbol). In our scheme, each word in the bit-string will be represented by a *data state*. Each data state points to the state representing the next word in the sequence for its `a` transition, but which state the `b` transition points will encode the next word. Every `b` transition points to one of the last 2^w data states, thereby encoding w bits of information.

Of course, the encoding is useless until we specify how to extract the encoded bit-string from the data states. The extraction scheme works as follows. To query the i^{th} data state for the bits it encodes, we run the data states on the string $\mathtt{a}^{i-1}\mathtt{ba}^{\infty}$ (a string of $i-1$ `a`'s followed by a `b` in the i^{th} position). After running the data states on that string, what remains on the tape is the string $\mathtt{b}^{i-1}\mathtt{ab}^{r}\mathtt{a}^{\infty}$, assuming that the i^{th} data state pointed to the r^{th}-to-last data state. Thus, what we are left with is essentially a unary encoding of the "value" of the word in binary. Thus, the job of the extractor is to set up a binary counter which removes one `b` at a time and increments the counter appropriately. Then, afterward, the extractor reverts the tape back to the form $\mathtt{a}^{i}\mathtt{ba}^{\infty}$, shifts all symbols on the tape over by w bits, and repeats the process. Finally, when the state beyond the last data state sees a `b` on the tape, we know that the process has completed, and we can pass control to the processor. Figure 5 depicts the introspection algorithm.

How much have we gained by using an introspective technique for encoding the program binary, instead of the naïve approach? It depends on how large the program binary is. By using introspection, we incur an $O(\log k)$ *additive* overhead, because we have to include the extractor in our machine. (Our implementation of the extractor takes $10w + 17$ states.) But in return, we save a *multiplicative* factor of w (which scales with $\log k$) on the number of data states needed.

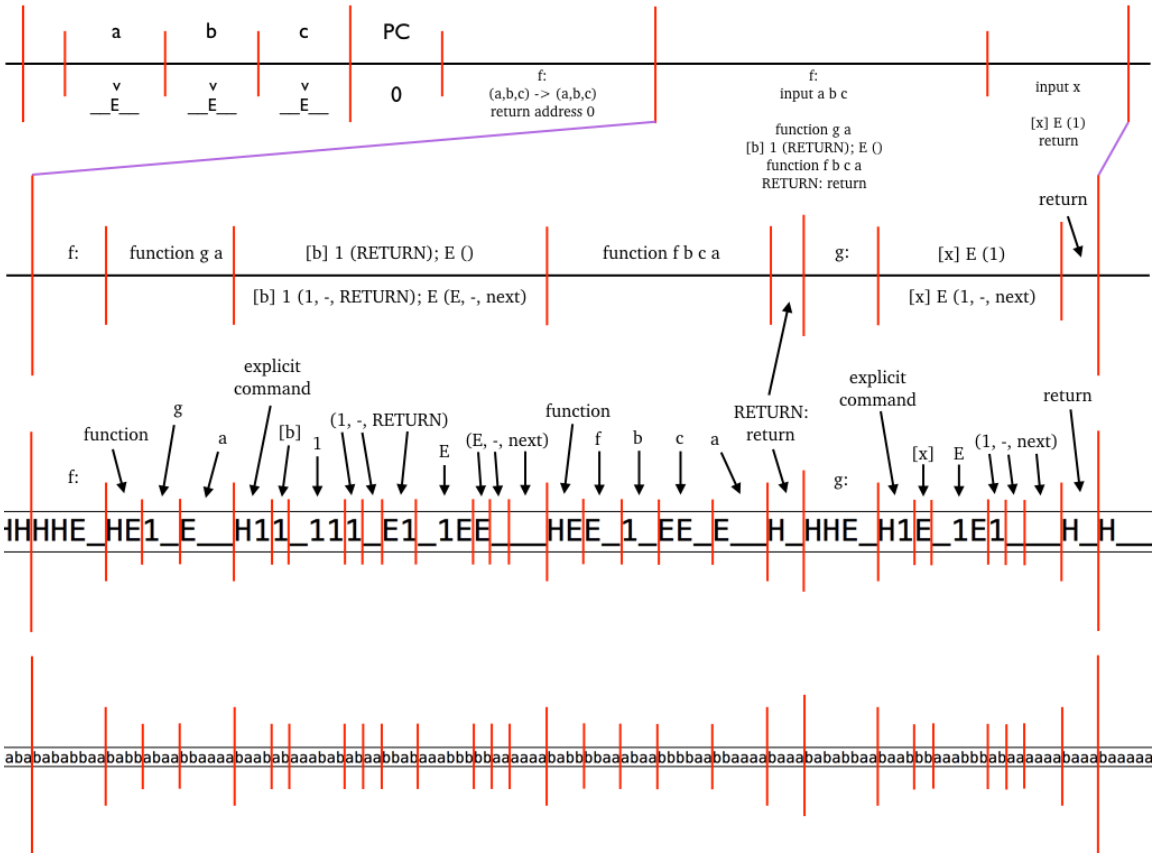

Figure 3: The state of the Turing machine tape after the printer completes. The TMD program being expressed in Turing machine form is described in full in Appendix B. The top bar is a high-level description of the entire tape; unfortunately, at this point there are so many symbols on the tape that it is impossible to see everything at once. For a detailed view of the first two-thirds of the tape (registers, program counter, and stack), see Figure 2. The bottom three bars show a zoomed-in view of the program binary. From the top, the second bar gives a high-level description of what each part of the program binary means; the third bar gives the direct correspondence between 4-symbol alphabet symbols on the tape and their meaning in TMD; the fourth and final bar gives the translation of the third bar into the 2-symbol alphabet. For a more detailed explanation of the encoding of TMD into tape symbols, see [18].

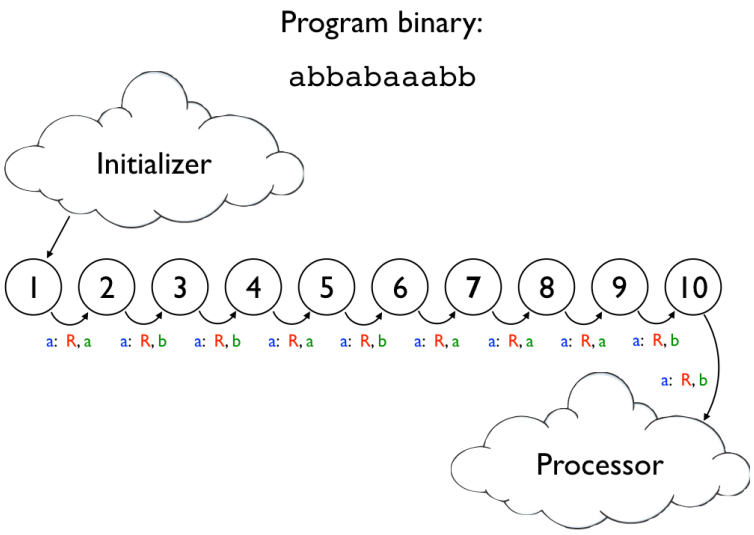

Figure 4: A naïve implementation of the printer. In this example, the hypothetical program is ten bits long, and the printer uses ten states, one for each bit. In the diagram, the blue symbol is the symbol that is read on a transition, the red letter indicates the direction the head moves, and the green symbol indicates the symbol that it written. Note the lack of transitions on reading a `b`; this is because in this implementation, the printer will only ever read the blank symbol, which is `a`, since the head is always proceeding to untouched parts of the tape. It therefore makes no difference what behavior the Turing machine adopts upon reading a `b` in states 1-10 (and therefore `b` transitions are presumed to lead to the `ERROR` state)

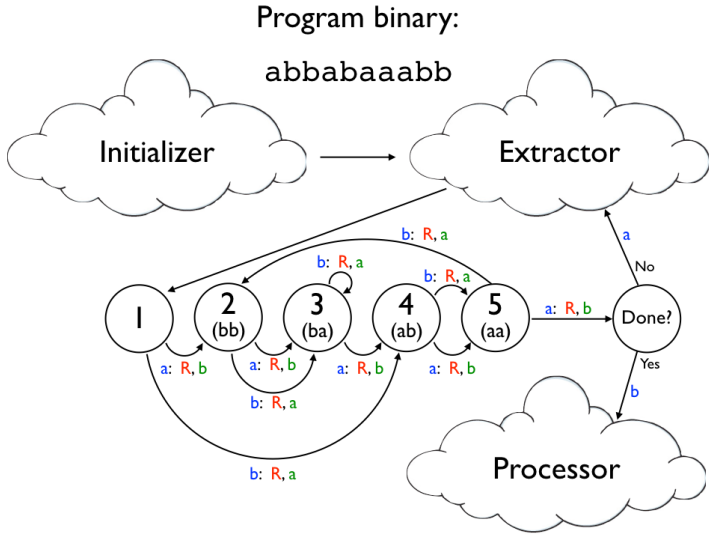

Figure 5: An introspective implementation of the printer. In this example, the hypothetical program is $k = 10$ bits long, and so the word size must be 2 (since $w = 2$ is the largest w such that $w2^w \leq 10$). There are therefore $n_w = \left\lceil \frac{k}{w} \right\rceil = 5$ data states, each encoding two bits. The `b` transitions carry the information about the encoding; note that each one only points to one of the last four data states. The last four data states have in parentheses what word we mean to encode if we point to them.

This is plainly not worth it for the 10-bit example binary shown in Figs. 4 and 5. For that binary, we require 69 additional states for the extractor in order to save 5 states on the data states. For real programs, however, it is worth it, as can be seen from the following table.

Program	Binary Size	w	n_w	Extractor Size	States (Naïve)	States (Introspective)
Example TMD	116	4	29	57	116	86
Goldbach	4,964	9	552	107	4,964	659
Riemann	9,532	10	1,024	117	9,532	1,141
ZFC	35,906	11	3,265	127	35,906	3,392

One minor detail concerns the numbers presented for the Riemann program. Ordinarily, with a binary of size 9,532, we would opt to split the program into 1,060 words of 9 bits each plus a 107-state extractor, since 9 is the greatest w such that $w2^w < 9{,}532$. But because 9,532 is so close to the "magic number" 10,240, it's actually more parsimonious to pad the program with copies of the blank symbol until it's 10,240 bits long, and split it into 1,024 words of 10 bits each plus a 117-state extractor.

8.4 The Processor

The processor's job is to interpret the code written onto the tape and modify the variable registers and function stack accordingly. The processor does this by the following sequence of steps:

START:

1. Find the function call at the top of the stack. Mark the function f in the code whose ID matches that of the top function call.
2. Read the current program counter. Mark the line of code l in f whose line number matches the program counter.
3. Read l. Depending on what type of command l is, carry out one of the following three lists of tasks.

IF l IS AN EXPLICIT TAPE COMMAND:

1. Read the variable name off l. Index the variable name into the list of variables in the top function on the stack. This list of variables corresponds to the mapping between the function's local variables and the register names.
2. Match the indexed variable to its corresponding register r. Mark r. Read the symbol s_r to the right of the head marker in that register.
3. Travel back to l, remembering the value of s_r using states. Find and mark the reaction x corresponding to the symbol. See what symbol s_w should be written in response to reading s_r.
4. Travel back to r, remembering the value of s_w using states. Replace s_r with s_w.

5. Travel back to x. See which direction d the head should move in response to reading s_r.

6. Travel back to r, remembering the value of d using states. Move the head marker accordingly.

7. Travel back to x. See if a jump is specified. If a jump is specified, copy the jump address onto the program counter. Otherwise, increment the program counter by 1.

8. Go back to START.

IF l IS A FUNCTION CALL:

1. Write the function's name to the top of the stack.

2. For each variable in the function call, index the variable name into the list of variables in the top function on the stack. This list of variables corresponds to the mapping between the function's local variables and the register names. Push the corresponding register names in the order that they correspond to the variables in the function call.

3. Copy the current program counter to the return address of the newborn function call at the top of the stack.

4. Replace the current program counter with 0 (meaning "read the first line of code").

5. Go back to START.

IF l IS A RETURN STATEMENT:

1. Replace the current program counter with f's return address.

2. Increment the program counter by 1.

3. Erase the call to f from the top of the stack.

4. Check if the stack is now empty. If so, halt.

5. Go back to START.

8.5 Cost Analysis

It is worthwhile to analyze the relative contributions of the initializer, the printer, and the processor to the machine's final state count. The following table lists the number of states in each submachine for each of the four different TMD programs under discussion.

Program	Initializer	Printer	Processor	Total
Example TMD	349	86	3,860	4,295
Goldbach	369	659	3,860	4,888
Riemann	371	1,141	3,860	5,372
ZFC	389	3,392	3,860	8,013

As can be seen from this table, the processor makes the largest contribution to all four programs. Improving the processor, therefore, is probably the best approach for improving upon the bounds we present. Equally clear, however, is that for programs more complicated than the ones presented here, the cost of the printer will grow almost linearly but the cost of the processor will stay the same. The cost of the initializer grows very slightly with the complexity of programs because of the need to initialize additional registers.

Improving the printer, and with it the TMD and Laconic languages, is probably the best approach for reducing state count for very large and complex programs.

9 Future Work

How much further can Z's state count be reduced? Without a significant new insight, further order-of-magnitude reductions seem unlikely via tweaks to our existing system: note that such tweaks would need to reduce the sizes of both the printer and the processor by an order of magnitude. However, improvement is possible by (for example) a redesigned processor, combined with a simpler independent mathematical statement than Friedman's.

Other future work might involve further use of our Laconic language to upper-bound the 'complexities' of mathematical statements and algorithms, in as standardized and model-independent a way as possible. Perhaps Laconic could be used to measure the complexity of other well-known conjectures, or even to compare different algorithms for solving the same problem to each other (e.g. to try to quantify the notion that an insertion sort is simpler than a merge sort)!

10 Acknowledgements

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We thank Luke Schaeffer for his early help, as well as his help designing introspective Turing machines.

We thank Alex Arkhipov for introducing us to the term "code golfing."

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[19] http://codegolf.stackexchange.com/ [A place where programmers go for recreational code golfing]

Appendices

A Example Laconic Program: Goldbach's Conjecture

The following is an example Laconic program, which compiles down to the Turing machine G mentioned in Section 4 (which halts if and only Goldbach's Conjecture is false).

```
func zero(x) {
    x = 0;
    return;
}

func one(x) {
    x = 1;
    return;
}

func incr(x) {
    x = x + 1;
    return;
}

/* Computes x modulo y */
func modulus(x, y, out) {
    out = x;

    while (out >= y) {
        out = out - y;
    }

    return;
}

func assignXtoYminusX(x, y) {
    x = y - x;
    return;
}

/* Figures out if x is prime, and puts the output in y */
/* Does not modify x, modifies y */
func isPrime(x, h, y) {
    if (x == 1) {
        zero(y);
        return;
    }

    y = 2;

    while (x > y) {
        modulus(x, y, h);

        if (h == 0) {
            zero(y);
            return;
        }
        incr(y);
    }

    return;
}

int evenNumber;
```

```
int primeCounter;
int isThisOnePrime;
int foundSum;
int h;

evenNumber = 2;
one(foundSum);

while (foundSum) {
    zero(foundSum);
    evenNumber = evenNumber + 2;
    one(primeCounter);

    while (primeCounter < evenNumber) {
        isPrime(primeCounter, h, isThisOnePrime);

        if (isThisOnePrime) {
            assignXtoYminusX(primeCounter, evenNumber);
            isPrime(primeCounter, h, isThisOnePrime);
            assignXtoYminusX(primeCounter, evenNumber);

            if (isThisOnePrime) {
                print evenNumber;
                print primeCounter;

                one(foundSum);
            }
        }

        incr(primeCounter);
    }
}

halt;
```

For detailed documentation of the Laconic programming language, see [18]. To find this file specifically, navigate to `parsimony/src/laconic/laconic_files/goldbach.lac` at [18].

B Example TMD Program

The following is an example TMD directory, which compiles down to a binary string to be written on a Turing machine tape. It is the example that is used in illustrations throughout this paper, most notably in the example compilation shown in Figs. 2 and 3. The program calls itself recursively three times until the starting symbol on each tape, `E`, is replaced with a `1`, at which point the program halts.

This TMD directory is called `example_tmd_dir`, and contains four files: `f.tmd`, `g.tmd`, `initvar`, and `functions`.

`f.tmd`:

```
input a b c

// Recursively writes a 1 on every tape.

function g a
[b] 1 (RETURN); E ()
function f b c a
RETURN: return
```

g.tmd:

```
input x

// Writes a 1 on the input tape.

[x] E (1)
return
```

functions:

```
f
g
```

initvar:

```
E
```

For detailed documentation of the TMD programming language, see [18]. To find this directory specifically, navigate to `parsimony/src/tmd/tmd_dirs/example_tmd_dir/` at [18].

C Explicit Listing of Z

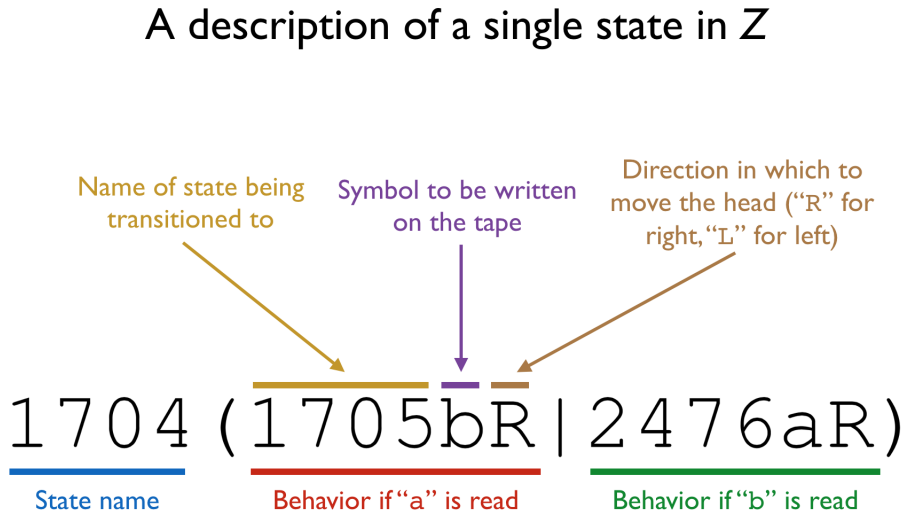


Figure 6: This figure explains how to read a description of a single state. Note that "`ERROR-`" or "`HALT--`" denote transitions to the `ERROR` or `HALT` states, respectively (no further information is provided because what symbol is written and which direction the head moves are at that point irrelevant).

We present below an explicit listing of Z. For a more easily readable version of Z, complete with descriptive state names, see [18].

We ran this Turing Machine for 10,000,000,000 steps (more than half a day on our simulators) and within that time it did not halt. We note, however, that it was designed for parsimony and not for efficiency, and that this "experiment" is of little consequence! We similarly designed and ran a Turing machine designed to test the conjecture that all perfect squares are less than 5, and it

ran for 2,895,083,899 steps (a couple hours on our simulators) before it found the counterexample 9 and halted.

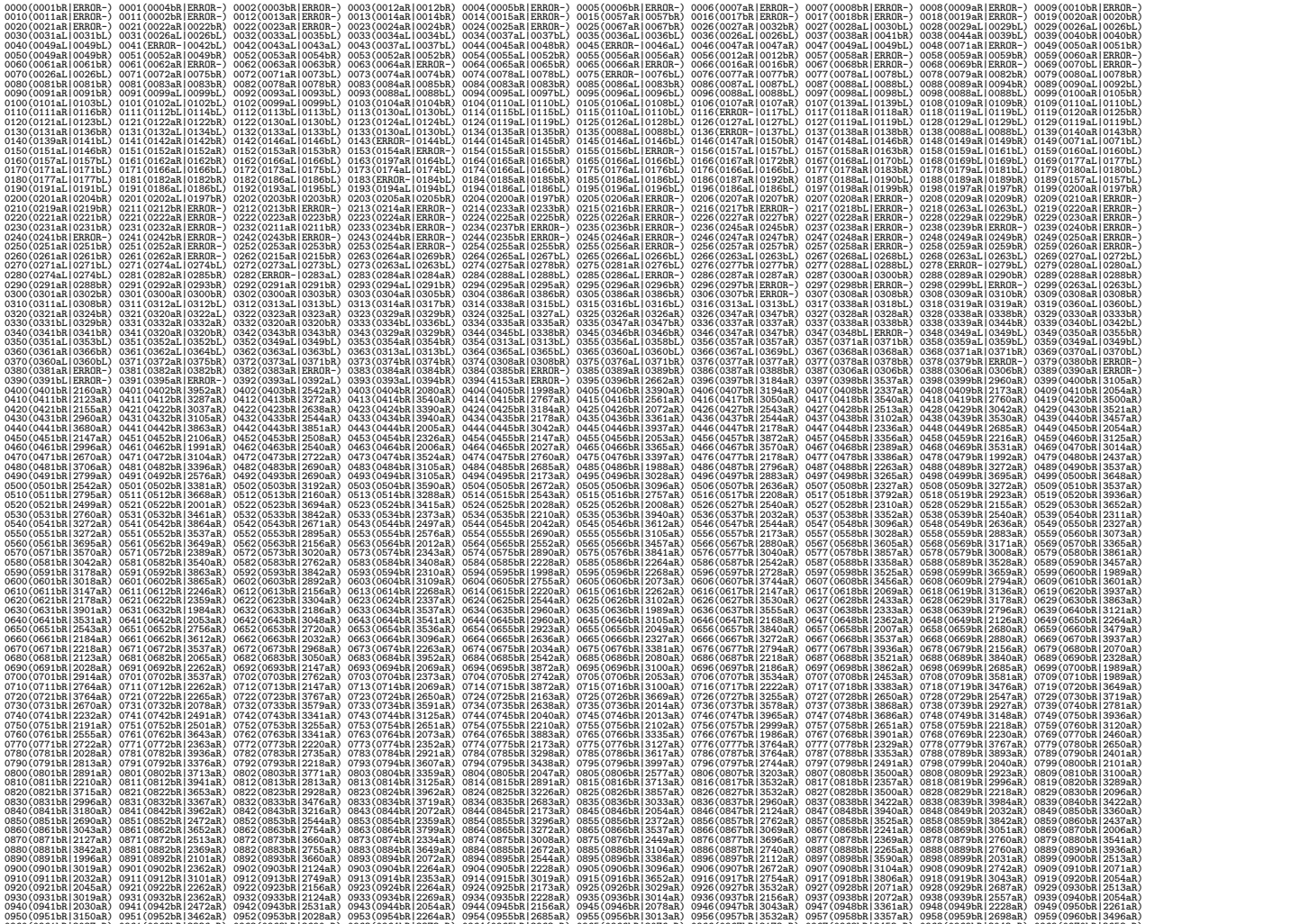
Figure 6 presents useful information for how to interpret the description shown below. In addition, note the following:

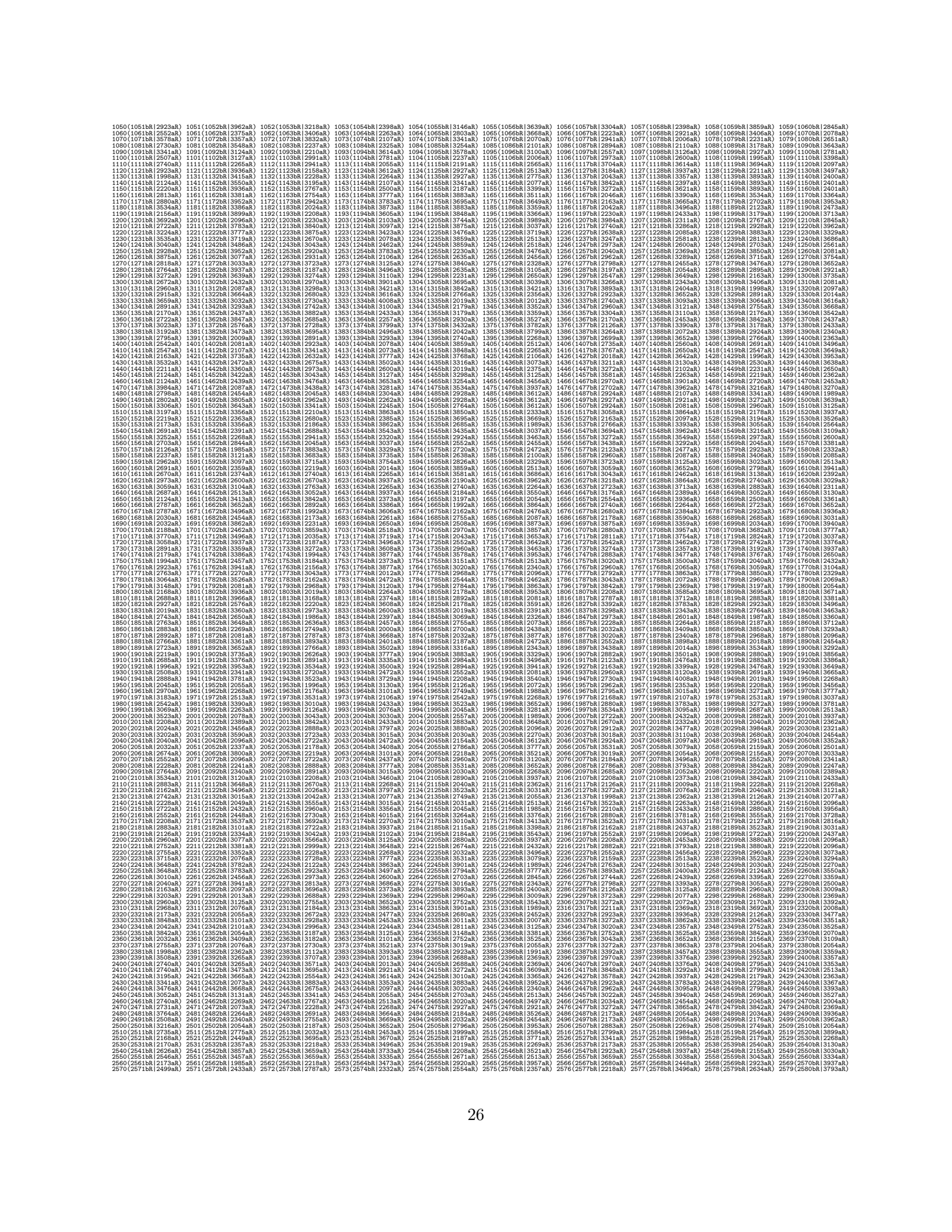
1. The tape has a 2-symbol alphabet, with tape symbols {a, b} and blank symbol a (in other words, a is the only symbol that can appear an infinite times on the tape).

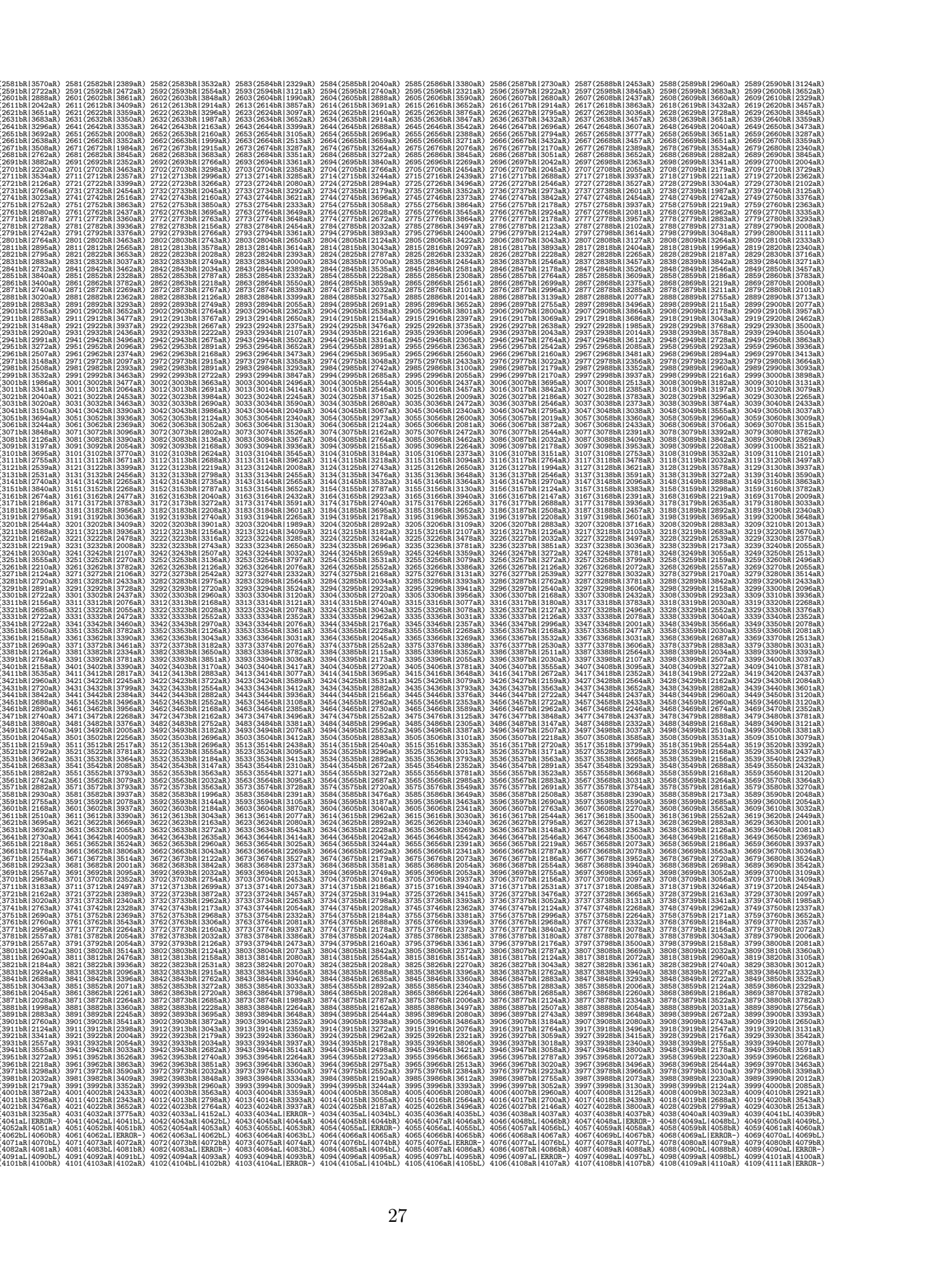
2. The start state of Z is 0000.

3. Z will never transition to the ERROR state. Any transition to the ERROR state could be replaced by a transition to any other state (including HALT) and the Turing machine's behavior would remain identical.

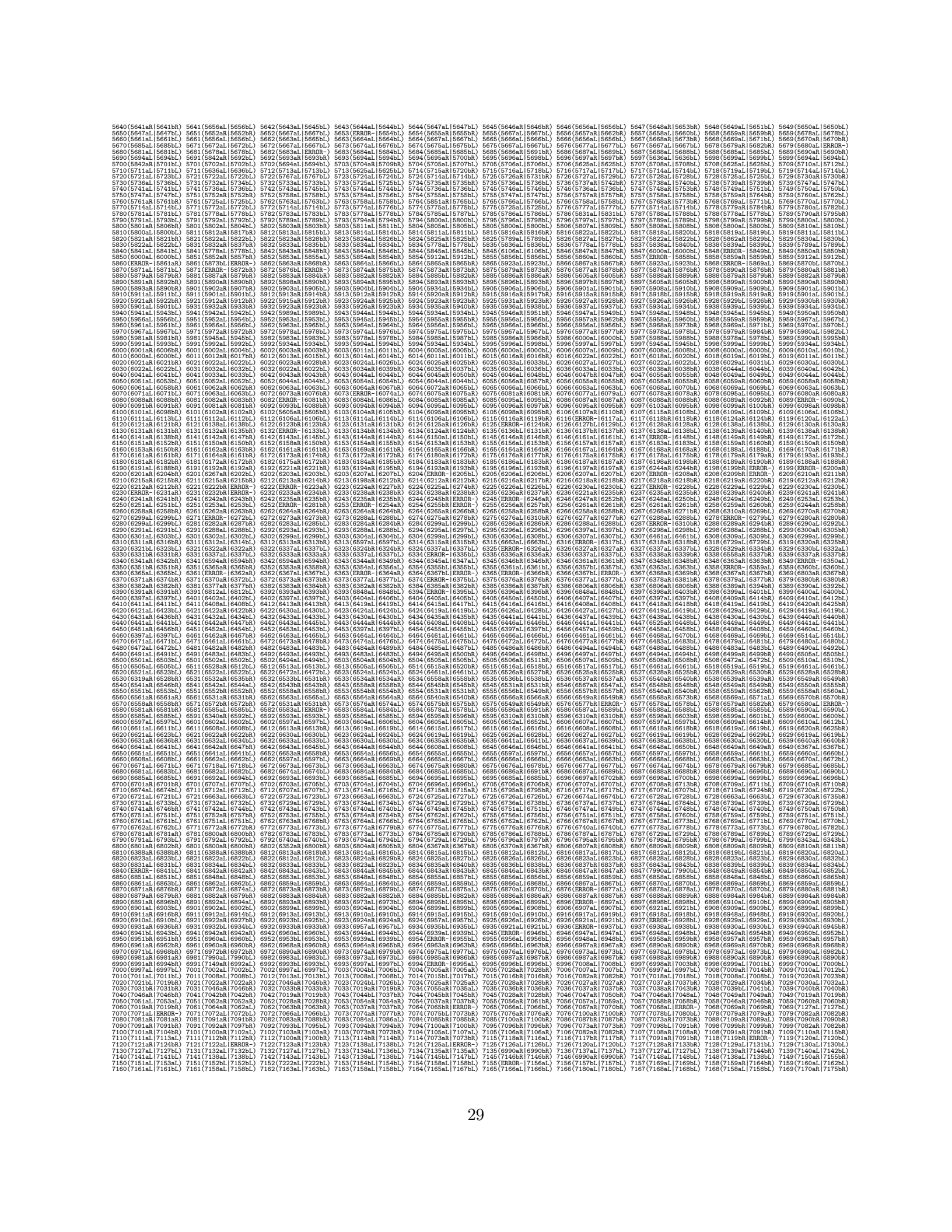
4. Z contains only one transition to the HALT state, out of state 7593.







4110(4112bR|4110bR)	4111(4112aL|ERROR-)	4112(4113aL|4112bR)	4113(4117aR|4114aL)	4114(4115aL|4114bL)	4115(4120aL|4115bL)	4116(4112aR|4116bR)	4117(4118bL|ERROR-)	4118(4119aL|ERROR-)	4119(0395aR|4119aL)
4120(4122bL|4121aL)	4121(4125bL|4124aL)	4122(4125aL|4125bL)	4123(4126aR|4126bR)	4124(4128bL|4127aL)	4125(4128aL|4128bL)	4126(4129aR|4129bR)	4127(4131bL|4130aL)	4128(4131aL|4131bL)	4129(4132aR|4132bR)
4130(4134bL|4133aL)	4131(4134aL|4134bL)	4132(4135aR|4135bR)	4133(4137bL|4136aL)	4134(4137aL|4137bL)	4135(4138aR|4138bR)	4136(4140bL|4139aL)	4137(4140aL|4140bL)	4138(4141aR|4141bR)	4139(4143bL|4142aL)
4140(4143aL|4143bL)	4141(4144aR|4144bR)	4142(4146bL|4145aL)	4143(4146aL|4146bL)	4144(4147aR|4147bR)	4145(4149bL|4148aL)	4146(4149aL|4149bL)	4147(4150aR|4150bR)	4148(4123bR|ERROR-)	4149(4123aR|4123bR)
4150(4151aR|4151bR)	4151(4116aR|ERROR-)	4152(0392aL|ERROR-)	4153(4154aR|ERROR-)	4154(4155aR|4155bR)	4155(4156bL|ERROR-)	4156(4157aL|4157bL)	4157(4158aR|4163bR)	4158(4159aL|4161bL)	4159(4160aL|4160bL)
4160(4157aL|4157bL)	4161(4162aL|4162bL)	4162(4157aL|4157bL)	4163(4164aL|4166bL)	4164(4165aL|4165bL)	4165(4168aL|4168bL)	4166(4167aL|4167bL)	4167(4157aL|4157bL)	4168(4169aR|4172bR)	4169(4202aR|4170bL)
4170(4171bL|4171bL)	4171(4168aL|4168bL)	4172(4240aR|4173bL)	4173(4174aL|4174aL)	4174(4157aL|4157bL)	4175(4176aR|4179bR)	4176(4175aR|4177aL)	4177(4178aR|4178aR)	4178(4184aR|4184bR)	4179(4180aL|4182aL)
4180(4181aR|4181aR)	4181(4211aR|4211bR)	4182(4183aR|4183aR)	4183(4202aR|4202bR)	4184(4185aR|4188bR)	4185(4186bL|4184bR)	4186(4187aR|4187aR)	4187(4175aR|4175bR)	4188(4189bL|4191bL)	4189(4190aR|4190aR)
4190(4193aR|4193bR)	4191(4192aR|4192aR)	4192(4202aR|4202bR)	4193(4194aR|4199bR)	4194(4195aL|4197aL)	4195(4196bR|4196bR)	4196(4175aR|4175bR)	4197(4198bR|4198bR)	4198(4184aR|4184bR)	4199(4193aR|4200aL)
4200(4201bR|4201bR)	4201(4202aR|4202bR)	4202(4203aR|4208bR)	4203(4204bL|4206bL)	4204(4205bR|4205bR)	4205(4175aR|4175bR)	4206(4207bR|4207bR)	4207(4184aR|4184bR)	4208(4209bL|4202bR)	4209(4210bR|4210bR)
4210(4193aR|4193bR)	4211(4212aR|4217bR)	4212(4213aL|4215aL)	4213(4214bL|4214bL)	4214(4220aL|4220bL)	4215(4216bR|4216bR)	4216(4184aR|4184bR)	4217(4193aR|4218aL)	4218(4219bR|4219bR)	4219(4202aR|4202bR)
4220(4221aR|4226bR)	4221(4222aL|4224bL)	4222(4223aL|4223bL)	4223(4220aL|4220bL)	4224(4225aL|4225bL)	4225(4220aL|4220bL)	4226(4227aL|4229bL)	4227(4228aL|4228bL)	4228(4231aL|4231bL)	4229(4230aL|4230bL)
4230(4220aL|4220bL)	4231(4232aR|4237bR)	4232(4233aL|4235bL)	4233(4234aR|4234bR)	4234(4168aL|4168bL)	4235(4236aR|4236bR)	4236(4168aL|4168bL)	4237(4240aR|4238bL)	4238(4239aL|4239bL)	4239(4220aL|4220bL)
4240(ERROR-|4241bR)	4241(4242aR|4242bR)	4242(4243aR|4244bR)	4243(4247aR|4242bR)	4244(4245aL|4242bR)	4245(4246aR|4246aR)	4246(4252aR|4252bR)	4247(4248aR|4249bR)	4248(4247aR|4242bR)	4249(4250aL|4242bR)
4250(4251aL|4251bL)	4251(4255aL|4255bL)	4252(4253aR|4254bR)	4253(4271aR|4252bR)	4254(4252aR|4252bR)	4255(4256aR|4261bR)	4256(4257aL|4259bL)	4257(4258aL|4258bL)	4258(4255aL|4255bL)	4259(4260aL|4260bL)
4260(4255aL|4255bL)	4261(4262aL|4264bL)	4262(4263aL|4263aL)	4263(4276aL|4276bL)	4264(4265aL|4265bL)	4265(4255aL|4255bL)	4266(4267aR|4270bR)	4267(4268aL|ERROR-)	4268(4269aR|4269bR)	4269(4157aL|4157bL)
4270(4287aR|ERROR-)	4271(4272aR|4273bR)	4272(4271aR|4252bR)	4273(4274aL|4252bR)	4274(4275aL|4275bL)	4275(4157aL|4157bL)	4276(4277aR|4282bR)	4277(4278aL|4280bL)	4278(4279aL|4279bL)	4279(4255aL|4255bL)
4280(4281aL|4281bL)	4281(4255aL|4255bL)	4282(4283aL|4285bL)	4283(4284aL|4284aL)	4284(4266aL|4266bL)	4285(4286aL|4286bL)	4286(4255aL|4255bL)	4287(4288aR|4289bR)	4288(4301aR|4292bR)	4289(4290aL|ERROR-)
4290(4291aL|4291bL)	4291(4329aL|4329bL)	4292(4293aR|4294bR)	4293(4295aR|4295bR)	4294(4295aR|4295bR)	4295(4296aR|4297bR)	4296(4298aR|4298bR)	4297(4298aR|4298bR)	4298(4299aR|4300bR)	4299(4314aR|4314bR)
4300(4314aR|4314bR)	4301(4302aR|4303bR)	4302(4327aR|4304bR)	4303(ERROR-|4304bR)	4304(4305aR|4306bR)	4305(4307aR|4304bR)	4306(4304aR|4304bR)	4307(4308aR|4311bR)	4308(4287aR|4309bL)	4309(4310aR|4310bR)
4310(4314aL|4314bL)	4311(ERROR-|4312bL)	4312(4313aR|4313bR)	4313(4317aL|4317bL)	4314(4315aR|4316bR)	4315(4287aR|4314bR)	4316(4314aR|4314bR)	4317(4318aR|4319bR)	4318(4320aR|4317bR)	4319(4317aR|4317bR)
4320(4321aR|4324bR)	4321(4301aR|4322bL)	4322(4323aR|4323bR)	4323(4317aL|4317bL)	4324(ERROR-|4325bL)	4325(4326aR|4326bR)	4326(4317aL|4317bL)	4327(ERROR-|4328bR)	4328(ERROR-|4287aR)	4329(4330aR|4335bR)
4330(4331aL|4333bL)	4331(4332aL|4332bL)	4332(4329aL|4329bL)	4333(4334aL|4334bL)	4334(4329aL|4329bL)	4335(4336aL|4338bL)	4336(4337aL|4337bL)	4337(4340aL|4340bL)	4338(4339aL|4339bL)	4339(4329aL|4329bL)
4340(4341aR|4344bR)	4341(4374aR|4342bL)	4342(4343bL|4343bL)	4343(4340aL|4340bL)	4344(4405aR|4345bL)	4345(4346aL|4346aL)	4346(4329aL|4329bL)	4347(4348aR|4351bR)	4348(4347aR|4349aL)	4349(4350aR|4350aR)
4350(4356aR|4356bR)	4351(4352aL|4354aL)	4352(4353aR|4353aR)	4353(4383aR|4383bR)	4354(4355aR|4355aR)	4355(4374aR|4374bR)	4356(4357aR|4360bR)	4357(4358bL|4356bR)	4358(4359aR|4359aR)	4359(4347aR|4347bR)
4360(4361bL|4363bL)	4361(4362aR|4362aR)	4362(4365aR|4365bR)	4363(4364aR|4364aR)	4364(4374aR|4374bR)	4365(4366aR|4371bR)	4366(4367aL|4369aL)	4367(4368bR|4368bR)	4368(4347aR|4347bR)	4369(4370bR|4370bR)
4370(4356aR|4356bR)	4371(4365aR|4372aL)	4372(4373bR|4373bR)	4373(4374aR|4374bR)	4374(4375aR|4380bR)	4375(4376bL|4378bL)	4376(4377bR|4377bR)	4377(4347aR|4347bR)	4378(4379bR|4379bR)	4379(4356aR|4356bR)
4380(4381bL|4374bR)	4381(4382bR|4382bR)	4382(4365aR|4365bR)	4383(4384bL|ERROR-)	4384(4385aL|4385bL)	4385(4386aR|4391bR)	4386(4387aL|4389bL)	4387(4388aL|4388bL)	4388(4385aL|4385bL)	4389(4390aL|4390bL)
4390(4385aL|4385bL)	4391(4392aL|4394bL)	4392(4393aL|4393bL)	4393(4396aL|4396bL)	4394(4395aL|4395bL)	4395(4385aL|4385bL)	4396(4397aR|4402bR)	4397(4398aL|4400bL)	4398(4399aR|4399bR)	4399(4340aL|4340bL)
4400(4401aR|4401bR)	4401(4340aL|4340bL)	4402(4405aR|4403bL)	4403(4404aR|4404bR)	4404(4385aL|4385bL)	4405(ERROR-|4406bR)	4406(4407aR|4407bR)	4407(4408aR|4409bR)	4408(4412aR|4407bR)	4409(4410aL|4407bR)
4410(4411aR|4411aR)	4411(4423aR|4423bR)	4412(4413aR|4418bR)	4413(4414aL|4416bL)	4414(4415aR|4415bR)	4415(4407aL|4407bL)	4416(4417aR|4417bR)	4417(4407aL|4407bL)	4418(4419aL|4421bL)	4419(4420aL|4420bL)
4420(4431aL|4431bL)	4421(4422aR|4422bR)	4422(4407aL|4407bL)	4423(4424aR|4425bR)	4424(4426aR|4423bR)	4425(4423aR|4423bR)	4426(4427aR|4428bR)	4427(4426aR|4423bR)	4428(4429aL|4423bR)	4429(4430aL|4430bL)
4430(4329aL|4329bL)	4431(4432aR|4437bR)	4432(4433aL|4435bL)	4433(4434aL|4434bL)	4434(4431aL|4431bL)	4435(4436aL|4436bL)	4436(4431aL|4431bL)	4437(4438aL|4440bL)	4438(4439aL|4439bL)	4439(4472aL|4472bL)
4440(4441aL|4441bL)	4441(4431aL|4431bL)	4442(4443aR|4444bR)	4443(4481aR|4481bR)	4444(4481aR|4481bR)	4445(4446bR|ERROR-)	4446(4447aR|4447bR)	4447(4448aR|4449bR)	4448(4447aR|4447bR)	4449(4450aR|4447bR)
4450(4451bR|ERROR-)	4451(4452aR|4452bR)	4452(4453bL|ERROR-)	4453(4454aL|4454bL)	4454(4455aR|4458bR)	4455(4456aL|ERROR-)	4456(4457aR|4457bR)	4457(4461aL|4461bL)	4458(4459aL|ERROR-)	4459(4460aL|4460bL)
4460(4454aL|4454bL)	4461(4462aR|4467bR)	4462(4463aL|4465bL)	4463(4464aL|4464bL)	4464(4461aL|4461bL)	4465(4466aL|4466bL)	4466(4461aL|4461bL)	4467(4468aL|4470bL)	4468(4469aL|4469bL)	4469(4484aL|4484bL)
4470(4471aL|4471bL)	4471(4461aL|4461bL)	4472(4473aR|4478bR)	4473(4474aL|4476bL)	4474(4475aL|4475bL)	4475(4431aL|4431bL)	4476(4477aL|4477bL)	4477(4431aL|4431bL)	4478(4442aR|4479bL)	4479(4480aL|4480bL)
4480(4431aL|4431bL)	4481(4482aR|4483bR)	4482(4445aR|4445bR)	4483(4445aR|4445bR)	4484(4485aR|4490bR)	4485(4486aL|4488bL)	4486(4487aL|4487bL)	4487(4484aL|4484bL)	4488(4489aR|4489bR)	4489(4495aL|4495bL)
4490(4491aL|4493bL)	4491(4492aR|4492aR)	4492(4484aL|4484bL)	4493(4494aR|4494bR)	4494(4495aL|4495bL)	4495(4496aR|4501bR)	4496(4497aL|4499bL)	4497(4498aL|4498bL)	4498(4508aL|4508bL)	4499(4500aL|4500bL)
4500(4495aL|4495bL)	4501(4502aL|4504bL)	4502(4503aL|4503bL)	4503(4495aL|4495bL)	4504(4505aL|4505bL)	4505(4495aL|4495bL)	4506(4507bR|ERROR-)	4507(4517aR|4517bR)	4508(4509aR|4512bR)	4509(4506aR|4510bL)
4510(4511aL|4511bL)	4511(4495aL|4495bL)	4512(4513aL|4515bL)	4513(4514aL|4514bL)	4514(4495aL|4495bL)	4515(4516aL|4516bL)	4516(4495aL|4495bL)	4517(4518aR|4523bR)	4518(4519aL|4521aL)	4519(4520aL|4520bL)
4520(4530aL|4530bL)	4521(4522bL|4522bL)	4522(4526aL|4526bL)	4523(ERROR-|4524aL)	4524(4525bL|4525bL)	4525(4528aL|4528bL)	4526(ERROR-|4527aR)	4527(4562bR|ERROR-)	4528(ERROR-|4529bR)	4529(4564bR|ERROR-)
4530(4531aR|4534bR)	4531(4539aR|4532bL)	4532(4533aL|4533bL)	4533(4530aL|4530bL)	4534(4535aL|4537bL)	4535(4536aL|4536bL)	4536(4530aL|4530bL)	4537(4538aL|4538bL)	4538(4530aL|4530bL)	4539(4540aR|4543bR)
4540(ERROR-|4541aL)	4541(4542bR|4542bR)	4542(4548aR|4548bR)	4543(4544aL|4546aL)	4544(4545aR|4545bR)	4545(4566aL|4566bL)	4546(4547bR|4547bR)	4547(4555aR|4555bR)	4548(4549aR|4550bR)	4549(ERROR-|4548bR)
4550(4551bL|4553bL)	4551(4552aR|4552aR)	4552(4566aL|4566bL)	4553(4554aR|4554aR)	4554(4555aR|4555bR)	4555(4556aR|4559bR)	4556(ERROR-|4557bL)	4557(4558bR|4558bR)	4558(4548aR|4548bR)	4559(4560bL|4555bR)
4560(4561bR|4561bR)	4561(4566aL|4566bL)	4562(ERROR-|4563bR)	4563(4569aR|4569bR)	4564(ERROR-|4565bR)	4565(4572aR|4572bR)	4566(4567aR|4568bR)	4567(4566aR|4566bR)	4568(4575aR|4566bR)	4569(4570aR|4571bR)
4570(4569aR|4569bR)	4571(4584aR|4569bR)	4572(4573aR|4574bR)	4573(4572aR|4572bR)	4574(4595aR|4572bR)	4575(4576aR|4581bR)	4576(4577aL|4579bL)	4577(4578aL|4578bL)	4578(4749aL|4749bL)	4579(4580aR|4580bR)
4580(4635aL|4635bL)	4581(ERROR-|4582bL)	4582(4583aR|4583bR)	4583(4635aL|4635bL)	4584(4585aR|4590bR)	4585(4586aL|4588aL)	4586(4587aR|4587bR)	4587(4635aL|4635bL)	4588(4589bL|4589bL)	4589(4593aL|4593bL)
4590(ERROR-|4591bL)	4591(4592aR|4592bR)	4592(4635aL|4635bL)	4593(ERROR-|4594aR)	4594(4606bL|ERROR-)	4595(4596aR|4601bR)	4596(4597aL|4599bL)	4597(4598aR|4598bR)	4598(4635aL|4635bL)	4599(4600aR|4600bR)
4600(4635aL|4635bL)	4601(ERROR-|4602aL)	4602(4603bL|4603bL)	4603(4604aL|4604bL)	4604(ERROR-|4605bR)	4605(4606bL|ERROR-)	4606(4607aR|4612bR)	4607(4608aL|4610bL)	4608(4609aL|4609bL)	4609(4606aL|4606bL)
4610(4611aL|4611bL)	4611(4606aL|4606bL)	4612(4517aR|4613bL)	4613(4614aL|4614bL)	4614(4606aL|4606bL)	4615(4616aR|ERROR-)	4616(4620aR|4617bR)	4617(4618aR|4619bR)	4618(4659aR|4659bR)	4619(4659aR|4659bR)
4620(4621aR|4624bR)	4621(4622aL|4625bR)	4622(4623bL|4623bL)	4623(4648aL|4648bL)	4624(ERROR-|4625bR)	4625(4626aR|4627bR)	4626(4628aR|4625bR)	4627(4625aR|4625bR)	4628(4629aR|4632bR)	4629(4615aR|4630bL)
4630(4631aR|4631bR)	4631(4635aL|4635bL)	4632(ERROR-|4633bL)	4633(4634aR|4634bR)	4634(4638aL|4638bL)	4635(4636aR|4637bR)	4636(4615aR|4635bR)	4637(4635aR|4635bR)	4638(4639aR|4640bR)	4639(4641aR|4638bR)
4640(4638aR|4638bR)	4641(4642aR|4645bR)	4642(4620aR|4643bL)	4643(4644aR|4644bR)	4644(4638aL|4638bL)	4645(ERROR-|4646bL)	4646(4647aR|4647bR)	4647(4638aL|4638bL)	4648(4649aR|4654bR)	4649(4650aL|4652bL)
4650(4651aL|4651bL)	4651(4648aL|4648bL)	4652(4653aL|4653bL)	4653(4648aL|4648bL)	4654(4655aL|4657bL)	4655(4656aR|4656bR)	4656(4665aL|4665bL)	4657(4658aL|4658bL)	4658(4648aL|4648bL)	4659(4660aR|4661bR)
4660(4662aR|4662bR)	4661(4662aR|4662bR)	4662(4663aR|4664bR)	4663(4635aR|4635bR)	4664(4635aR|4635bR)	4665(4666aR|4669bR)	4666(4674aR|4667bL)	4667(4668aL|4668bL)	4668(4665aL|4665bL)	4669(4670aL|4672bL)
4670(4671aL|4671bL)	4671(4665aL|4665bL)	4672(4673aL|4673bL)	4673(4665aL|4665bL)	4674(4675aR|4678bR)	4675(ERROR-|4676aL)	4676(4677aR|4677aR)	4677(4683aR|4683bR)	4678(4679aL|4681aL)	4679(4680aR|4680aR)
4680(4697aL|4697bL)	4681(4682aR|4682aR)	4682(4690aR|4690bR)	4683(4684aR|4685bR)	4684(ERROR-|4683bR)	4685(4686bL|4688bL)	4686(4687aR|4687aR)	4687(4697aL|4697bL)	4688(4689aR|4689aR)	4689(4690aR|4690bR)
4690(4691aR|4694bR)	4691(ERROR-|4692bL)	4692(4693bR|4693bR)	4693(4683aR|4683bR)	4694(4695bL|4690bR)	4695(4696bR|4696bR)	4696(4697aL|4697bL)	4697(4698aR|4703bR)	4698(4699aL|4701bL)	4699(4700aL|4700bL)
4700(4697aL|4697bL)	4701(4702aL|4702bL)	4702(4697aL|4697bL)	4703(4704aL|4706bL)	4704(4705aR|4705bR)	4705(4708aL|4708bL)	4706(4707aL|4707bL)	4707(4697aL|4697bL)	4708(4709aR|4712bR)	4709(4717aR|4710bL)
4710(4711aL|4711bL)	4711(4708aL|4708bL)	4712(4713aL|4715bL)	4713(4714aL|4714bL)	4714(4708aL|4708bL)	4715(4716aL|4716bL)	4716(4708aL|4708bL)	4717(4718aR|4721bR)	4718(ERROR-|4719aL)	4719(4720bR|4720bR)
4720(4726aR|4726bR)	4721(4722aL|4724aL)	4722(4723aR|4723bR)	4723(4740aL|4740bL)	4724(4725bR|4725bR)	4725(4733aR|4733bR)	4726(4727aR|4728bR)	4727(ERROR-|4726bR)	4728(4729bL|4731bL)	4729(4730aR|4730aR)
4730(4740aL|4740bL)	4731(4732aR|4732aR)	4732(4733aR|4733bR)	4733(4734aR|4737bR)	4734(ERROR-|4735bL)	4735(4736bR|4736bR)	4736(4726aR|4726bR)	4737(4738bL|4733bR)	4738(4739bR|4739bR)	4739(4740aL|4740bL)
4740(4741aR|4746bR)	4741(4742aL|4744bL)	4742(4743aL|4743bL)	4743(4740aL|4740bL)	4744(4745aL|4745bL)	4745(4740aL|4740bL)	4746(4517aR|4747bL)	4747(4748aL|4748bL)	4748(4740aL|4740bL)	4749(4750aR|4753bR)
4750(4758aR|4751bL)	4751(4752aL|4752bL)	4752(4749aL|4749bL)	4753(4754aL|4756bL)	4754(4755aL|4755bL)	4755(4749aL|4749bL)	4756(4757aL|4757bL)	4757(4749aL|4749bL)	4758(4759aR|4762bR)	4759(ERROR-|4760aL)
4760(4761bR|4761bR)	4761(4767aR|4767bR)	4762(4763aL|4765aL)	4763(4764aR|4764bR)	4764(4781aL|4781bL)	4765(4766bR|4766bR)	4766(4774aR|4774bR)	4767(4768aR|4769bR)	4768(ERROR-|4767bR)	4769(4770bL|4772bL)
4770(4771aR|4771aR)	4771(4781aL|4781bL)	4772(4773aR|4773aR)	4773(4774aR|4774bR)	4774(4775aR|4778bR)	4775(ERROR-|4776bL)	4776(4777bR|4777bR)	4777(4767aR|4767bR)	4778(4779bL|4774bR)	4779(4780bR|4780bR)
4780(4781aL|4781bL)	4781(4782aR|4787bR)	4782(4783aL|4785bL)	4783(4784aL|4784bL)	4784(4781aL|4781bL)	4785(4786aL|4786bL)	4786(4781aL|4781bL)	4787(4788aL|4790bL)	4788(4789aL|4789bL)	4789(4792aL|4792bL)
4790(4791aL|4791bL)	4791(4781aL|4781bL)	4792(4793aR|4798bR)	4793(4794aL|4796bL)	4794(4795aL|4795bL)	4795(4792aL|4792bL)	4796(4797aL|4797bL)	4797(4792aL|4792bL)	4798(4801aR|4799bL)	4799(4800aL|4800bL)
4800(4792aL|4792bL)	4801(4802aR|4805bR)	4802(4810aR|4803bL)	4803(4804aL|4804bL)	4804(4801aL|4801bL)	4805(4806aL|4808bL)	4806(4807aL|4807bL)	4807(4801aL|4801bL)	4808(4809aL|4809bL)	4809(4801aL|4801bL)
4810(4811aR|4814bR)	4811(ERROR-|4812aL)	4812(4813aR|4813aR)	4813(4819aR|4819bR)	4814(4815aL|4817aL)	4815(4816aR|4816aR)	4816(4833aL|4833bL)	4817(4818aR|4818aR)	4818(4826aR|4826bR)	4819(4820aR|4821bR)
4820(ERROR-|4819bR)	4821(4822bL|4824bL)	4822(4823aR|4823aR)	4823(4833aL|4833bL)	4824(4825aR|4825aR)	4825(4826aR|4826bR)	4826(4827aR|4830bR)	4827(ERROR-|4828bL)	4828(4829bR|4829bR)	4829(4819aR|4819bR)
4830(4831bL|4826bR)	4831(4832bR|4832bR)	4832(4833aL|4833bL)	4833(4834aR|4839bR)	4834(4835aL|4837bL)	4835(4836aL|4836bL)	4836(4883aL|4883bL)	4837(4838aL|4838bL)	4838(4833aL|4833bL)	4839(4840aL|4842bL)
4840(4841aL|4841bL)	4841(4833aL|4833bL)	4842(4843aL|4843bL)	4843(4833aL|4833bL)	4844(4845aR|4850bR)	4845(4846aL|4848bL)	4846(4847aL|4847bL)	4847(4844aL|4844bL)	4848(4849aR|4849bR)	4849(4853aL|4853bL)
4850(ERROR-|4851bL)	4851(4852aR|4852bR)	4852(4853aL|4853bL)	4853(4854aR|4859bR)	4854(4855aL|4857bL)	4855(4856bR|4856bR)	4856(4864aR|4864bR)	4857(4858aL|4858bL)	4858(4853aL|4853bL)	4859(4860aL|4862bL)
4860(4861aL|4861bL)	4861(4853aL|4853bL)	4862(4863aL|4863bL)	4863(4853aL|4853bL)	4864(4865aR|4866bR)	4865(4864aR|4864bR)	4866(4867aL|4864bR)	4867(4868aR|4868aR)	4868(4869aR|4869bR)	4869(4870aR|4871bR)
4870(4872aR|4869bR)	4871(4869aR|4869bR)	4872(4873bL|ERROR-)	4873(4874aL|4874bL)	4874(4875aR|4880bR)	4875(4876aL|4878bL)	4876(4877aL|4877bL)	4877(4874aL|4874bL)	4878(4879aL|4879bL)	4879(4874aL|4874bL)
4880(4905aR|4881bL)	4881(4882aL|4882bL)	4882(4874aL|4874bL)	4883(4884aR|4889bR)	4884(4885aL|4887bL)	4885(4886aL|4886bL)	4886(4894aL|4894bL)	4887(4888aL|4888bL)	4888(4833aL|4833bL)	4889(4890aL|4892bL)
4890(4891aL|4891bL)	4891(4833aL|4833bL)	4892(4893aL|4893bL)	4893(4833aL|4833bL)	4894(4895aR|4900bR)	4895(4896aL|4898bL)	4896(4897aR|4897bR)	4897(4844aL|4844bL)	4898(4899aL|4899bL)	4899(4833aL|4833bL)
4900(4901aL|4903bL)	4901(4902aL|4902bL)	4902(4833aL|4833bL)	4903(4904aL|4904bL)	4904(4833aL|4833bL)	4905(4906aR|4911bR)	4906(4907aL|4909aL)	4907(4908aL|4908bL)	4908(4918aL|4918bL)	4909(4910bL|4910bL)
4910(4914aL|4914bL)	4911(ERROR-|4912aL)	4912(4913bL|4913bL)	4913(4916aL|4916bL)	4914(ERROR-|4915aR)	4915(4950bR|ERROR-)	4916(ERROR-|4917bR)	4917(4952bR|ERROR-)	4918(4919aR|4922bR)	4919(4927aR|4920bL)
4920(4921aL|4921bL)	4921(4918aL|4918bL)	4922(4923aL|4925bL)	4923(4924aL|4924bL)	4924(4918aL|4918bL)	4925(4926aL|4926bL)	4926(4918aL|4918bL)	4927(4928aR|4931bR)	4928(ERROR-|4929aL)	4929(4930bR|4930bR)
4930(4936aR|4936bR)	4931(4932aL|4934aL)	4932(4933aR|4933bR)	4933(4954aL|4954bL)	4934(4935bR|4935bR)	4935(4943aR|4943bR)	4936(4937aR|4938bR)	4937(ERROR-|4936bR)	4938(4939bL|4941bL)	4939(4940aR|4940aR)
4940(4954aL|4954bL)	4941(4942aR|4942aR)	4942(4943aR|4943bR)	4943(4944aR|4947bR)	4944(ERROR-|4945bL)	4945(4946bR|4946bR)	4946(4936aR|4936bR)	4947(4948bL|4943bR)	4948(4949bR|4949bR)	4949(4954aL|4954bL)
4950(ERROR-|4951bR)	4951(4957aR|4957bR)	4952(ERROR-|4953bR)	4953(4960aR|4960bR)	4954(4955aR|4956bR)	4955(4954aR|4954bR)	4956(4963aR|4954bR)	4957(4958aR|4959bR)	4958(4957aR|4957bR)	4959(4972aR|4957bR)
4960(4961aR|4962bR)	4961(4960aR|4960bR)	4962(4983aR|4960bR)	4963(4964aR|4969bR)	4964(4965aL|4967bL)	4965(4966aL|4966bL)	4966(5136aL|5136bL)	4967(4968aR|4968bR)	4968(5003aL|5003bL)	4969(ERROR-|4970bL)
4970(4971aR|4971bR)	4971(5003aL|5003bL)	4972(4973aR|4978bR)	4973(4974aL|4976aL)	4974(4975aR|4975bR)	4975(5003aL|5003bL)	4976(4977bL|4977bL)	4977(4981aL|4981bL)	4978(ERROR-|4979bL)	4979(4980aR|4980bR)
4980(5003aL|5003bL)	4981(ERROR-|4982aR)	4982(4994bL|ERROR-)	4983(4984aR|4989bR)	4984(4985aL|4987bL)	4985(4986aR|4986bR)	4986(5003aL|5003bL)	4987(4988aR|4988bR)	4988(5003aL|5003bL)	4989(ERROR-|4990aL)
4990(4991bL|4991bL)	4991(4992aL|4992bL)	4992(ERROR-|4993bR)	4993(4994bL|ERROR-)	4994(4995aR|5000bR)	4995(4996aL|4998bL)	4996(4997aL|4997bL)	4997(4994aL|4994bL)	4998(4999aL|4999bL)	4999(4994aL|4994bL)
5000(4905aR|5001bL)	5001(5002aL|5002bL)	5002(4994aL|4994bL)	5003(5004aR|5005bR)	5004(5013aR|5003bR)	5005(5003aR|5003bR)	5006(5007aR|ERROR-)	5007(5008aL|5010bR)	5008(5009bL|5009bL)	5009(5035aL|5035bL)
5010(5011aR|5012bR)	5011(5046aR|5046bR)	5012(5046aR|5046bR)	5013(5014aR|5017bR)	5014(5006aR|5015bL)	5015(5016aR|5016bR)	5016(5020aL|5020bL)	5017(ERROR-|5018bL)	5018(5019aR|5019bR)	5019(5023aL|5023bL)
5020(5021aR|5022bR)	5021(5006aR|5020bR)	5022(5020aR|5020bR)	5023(5024aR|5025bR)	5024(5026aR|5023bR)	5025(5023aR|5023bR)	5026(5027aR|5032bR)	5027(5028aL|5030bL)	5028(5029bL|5029bL)	5029(5035aL|5035bL)
5030(5031aR|5031bR)	5031(5023aL|5023bL)	5032(ERROR-|5033bL)	5033(5034aR|5034bR)	5034(5023aL|5023bL)	5035(5036aR|5041bR)	5036(5037aL|5039bL)	5037(5038aL|5038bL)	5038(5035aL|5035bL)	5039(5040aL|5040bL)
5040(5035aL|5035bL)	5041(5042aL|5044bL)	5042(5043aR|5043bR)	5043(5052aL|5052bL)	5044(5045aL|5045bL)	5045(5035aL|5035bL)	5046(5047aR|5048bR)	5047(5049aR|5049bR)	5048(5049aR|5049bR)	5049(5050aR|5051bR)
5050(5020aR|5020bR)	5051(5020aR|5020bR)	5052(5053aR|5056bR)	5053(5061aR|5054bL)	5054(5055aL|5055bL)	5055(5052aL|5052bL)	5056(5057aL|5059bL)	5057(5058aL|5058bL)	5058(5052aL|5052bL)	5059(5060aL|5060bL)
5060(5052aL|5052bL)	5061(5062aR|5065bR)	5062(ERROR-|5063aL)	5063(5064aR|5064aR)	5064(5070aR|5070bR)	5065(5066aL|5068aL)	5066(5067aR|5067aR)	5067(5084aL|5084bL)	5068(5069aR|5069aR)	5069(5077aR|5077bR)
5070(5071aR|5072bR)	5071(ERROR-|5070bR)	5072(5073bL|5075bL)	5073(5074aR|5074aR)	5074(5084aL|5084bL)	5075(5076aR|5076aR)	5076(5077aR|5077bR)	5077(5078aR|5081bR)	5078(ERROR-|5079bL)	5079(5080bR|5080bR)
5080(5070aR|5070bR)	5081(5082bL|5077bR)	5082(5083bR|5083bR)	5083(5084aL|5084bL)	5084(5085aR|5090bR)	5085(5086aL|5088bL)	5086(5087aL|5087bL)	5087(5084aL|5084bL)	5088(5089aL|5089bL)	5089(5084aL|5084bL)
5090(5091aL|5093bL)	5091(5092aR|5092bR)	5092(5095aL|5095bL)	5093(5094aL|5094bL)	5094(5084aL|5084bL)	5095(5096aR|5099bR)	5096(5104aR|5097bL)	5097(5098aL|5098bL)	5098(5095aL|5095bL)	5099(5100aL|5102bL)
5100(5101aL|5101bL)	5101(5095aL|5095bL)	5102(5103aL|5103bL)	5103(5095aL|5095bL)	5104(5105aR|5108bR)	5105(ERROR-|5106aL)	5106(5107bR|5107bR)	5107(5113aR|5113bR)	5108(5109aL|5111aL)	5109(5110aR|5110bR)
5110(5127aL|5127bL)	5111(5112bR|5112bR)	5112(5120aR|5120bR)	5113(5114aR|5115bR)	5114(ERROR-|5113bR)	5115(5116bL|5118bL)	5116(5117aR|5117aR)	5117(5127aL|5127bL)	5118(5119aR|5119aR)	5119(5120aR|5120bR)
5120(5121aR|5124bR)	5121(ERROR-|5122bL)	5122(5123bR|5123bR)	5123(5113aR|5113bR)	5124(5125bL|5120bR)	5125(5126bR|5126bR)	5126(5127aL|5127bL)	5127(5128aR|5133bR)	5128(5129aL|5131bL)	5129(5130aL|5130bL)
5130(5127aL|5127bL)	5131(5132aL|5132bL)	5132(5127aL|5127bL)	5133(4905aR|5134bL)	5134(5135aL|5135bL)	5135(5127aL|5127bL)	5136(5137aR|5140bR)	5137(5145aR|5138bL)	5138(5139aL|5139bL)	5139(5136aL|5136bL)
5140(5141aL|5143bL)	5141(5142aL|5142bL)	5142(5136aL|5136bL)	5143(5144aL|5144bL)	5144(5136aL|5136bL)	5145(5146aR|5149bR)	5146(ERROR-|5147aL)	5147(5148bR|5148bR)	5148(5154aR|5154bR)	5149(5150aL|5152aL)
5150(5151aR|5151bR)	5151(5168aL|5168bL)	5152(5153bR|5153bR)	5153(5161aR|5161bR)	5154(5155aR|5156bR)	5155(ERROR-|5154bR)	5156(5157bL|5159bL)	5157(5158aR|5158aR)	5158(5168aL|5168bL)	5159(5160aR|5160aR)
5160(5161aR|5161bR)	5161(5162aR|5165bR)	5162(ERROR-|5163bL)	5163(5164bR|5164bR)	5164(5154aR|5154bR)	5165(5166bL|5161bR)	5166(5167bR|5167bR)	5167(5168aL|5168bL)	5168(5169aR|5174bR)	5169(5170aL|5172bL)
5170(5171aL|5171bL)	5171(5168aL|5168bL)	5172(5173aL|5173bL)	5173(5168aL|5168bL)	5174(5175aL|5177bL)	5175(5176aL|5176bL)	5176(5179aL|5179bL)	5177(5178aL|5178bL)	5178(5168aL|5168bL)	5179(5180aR|5185bR)
5180(5181aL|5183bL)	5181(5182aL|5182bL)	5182(5179aL|5179bL)	5183(5184aL|5184bL)	5184(5179aL|5179bL)	5185(5188aR|5186bL)	5186(5187aL|5187bL)	5187(5179aL|5179bL)	5188(5189aR|5192bR)	5189(5197aR|5190bL)
5190(5191aL|5191bL)	5191(5188aL|5188bL)	5192(5193aL|5195bL)	5193(5194aL|5194bL)	5194(5188aL|5188bL)	5195(5196aL|5196bL)	5196(5188aL|5188bL)	5197(5198aR|5201bR)	5198(ERROR-|5199aL)	5199(5200aR|5200aR)
5200(5206aR|5206bR)	5201(5202aL|5204aL)	5202(5203aR|5203aR)	5203(5220aL|5220bL)	5204(5205aR|5205aR)	5205(5213aR|5213bR)	5206(5207aR|5208bR)	5207(ERROR-|5206bR)	5208(5209bL|5211bL)	5209(5210aR|5210aR)
5210(5220aL|5220bL)	5211(5212aR|5212aR)	5212(5213aR|5213bR)	5213(5214aR|5217bR)	5214(ERROR-|5215bL)	5215(5216bR|5216bR)	5216(5206aR|5206bR)	5217(5218bL|5213bR)	5218(5219bR|5219bR)	5219(5220aL|5220bL)
5220(5221aR|5222bR)	5221(5223aR|5223bR)	5222(5223aR|5223bR)	5223(5224aR|5225bR)	5224(5226aR|5223bR)	5225(5223aR|5223bR)	5226(5227bR|ERROR-)	5227(5228aL|5228bL)	5228(5229aR|5230bR)	5229(5228aR|5228bR)
5230(5228aR|5231bL)	5231(5232aR|5232bR)	5232(5233aL|5233bL)	5233(5234aR|5235bR)	5234(5268aR|5233bR)	5235(5233aR|5233bR)	5236(5237aR|ERROR-)	5237(5303bR|ERROR-)	5238(5239aR|5244bR)	5239(5240aL|5242bL)
5240(5241aL|5241bL)	5241(5238aL|5238bL)	5242(5243aL|5243bL)	5243(5238aL|5238bL)	5244(5247aR|5245bL)	5245(5246aL|5246bL)	5246(5238aL|5238bL)	5247(5248aR|ERROR-)	5248(5249aL|5251aL)	5249(5250aR|5250bR)
5250(5253aL|5253bL)	5251(5252aR|5252aR)	5252(5303aL|5303bL)	5253(5254aR|5255bR)	5254(5253aR|5253bR)	5255(5256aL|5253bR)	5256(5257aR|5257aR)	5257(5258aR|5258bR)	5258(5259aR|5260bR)	5259(5261aR|5258bR)
5260(5258aR|5258bR)	5261(5262aR|5267bR)	5262(5263aL|5265aL)	5263(5264aR|5264bR)	5264(7802aL|7802bL)	5265(5266bL|5266bL)	5266(5341aL|5341bL)	5267(ERROR-|6990bR)	5268(5269aR|5270bR)	5269(5271aR|5233bR)
5270(5233aR|5233bR)	5271(5272aR|5275bR)	5272(5273aL|5233bR)	5273(5274bR|5274bR)	5274(5236aR|5236bR)	5275(5233aR|5233bR)	5276(5277aR|5280bR)	5277(5278bL|5276bR)	5278(5279aR|5279aR)	5279(5303aR|5303bR)
5280(5281bL|5283bL)	5281(5282aR|5282aR)	5282(5285aR|5285bR)	5283(5284aR|5284aR)	5284(5294aR|5294bR)	5285(5286aR|5291bR)	5286(5287aL|5289aL)	5287(5288bR|5288bR)	5288(5303aR|5303bR)	5289(5290bR|5290bR)
5290(5276aR|5276bR)	5291(5312aR|5292aL)	5292(5293bR|5293bR)	5293(5294aR|5294bR)	5294(5295aR|5300bR)	5295(5296bL|5298bL)	5296(5297bR|5297bR)	5297(5303aR|5303bR)	5298(5299bR|5299bR)	5299(5276aR|5276bR)
5300(5301bL|5294bR)	5301(5302bR|5302bR)	5302(5285aR|5285bR)	5303(5304aR|5307bR)	5304(5303aR|5305aL)	5305(5306aR|5306aR)	5306(5276aR|5276bR)	5307(5308aL|5310aL)	5308(5309aR|5309aR)	5309(5285aR|5285bR)
5310(5311aR|5311aR)	5311(5294aR|5294bR)	5312(5313aR|5318bR)	5313(5314aL|5316aL)	5314(5315bR|5315bR)	5315(5303aR|5303bR)	5316(5317bR|5317bR)	5317(5276aR|5276bR)	5318(5321aR|5319aL)	5319(5320bR|5320bR)
5320(5294aR|5294bR)	5321(5322bR|ERROR-)	5322(5323aL|5323bL)	5323(5324aR|5327bR)	5324(5325aL|ERROR-)	5325(5326aR|5326bR)	5326(5330aL|5330bL)	5327(5328aL|ERROR-)	5328(5329aL|5329bL)	5329(5323aL|5323bL)
5330(5331aR|5336bR)	5331(5332aL|5334bL)	5332(5333aL|5333bL)	5333(5330aL|5330bL)	5334(5335aL|5335bL)	5335(5330aL|5330bL)	5336(5337aL|5339bL)	5337(5338aL|5338bL)	5338(5238aL|5238bL)	5339(5340aL|5340bL)
5340(5330aL|5330bL)	5341(5342aR|5347bR)	5342(5343aL|5345bL)	5343(5344aL|5344bL)	5344(5341aL|5341bL)	5345(5346aL|5346bL)	5346(5341aL|5341bL)	5347(5348aL|5350bL)	5348(5349aR|5349aR)	5349(5352aL|5352bL)
5350(5351aL|5351bL)	5351(5341aL|5341bL)	5352(5353aR|5354bR)	5353(5352aR|5352bR)	5354(5355aR|5352bR)	5355(5356aR|5361bR)	5356(5357aL|5359aL)	5357(5358aL|5358bL)	5358(5395aL|5395bL)	5359(5360bL|5360bL)
5360(5364aL|5364bL)	5361(ERROR-|5362aL)	5362(5363bL|5363bL)	5363(5366aL|5366bL)	5364(ERROR-|5365aR)	5365(5370bL|ERROR-)	5366(ERROR-|5367bR)	5367(5381bL|ERROR-)	5368(5369bR|ERROR-)	5369(5392aR|5392bR)
5370(5371aR|5376bR)	5371(5372aL|5374bL)	5372(5373aL|5373bL)	5373(5370aL|5370bL)	5374(5375aL|5375bL)	5375(5370aL|5370bL)	5376(5377bL|5379bL)	5377(5378aR|5378aR)	5378(5368aR|5368bR)	5379(5380aL|5380bL)
5380(5370aL|5370bL)	5381(5382aR|5387bR)	5382(5383aL|5385bL)	5383(5384aL|5384bL)	5384(5381aL|5381bL)	5385(5386aL|5386bL)	5386(5381aL|5381bL)	5387(5388bL|5390bL)	5388(5389bR|5389bR)	5389(5368aR|5368bR)
5390(5391aL|5391bL)	5391(5381aL|5381bL)	5392(5393aR|5394bR)	5393(5392aR|5392bR)	5394(5355aR|5392bR)	5395(5396aR|5399bR)	5396(5404aR|5397bL)	5397(5398aL|5398bL)	5398(5395aL|5395bL)	5399(5400aL|5402bL)
5400(5401aL|5401bL)	5401(5395aL|5395bL)	5402(5403aL|5403bL)	5403(5395aL|5395bL)	5404(5405aR|5406bR)	5405(ERROR-|5411bR)	5406(5407bL|5409bL)	5407(5408aR|5408aR)	5408(5425aL|5425bL)	5409(5410aR|5410aR)
5410(5418aR|5418bR)	5411(5412aR|5413bR)	5412(ERROR-|5411bR)	5413(5414bL|5416bL)	5414(5415aR|5415aR)	5415(5425aL|5425bL)	5416(5417aR|5417aR)	5417(5418aR|5418bR)	5418(5419aR|5422bR)	5419(ERROR-|5420bL)
5420(5421bR|5421bR)	5421(5411aR|5411bR)	5422(5423bL|5418bR)	5423(5424bR|5424bR)	5424(5425aL|5425bL)	5425(5426aR|5429bR)	5426(5427aL|5425bR)	5427(5428bL|5428bL)	5428(5430aL|5430bL)	5429(5425aR|5425bR)
5430(5431aR|5436bR)	5431(5432aL|5434bL)	5432(5433aL|5433bL)	5433(5430aL|5430bL)	5434(5435aL|5435bL)	5435(5430aL|5430bL)	5436(5439aR|5437bL)	5437(5438aL|5438bL)	5438(5430aL|5430bL)	5439(5440aR|5441bR)
5440(5439aR|5439bR)	5441(5439aR|5442bL)	5442(5443aR|5443bR)	5443(5444aL|5444bL)	5444(5445aR|5446bR)	5445(5473aR|5444bR)	5446(5444aR|5444bR)	5447(5448aL|ERROR-)	5448(5449aL|5449bL)	5449(5450aR|5455bR)
5450(5451aL|5453bL)	5451(5452aL|5452bL)	5452(5481aL|5481bL)	5453(5454aL|5454bL)	5454(5449aL|5449bL)	5455(5456aL|5458bL)	5456(5457aL|5457bL)	5457(5449aL|5449bL)	5458(5459aL|5459bL)	5459(5449aL|5449bL)
5460(5461bL|ERROR-)	5461(5462aL|5462bL)	5462(5463aR|5468bR)	5463(5464aL|5466bL)	5464(5465aL|5465bL)	5465(5462aL|5462bL)	5466(5467aL|5467bL)	5467(5462aL|5462bL)	5468(5469aL|5471bL)	5469(5470aL|5470bL)
5470(5490aL|5490bL)	5471(5472aL|5472bL)	5472(5462aL|5462bL)	5473(5474aR|5475bR)	5474(5476aR|5444bR)	5475(5444aR|5444bR)	5476(5477aR|5480bR)	5477(5478aL|5444bR)	5478(5479aL|5479bL)	5479(5447aL|5447bL)
5480(5444aR|5444bR)	5481(5482aR|5485bR)	5482(5460aR|5483bL)	5483(5484aL|5484bL)	5484(5449aL|5449bL)	5485(5486aL|5488bL)	5486(5487aL|5487bL)	5487(5449aL|5449bL)	5488(5489aL|5489bL)	5489(5449aL|5449bL)
5490(5491aR|5494bR)	5491(5497aR|5492bL)	5492(5493bR|5493bR)	5493(5504aL|5504bL)	5494(ERROR-|5495bL)	5495(5496aL|5496aL)	5496(5490aL|5490bL)	5497(5498aR|5501bR)	5498(ERROR-|5499aL)	5499(5500aR|5500aR)
5500(5504aR|5504bR)	5501(5502aL|ERROR-)	5502(5503aR|5503aR)	5503(5528aR|5528bR)	5504(5505aR|5506bR)	5505(5504aR|5504bR)	5506(5507aR|5504bR)	5507(5508aR|5509bR)	5508(5507aR|5507bR)	5509(5510aL|5507bR)
5510(5511aR|5511aR)	5511(5512aR|5512bR)	5512(5513aR|5516bR)	5513(5514aL|5512bR)	5514(5515bL|5515bL)	5515(5517aL|5517bL)	5516(5512aR|5512bR)	5517(5518aR|5523bR)	5518(5519aL|5521bL)	5519(5520aL|5520bL)
5520(5517aL|5517bL)	5521(5522aL|5522bL)	5522(5517aL|5517bL)	5523(5524aL|5526bL)	5524(5525aL|5525bL)	5525(5490aL|5490bL)	5526(5527aL|5527bL)	5527(5517aL|5517bL)	5528(5529aR|5534bR)	5529(5530aL|5532bL)
5530(5531aL|5531bL)	5531(5528aL|5528bL)	5532(5533aL|5533bL)	5533(5528aL|5528bL)	5534(5535aL|5537bL)	5535(5536aL|5536bL)	5536(5591aL|5591bL)	5537(5538aL|5538bL)	5538(5528aL|5528bL)	5539(5540aR|5545bR)
5540(5541aL|5543bL)	5541(5542aL|5542bL)	5542(5539aL|5539bL)	5543(5544aR|5544bR)	5544(5550aL|5550bL)	5545(5546aL|5548bL)	5546(5547aL|5547bL)	5547(5539aL|5539bL)	5548(5549aR|5549bR)	5549(5550aL|5550bL)
5550(5551aR|5556bR)	5551(5552aL|5554bL)	5552(5553aR|5553bR)	5553(5561aL|5561bL)	5554(5555aL|5555bL)	5555(5550aL|5550bL)	5556(5557aL|5559bL)	5557(5558aL|5558bL)	5558(5550aL|5550bL)	5559(5560aL|5560bL)
5560(5550aL|5550bL)	5561(5562aR|5567bR)	5562(5563aL|5565bL)	5563(5564aL|5564bL)	5564(5561aL|5561bL)	5565(5566aR|5566bR)	5566(5572aL|5572bL)	5567(5568aL|5570bL)	5568(5569aL|5569bL)	5569(5561aL|5561bL)
5570(5571aR|5571bR)	5571(5572aL|5572bL)	5572(5573aR|5578bR)	5573(5574aL|5576bL)	5574(5575bR|5575bR)	5575(5583aL|5583bL)	5576(5577aL|5577bL)	5577(5572aL|5572bL)	5578(5579aL|5581bL)	5579(5580aL|5580bL)
5580(5572aL|5572bL)	5581(5582aL|5582bL)	5582(5572aL|5572bL)	5583(5584aR|5585bR)	5584(5583aR|5583bR)	5585(5602aR|5583bR)	5586(5587aR|5588bR)	5587(5586aR|5586bR)	5588(5589aL|5586bR)	5589(5590aR|5590aR)
5590(5605aR|5605bR)	5591(5592aR|5597bR)	5592(5593aL|5595bL)	5593(5594aL|5594bL)	5594(5528aL|5528bL)	5595(5596aL|5596bL)	5596(5528aL|5528bL)	5597(5598aL|5600bL)	5598(5599aL|5599bL)	5599(5539aL|5539bL)
5600(5601aL|5601bL)	5601(5528aL|5528bL)	5602(5603aR|5604bR)	5603(5583aR|5583bR)	5604(5586aR|5583bR)	5605(5606aR|5611bR)	5606(5607aL|5609aL)	5607(5608aL|5608bL)	5608(5614aL|5614bL)	5609(5610bL|5610bL)
5610(5714aL|5714bL)	5611(ERROR-|5612aL)	5612(5613bL|5613bL)	5613(5778aL|5778bL)	5614(5615aR|5620bR)	5615(5616aL|5618bL)	5616(5617bL|5617bL)	5617(5625aL|5625bL)	5618(5619aL|5619bL)	5619(5614aL|5614bL)
5620(5621aL|5623bL)	5621(5622aL|5622bL)	5622(5614aL|5614bL)	5623(5624aL|5624bL)	5624(5614aL|5614bL)	5625(5626aR|5631bR)	5626(5627aL|5629bL)	5627(5628aL|5628bL)	5628(5625aL|5625bL)	5629(5630aL|5630bL)
5630(5625aL|5625bL)	5631(5632aL|5634bL)	5632(5633aL|5633bL)	5633(5703aL|5703bL)	5634(5635aL|5635bL)	5635(5625aL|5625bL)	5636(5637aR|5642bR)	5637(5638aL|5640bL)	5638(5639aL|5639bL)	5639(5636aL|5636bL)



7170(7171aL|7173bL)	7171(7172aL|7172bL)	7172(7169aL|7169bL)	7173(7174aL|7174bL)	7174(7169aL|7169bL)	7175(7176aL|7178bL)	7176(7177aL|7177bL)	7177(7191aL|7191bL)	7178(7179aL|7179bL)	7179(7169aL|7169bL)
7180(7181aR|7186bR)	7181(7182aL|7184bL)	7182(7183aL|7183bL)	7183(7180aL|7180bL)	7184(7185aL|7185bL)	7185(7180aL|7180bL)	7186(7187bL|7189bL)	7187(7188aR|7188aR)	7188(7202aR|7202bR)	7189(7190aL|7190bL)
7190(7180aL|7180bL)	7191(7192aR|7197bR)	7192(7193aL|7195bL)	7193(7194aL|7194bL)	7194(7191aL|7191bL)	7195(7196aL|7196bL)	7196(7191aL|7191bL)	7197(7198bL|7200bL)	7198(7199bR|7199bR)	7199(7204aR|7204bR)
7200(7201aL|7201bL)	7201(7191aL|7191bL)	7202(7203bR|ERROR-)	7203(7206aR|7206bR)	7204(7205bR|ERROR-)	7205(7209aR|7209bR)	7206(7207aR|7208bR)	7207(7206aR|7206bR)	7208(7212aR|7206bR)	7209(7210aR|7211bR)
7210(7209aR|7209bR)	7211(7217aR|7209bR)	7212(7213aR|7214bR)	7213(7212aR|7212bR)	7214(7215bL|7212bR)	7215(7216aR|7216aR)	7216(7149aR|7149bR)	7217(7218aR|7219bR)	7218(7217aR|7217bR)	7219(7220bL|7217bR)
7220(7221bR|7221bR)	7221(7149aR|7149bR)	7222(7223aR|7228bR)	7223(7224aL|7226bL)	7224(7225aL|7225bL)	7225(7222aL|7222bL)	7226(7227aL|7227bL)	7227(7222aL|7222bL)	7228(7229aL|7231bL)	7229(7230aR|7230aR)
7230(7288aR|7288bR)	7231(7232aL|7232bL)	7232(7222aL|7222bL)	7233(7234aR|7239bR)	7234(7235aL|7237bL)	7235(7236aL|7236bL)	7236(7233aL|7233bL)	7237(7238aL|7238bL)	7238(7233aL|7233bL)	7239(7240aL|7242bL)
7240(7241aL|7241bL)	7241(7244aL|7244bL)	7242(7243aL|7243bL)	7243(7233aL|7233bL)	7244(7245aR|7250bR)	7245(7246aL|7248bL)	7246(7247aL|7247bL)	7247(7344aL|7344bL)	7248(7249aL|7249bL)	7249(7244aL|7244bL)
7250(7251aL|7253bL)	7251(7252aL|7252bL)	7252(7244aL|7244bL)	7253(7254aL|7254bL)	7254(7244aL|7244bL)	7255(7256aR|7261bR)	7256(7257aL|7259bL)	7257(7258aL|7258bL)	7258(7355aL|7355bL)	7259(7260aL|7260bL)
7260(7255aL|7255bL)	7261(7262aL|7264bL)	7262(7263aL|7263bL)	7263(7255aL|7255bL)	7264(7265aL|7265bL)	7265(7255aL|7255bL)	7266(7267bL|ERROR-)	7267(7268aL|7268bL)	7268(7269aR|7274bR)	7269(7270aL|7272bL)
7270(7271aL|7271bL)	7271(7268aL|7268bL)	7272(7273aL|7273bL)	7273(7268aL|7268bL)	7274(7275aL|7277bL)	7275(7276aL|7276bL)	7276(7364aL|7364bL)	7277(7278aL|7278bL)	7278(7268aL|7268bL)	7279(7280aR|7283bR)
7280(7281bL|7279bR)	7281(7282aR|7282aR)	7282(7306aR|7306bR)	7283(7284bL|7286bL)	7284(7285aR|7285aR)	7285(7288aR|7288bR)	7286(7287aR|7287aR)	7287(7297aR|7297bR)	7288(7289aR|7294bR)	7289(7290aL|7292aL)
7290(7291bR|7291bR)	7291(7306aR|7306bR)	7292(7293bR|7293bR)	7293(7279aR|7279bR)	7294(7315aR|7295aL)	7295(7296bR|7296bR)	7296(7297aR|7297bR)	7297(7298aR|7303bR)	7298(7299bL|7301bL)	7299(7300bR|7300bR)
7300(7306aR|7306bR)	7301(7302bR|7302bR)	7302(7279aR|7279bR)	7303(7304bL|7297bR)	7304(7305bR|7305bR)	7305(7288aR|7288bR)	7306(7307aR|7310bR)	7307(7306aR|7308aL)	7308(7309aR|7309aR)	7309(7279aR|7279bR)
7310(7311aL|7313aL)	7311(7312aR|7312aR)	7312(7288aR|7288bR)	7313(7314aR|7314aR)	7314(7297aR|7297bR)	7315(7316aR|7321bR)	7316(7317aL|7319aL)	7317(7318bR|7318bR)	7318(7306aR|7306bR)	7319(7320bR|7320bR)
7320(7279aR|7279bR)	7321(7324aR|7322aL)	7322(7323bR|7323bR)	7323(7297aR|7297bR)	7324(7325bR|ERROR-)	7325(7326aL|7326bL)	7326(7327aR|7330bR)	7327(7328aL|ERROR-)	7328(7329aR|7329bR)	7329(7333aL|7333bL)
7330(7331aL|ERROR-)	7331(7332aL|7332bL)	7332(7326aL|7326bL)	7333(7334aR|7339bR)	7334(7335aL|7337bL)	7335(7336aL|7336bL)	7336(7333aL|7333bL)	7337(7338aL|7338bL)	7338(7333aL|7333bL)	7339(7340aL|7342bL)
7340(7341aL|7341bL)	7341(7233aL|7233bL)	7342(7343aL|7343bL)	7343(7333aL|7333bL)	7344(7345aR|7350bR)	7345(7346aL|7348bL)	7346(7347aL|7347bL)	7347(7255aL|7255bL)	7348(7349aL|7349bL)	7349(7244aL|7244bL)
7350(7351aL|7353bL)	7351(7352aL|7352bL)	7352(7244aL|7244bL)	7353(7354aL|7354bL)	7354(7244aL|7244bL)	7355(7356aR|7359bR)	7356(7266aR|7357bL)	7357(7358aL|7358bL)	7358(7255aL|7255bL)	7359(7360aL|7362bL)
7360(7361aL|7361bL)	7361(7255aL|7255bL)	7362(7363aL|7363bL)	7363(7255aL|7255bL)	7364(7365aR|7368bR)	7365(7371aR|7366bL)	7366(7367bR|7367bR)	7367(7378aL|7378bL)	7368(ERROR-|7369bL)	7369(7370aL|7370aL)
7370(7364aL|7364bL)	7371(7372aR|7375bR)	7372(ERROR-|7373aL)	7373(7374aR|7374aR)	7374(7378aR|7378bR)	7375(7376aL|ERROR-)	7376(7377aR|7377aR)	7377(7402aR|7402bR)	7378(7379aR|7380bR)	7379(7378aR|7378bR)
7380(7381aR|7378bR)	7381(7382aR|7383bR)	7382(7381aR|7381bR)	7383(7384aL|7381bR)	7384(7385aR|7385aR)	7385(7386aR|7386bR)	7386(7387aR|7390bR)	7387(7388aL|7386bR)	7388(7389bL|7389bL)	7389(7391aL|7391bL)
7390(7386aR|7386bR)	7391(7392aR|7397bR)	7392(7393aL|7395bL)	7393(7394aL|7394bL)	7394(7391aL|7391bL)	7395(7396aL|7396bL)	7396(7391aL|7391bL)	7397(7398aL|7400bL)	7398(7399aL|7399bL)	7399(7364aL|7364bL)
7400(7401aL|7401bL)	7401(7391aL|7391bL)	7402(7403aR|7404bR)	7403(7402aR|7402bR)	7404(7405aL|7402bR)	7405(7406aR|7406aR)	7406(7407aR|7407bR)	7407(7408aR|7413bR)	7408(7409aL|7411aL)	7409(7410aR|7410bR)
7410(7600aL|7600bL)	7411(7412bR|7412bR)	7412(7416aR|7416bR)	7413(ERROR-|7414aL)	7414(7415bR|7415bR)	7415(7421aR|7421bR)	7416(7417aR|7418bR)	7417(7416aR|7416bR)	7418(7419bL|7416bR)	7419(7420aR|7420aR)
7420(7479aR|7479bR)	7421(7422aR|7423bR)	7422(7421aR|7421bR)	7423(7424bL|7421bR)	7424(7425bR|7425bR)	7425(7533aR|7533bR)	7426(7427aR|7432bR)	7427(7428aL|7430bL)	7428(7429aL|7429bL)	7429(7426aL|7426bL)
7430(7431aL|7431bL)	7431(7426aL|7426bL)	7432(7433aL|7435bL)	7433(7434aL|7434bL)	7434(7448aL|7448bL)	7435(7436aL|7436bL)	7436(7426aL|7426bL)	7437(7438aR|7443bR)	7438(7439aL|7441bL)	7439(7440aL|7440bL)
7440(7437aL|7437bL)	7441(7442aL|7442bL)	7442(7437aL|7437bL)	7443(7444aL|7446bL)	7444(7445aL|7445bL)	7445(7459aL|7459bL)	7446(7447aL|7447bL)	7447(7437aL|7437bL)	7448(7449aR|7454bR)	7449(7450aL|7452bL)
7450(7451aL|7451bL)	7451(7448aL|7448bL)	7452(7453aL|7453bL)	7453(7448aL|7448bL)	7454(7455bL|7457bL)	7455(7456aR|7456aR)	7456(7407aR|7407bR)	7457(7458aL|7458bL)	7458(7448aL|7448bL)	7459(7460aR|7465bR)
7460(7461aL|7463bL)	7461(7462aL|7462bL)	7462(7459aL|7459bL)	7463(7464aL|7464bL)	7464(7459aL|7459bL)	7465(7466bL|7468bL)	7466(7467bR|7467bR)	7467(7407aR|7407bR)	7468(7469aL|7469bL)	7469(7459aL|7459bL)
7470(7471aR|7474bR)	7471(7472bL|7470bR)	7472(7473aR|7473aR)	7473(7497aR|7497bR)	7474(7475bL|7477bL)	7475(7476aR|7476aR)	7476(7479aR|7479bR)	7477(7478aR|7478aR)	7478(7488aR|7488bR)	7479(7480aR|7485bR)
7480(7481aL|7483aL)	7481(7482bR|7482bR)	7482(7497aR|7497bR)	7483(7484bR|7484bR)	7484(7470aR|7470bR)	7485(7506aR|7486aL)	7486(7487bR|7487bR)	7487(7488aR|7488bR)	7488(7489aR|7494bR)	7489(7490bL|7492bL)
7490(7491bR|7491bR)	7491(7497aR|7497bR)	7492(7493bR|7493bR)	7493(7470aR|7470bR)	7494(7495bL|7488bR)	7495(7496bR|7496bR)	7496(7479aR|7479bR)	7497(7498aR|7501bR)	7498(7497aR|7499aL)	7499(7500aR|7500aR)
7500(7470aR|7470bR)	7501(7502aL|7504aL)	7502(7503aR|7503aR)	7503(7479aR|7479bR)	7504(7505aR|7505aR)	7505(7488aR|7488bR)	7506(7507aR|7512bR)	7507(7508aL|7510aL)	7508(7509bR|7509bR)	7509(7497aR|7497bR)
7510(7511bR|7511bR)	7511(7470aR|7470bR)	7512(7515aR|7513aL)	7513(7514bR|7514bR)	7514(7488aR|7488bR)	7515(7516bR|ERROR-)	7516(7517aL|7517bL)	7517(7518aR|7521bR)	7518(7519aL|ERROR-)	7519(7520aR|7520bR)
7520(7578aL|7578bL)	7521(7522aL|ERROR-)	7522(7523aL|7523bL)	7523(7517aL|7517bL)	7524(7525aR|7528bR)	7525(7526bL|7524bR)	7526(7527aR|7527aR)	7527(7551aR|7551bR)	7528(7529bL|7531bL)	7529(7530aR|7530aR)
7530(7533aR|7533bR)	7531(7532aR|7532aR)	7532(7542aR|7542bR)	7533(7534aR|7539bR)	7534(7535aL|7537aL)	7535(7536bR|7536bR)	7536(7551aR|7551bR)	7537(7538bR|7538bR)	7538(7524aR|7524bR)	7539(7560aR|7540aL)
7540(7541bR|7541bR)	7541(7542aR|7542bR)	7542(7543aR|7548bR)	7543(7544bL|7546bL)	7544(7545bR|7545bR)	7545(7551aR|7551bR)	7546(7547bR|7547bR)	7547(7524aR|7524bR)	7548(7549bL|7542bR)	7549(7550bR|7550bR)
7550(7533aR|7533bR)	7551(7552aR|7555bR)	7552(7551aR|7553aL)	7553(7554aR|7554aR)	7554(7524aR|7524bR)	7555(7556aL|7558aL)	7556(7557aR|7557aR)	7557(7533aR|7533bR)	7558(7559aR|7559aR)	7559(7542aR|7542bR)
7560(7561aR|7566bR)	7561(7562aL|7564aL)	7562(7563bR|7563bR)	7563(7551aR|7551bR)	7564(7565bR|7565bR)	7565(7524aR|7524bR)	7566(7569aR|7567aL)	7567(7568bR|7568bR)	7568(7542aR|7542bR)	7569(7570bR|ERROR-)
7570(7571aL|7571bL)	7571(7572aR|7575bR)	7572(7573aL|ERROR-)	7573(7574aR|7574bR)	7574(7589aL|7589bL)	7575(7576aL|ERROR-)	7576(7577aL|7577bL)	7577(7571aL|7571bL)	7578(7579aR|7584bR)	7579(7580aL|7582bL)
7580(7581aL|7581bL)	7581(7578aL|7578bL)	7582(7583aL|7583bL)	7583(7578aL|7578bL)	7584(7585aL|7587bL)	7585(7586aL|7586bL)	7586(7426aL|7426bL)	7587(7588aL|7588bL)	7588(7578aL|7578bL)	7589(7590aR|7595bR)
7590(7591aL|7593bL)	7591(7592aL|7592bL)	7592(7589aL|7589bL)	7593(7594aL|7594bL)	7594(7589aL|7589bL)	7595(7596aL|7598bL)	7596(7597aL|7597bL)	7597(7437aL|7437bL)	7598(7599aL|7599bL)	7599(7589aL|7589bL)
7600(7601aR|7606bR)	7601(7602aL|7604bL)	7602(7603aL|7603bL)	7603(7600aL|7600bL)	7604(7605aL|7605bL)	7605(7600aL|7600bL)	7606(7609aR|7607bL)	7607(7608aL|7608bL)	7608(7600aL|7600bL)	7609(7610aR|7613bR)
7610(7609aR|7611bL)	7611(7612aR|7612bR)	7612(7616aL|7616bL)	7613(ERROR-|7614bL)	7614(7615aR|7615bR)	7615(7616aL|7616bL)	7616(7617aR|7618bR)	7617(7619aR|7616bR)	7618(7616aR|7616bR)	7619(7620bR|ERROR-)
7620(7621aR|7621bR)	7621(7622aR|7623bR)	7622(7621aR|7621bR)	7623(7624aR|7621bR)	7624(7625aR|7626bR)	7625(7624aR|7624bR)	7626(7627aL|7624bR)	7627(7628aR|7628aR)	7628(7629aR|7629bR)	7629(7630aR|7635bR)
7630(7631aL|7633bL)	7631(7632aR|7632bR)	7632(7638aL|7638bL)	7633(7634aR|7634bR)	7634(7149aL|7149bL)	7635(ERROR-|7636bL)	7636(7637aR|7637bR)	7637(7149aL|7149bL)	7638(7639aR|7644bR)	7639(7640aL|7642bL)
7640(7641aL|7641bL)	7641(7638aL|7638bL)	7642(7643aL|7643bL)	7643(7638aL|7638bL)	7644(7645aL|7647bL)	7645(7646aL|7646bL)	7646(7649aL|7649bL)	7647(7648aL|7648bL)	7648(7638aL|7638bL)	7649(7650aR|7655bR)
7650(7651aL|7653bL)	7651(7652aL|7652bL)	7652(7649aL|7649bL)	7653(7654aL|7654bL)	7654(7649aL|7649bL)	7655(7656aL|7658bL)	7656(7657aR|7657aR)	7657(7660aL|7660bL)	7658(7659aL|7659bL)	7659(7649aL|7649bL)
7660(7661aR|7666bR)	7661(7662aL|7664bL)	7662(7663aL|7663bL)	7663(7660aL|7660bL)	7664(7665aR|7665bR)	7665(7669aL|7669bL)	7666(ERROR-|7667bL)	7667(7668aR|7668bR)	7668(7669aL|7669bL)	7669(7670aR|7675bR)
7670(7671aL|7673bL)	7671(7672bR|7672bR)	7672(7680aL|7680bL)	7673(7674aL|7674bL)	7674(7669aL|7669bL)	7675(7676aL|7678bL)	7676(7677aL|7677bL)	7677(7669aL|7669bL)	7678(7679aL|7679bL)	7679(7669aL|7669bL)
7680(7681aR|7686bR)	7681(7682aL|7684aL)	7682(7683aL|7683bL)	7683(7721aL|7721bL)	7684(7685bL|7685bL)	7685(7689aL|7689bL)	7686(7697aR|7687aL)	7687(7688bL|7688bL)	7688(7691aL|7691bL)	7689(ERROR-|7690aR)
7690(7693bR|ERROR-)	7691(ERROR-|7692bR)	7692(7695bR|ERROR-)	7693(ERROR-|7694bR)	7694(7702aR|7702bR)	7695(ERROR-|7696bR)	7696(7707aR|7707bR)	7697(7698aR|7699bR)	7698(7697aR|7697bR)	7699(7700aL|7697bR)
7700(7701aR|7701aR)	7701(7746aR|7746bR)	7702(7703aR|7704bR)	7703(7702aR|7702bR)	7704(7705bL|7702bR)	7705(7706aR|7706aR)	7706(7746aR|7746bR)	7707(7708aR|7709bR)	7708(7707aR|7707bR)	7709(7710bL|7707bR)
7710(7711bR|7711bR)	7711(7746aR|7746bR)	7712(7713aR|7718bR)	7713(7714aL|7716bL)	7714(7715aL|7715bL)	7715(7712aL|7712bL)	7716(7717aL|7717bL)	7717(7712aL|7712bL)	7718(7680aR|7719bL)	7719(7720aL|7720bL)
7720(7712aL|7712bL)	7721(ERROR-|7722bR)	7722(7723bL|ERROR-)	7723(7724aL|7724bL)	7724(7725aL|7725bL)	7725(7726aR|7731bR)	7726(7727aL|7729aL)	7727(7728aR|7728bR)	7728(7734aL|7734bL)	7729(7730aL|7730aL)
7730(7725aL|7725bL)	7731(ERROR-|7732aL)	7732(7733aL|7733aL)	7733(7725aL|7725bL)	7734(7735aR|7736bR)	7735(7734aR|7734bR)	7736(7734aR|7990bR)	7737(7738aR|7741bR)	7738(7739bL|7737bR)	7739(7740aR|7740aR)
7740(7764aR|7764bR)	7741(7747bL|7744bL)	7742(7743aR|7743aR)	7743(7746aR|7746bR)	7744(7745aR|7745aR)	7745(7755aR|7755bR)	7746(7747aR|7752bR)	7747(7748aL|7750aL)	7748(7749bR|7749bR)	7749(7764aR|7764bR)
7750(7751bR|7751bR)	7751(7737aR|7737bR)	7752(7773aR|7753aL)	7753(7754bR|7754bR)	7754(7755aR|7755bR)	7755(7756aR|7761bR)	7756(7757bL|7759bL)	7757(7758bR|7758bR)	7758(7764aR|7764bR)	7759(7760bR|7760bR)
7760(7737aR|7737bR)	7761(7762bL|7755bR)	7762(7763bR|7763bR)	7763(7746aR|7746bR)	7764(7765aR|7768bR)	7765(7764aR|7766aL)	7766(7767aR|7767aR)	7767(7737aR|7737bR)	7768(7769aL|7771aL)	7769(7770aR|7770aR)
7770(7746aR|7746bR)	7771(7772aR|7772aR)	7772(7755aR|7755bR)	7773(7774aR|7779bR)	7774(7775aL|7777aL)	7775(7776bR|7776bR)	7776(7764aR|7764bR)	7777(7778bR|7778bR)	7778(7737aR|7737bR)	7779(7782aR|7780aL)
7780(7781bR|7781bR)	7781(7755aR|7755bR)	7782(7783bR|ERROR-)	7783(7784aL|7784bL)	7784(7785aR|7788bR)	7785(7786aL|ERROR-)	7786(7787aR|7787bR)	7787(7791aL|7791bL)	7788(7789aL|ERROR-)	7789(7790aL|7790bL)
7790(7784aL|7784bL)	7791(7792aR|7797bR)	7792(7793aL|7795bL)	7793(7794aL|7794bL)	7794(7791aL|7791bL)	7795(7796aL|7796bL)	7796(7791aL|7791bL)	7797(7798aL|7800bL)	7798(7799aL|7799bL)	7799(7712aL|7712bL)
7800(7801aL|7801bL)	7801(7791aL|7791bL)	7802(7803aR|7808bR)	7803(7804aL|7806bL)	7804(7805aL|7805bL)	7805(7802aL|7802bL)	7806(7807aL|7807bL)	7807(7802aL|7802bL)	7808(7809aL|7811bL)	7809(7810aL|7810aL)
7810(7813aL|7813bL)	7811(7812aL|7812bL)	7812(7802aL|7802bL)	7813(7814aR|7819bR)	7814(7815aL|7817bL)	7815(7816aL|7816bL)	7816(7813aL|7813bL)	7817(7818aR|7818bR)	7818(7822aL|7822bL)	7819(ERROR-|7820bL)
7820(7821aR|7821bR)	7821(7822aL|7822bL)	7822(7823aR|7828bR)	7823(7824aL|7826bL)	7824(7825bL|7825bL)	7825(7833aL|7833bL)	7826(7827aL|7827bL)	7827(7822aL|7822bL)	7828(7829aL|7831bL)	7829(7830aL|7830bL)
7830(7822aL|7822bL)	7831(7832aL|7832bL)	7832(7822aL|7822bL)	7833(7834aR|7839bR)	7834(7835aL|7837bL)	7835(7836aL|7836bL)	7836(7833aL|7833bL)	7837(7838aL|7838bL)	7838(7833aL|7833bL)	7839(7840aL|7842bL)
7840(7841aL|7841aL)	7841(7844aL|7844bL)	7842(7843aL|7843bL)	7843(7833aL|7833bL)	7844(7845bL|ERROR-)	7845(7846aL|7846bL)	7846(7847aR|7852bR)	7847(7848aL|7850aL)	7848(7849aR|7849bR)	7849(7855aL|7855bL)
7850(7851aL|7851aL)	7851(7846aL|7846bL)	7852(ERROR-|7853aL)	7853(7854aL|7854aL)	7854(7846aL|7846bL)	7855(7856aR|7857bR)	7856(7855aR|7855bR)	7857(7858aR|7855bR)	7858(7859aR|7860bR)	7859(7858aR|7858bR)
7860(7861aL|7858bR)	7861(7862aR|7862aR)	7862(7863aR|7863bR)	7863(7864aR|7869bR)	7864(7865aL|7867aL)	7865(7866aR|7866bR)	7866(7946aL|7946bL)	7867(7868bL|7868bL)	7868(7872aL|7872bL)	7869(EROR-|7871bL)
7870(7871bL|7871bL)	7871(7883aL|7883bL)	7872(7873aR|7878bR)	7873(7874aL|7876bL)	7874(7875aL|7875bL)	7875(7872aL|7872bL)	7876(7877aL|7877bL)	7877(7872aL|7872bL)	7878(7879aL|7881bL)	7879(7880aL|7880bL)
7880(7894aL|7894bL)	7881(7882aL|7882bL)	7882(7872aL|7872bL)	7883(7884aR|7889bR)	7884(7885aL|7887bL)	7885(7886aL|7886bL)	7886(7883aL|7883bL)	7887(7888aL|7888bL)	7888(7883aL|7883bL)	7889(7890aL|7892bL)
7890(7891aL|7891bL)	7891(7921aL|7921bL)	7892(7893aL|7893bL)	7893(7883aL|7883bL)	7894(7895aR|7900bR)	7895(7896bL|7898bL)	7896(7897aR|7897aR)	7897(7930aL|7930bL)	7898(7899aL|7899bL)	7899(7894aL|7894bL)
7900(ERROR-|7901bL)	7901(7902aL|7902aL)	7902(7903aL|7903bL)	7903(7904aR|7909bR)	7904(7905bL|7907bL)	7905(7906bR|7906bR)	7906(7930aL|7930bL)	7907(7908bL|7908bL)	7908(7894aL|7894bL)	7909(ERROR-|7910bL)
7910(7911aL|7911bL)	7911(7903aL|7903bL)	7912(7913aR|7918bR)	7913(7914bL|7916bL)	7914(7915aR|7915aR)	7915(7933aL|7933bL)	7916(7917aL|7917bL)	7917(7912aL|7912bL)	7918(ERROR-|7919bL)	7919(7920aL|7920aL)
7920(7921aL|7921bL)	7921(7922aR|7927bR)	7922(7923bL|7925bL)	7923(7924bR|7924bR)	7924(7933aL|7933bL)	7925(7926bL|7926bL)	7926(7912aL|7912bL)	7927(ERROR-|7928bL)	7928(7929aL|7929bL)	7929(7921aL|7921bL)
7930(7931aR|7932bR)	7931(7930aR|7930bR)	7932(7936aR|7930bR)	7933(7934aR|7935bR)	7934(7933aR|7933bR)	7935(7941aR|7933bR)	7936(7937aR|7938bR)	7937(7936aR|7936bR)	7938(7939bL|7936bR)	7939(7940aR|7940aR)
7940(7963aR|7963bR)	7941(7942aR|7943bR)	7942(7941aR|7941bR)	7943(7944bL|7941bR)	7944(7945bR|7945bR)	7945(7863aR|7863bR)	7946(7947aR|7952bR)	7947(7948aL|7950aL)	7948(7949aL|7949bL)	7949(7955aL|7955bL)
7950(7951aL|7951aL)	7951(7946aL|7946bL)	7952(ERROR-|7953aL)	7953(7954aL|7954aL)	7954(7946aL|7946bL)	7955(7956aR|7961bR)	7956(7957aL|7959aL)	7957(7958aL|7958bL)	7958(7964aL|7964bL)	7959(7960aL|7960aL)
7960(7946aL|7946bL)	7961(ERROR-|7962aL)	7962(7963aL|7963aL)	7963(7946aL|7946bL)	7964(7965aR|7966bR)	7965(HALT--|7967bR)	7966(ERROR-|7967bR)	7967(7968aR|7973bR)	7968(7969aL|7971bL)	7969(7970aL|7970bL)
7970(7967aL|7967bL)	7971(7972aL|7972bL)	7972(7967aL|7967bL)	7973(7974aL|7976bL)	7974(7975aL|7975aL)	7975(7978aL|7978bL)	7976(7977aL|7977bL)	7977(7967aL|7967bL)	7978(7979aR|7984bR)	7979(7980bL|7982bL)
7980(7981bR|7981bR)	7981(7987aL|7987bL)	7982(7983bL|7983bL)	7983(7978aL|7978bL)	7984(ERROR-|7985bL)	7985(7986aR|7986aR)	7986(7987aL|7987bL)	7987(7988aR|7989bR)	7988(7990aR|7987bR)	7989(7987aR|7987bR)
7990(7991aR|7992bR)	7991(7990aR|ERROR-)	7992(ERROR-|7993bL)	7993(7994aR|7994bR)	7994(7995aL|7995bL)	7995(7996aR|7997bR)	7996(7998aR|7995bR)	7997(7995aR|7995bR)	7998(7999aR|8000bR)	7999(8001aR|7995bR)
8000(7995aR|7995bR)	8001(8002aR|8005bR)	8002(8003aL|7995bR)	8003(8004aR|8004bR)	8004(8006aL|8006bL)	8005(7995aR|7995bR)	8006(8007aR|8008bR)	8007(8006aR|ERROR-)	8008(ERROR-|8009bL)	8009(8010aL|8010bL)
8010(8011aL|8011bL)	8011(8012bL|ERROR-)	8012(4484aL|4484bL)							