A Relatively Small Turing Machine Whose Behavior Is Independent of Set Theory

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Abstract

Since the definition of the Busy Beaver function by Radó in 1962, an interesting open question has been what the smallest value of n for which $BB(n)$ is independent of ZFC set theory. Is this n approximately 10, or closer to 1,000,000, or is it even larger? In this paper, we show that it is at most 7,910 by presenting an explicit description of a 7,910-state Turing machine Z with 1 tape and a 2-symbol alphabet that cannot be proved to run forever in ZFC (even though it presumably does), assuming ZFC is consistent. The machine is based on work of Harvey Friedman on independent statements involving order-invariant graphs. In doing so, we give the first known upper bound on the highest provable Busy Beaver number in ZFC. To create Z, we develop and use a higher-level language, Laconic, which is much more convenient than direct state manipulation. We also use Laconic to design two Turing machines, G and R, that halt if and only if there are counterexamples to Goldbach's conjecture and Riemann's hypothesis, respectively.

1 Introduction

1.1 Background and Motivation

Zermelo-Fraenkel set theory with the axiom of choice, more commonly known as ZFC, is an axiomatic system invented in the twentieth which has since been used as the foundation of most of modern mathematics. It encodes arithmetic by describing natural numbers as increasing sets of sets.

Like any axiomatic system capable of encoding arithmetic, ZFC is constrained by Gödel's two incompleteness theorems. The first incompleteness theorem states that if ZFC is *consistent* (it never proves both a statement and its opposite), then ZFC cannot also be *complete* (able to prove every true statement). The second incompleteness theorem states that if ZFC is consistent, then ZFC cannot prove its own consistency. Because we have built modern mathematics on top of ZFC, we can reasonably be said to have assumed ZFC's consistency. This means that we must also believe that ZFC cannot prove its own consistency. This fact carries with it certain surprising conclusions.

In particular, consider a Turing machine Z that enumerates, one after the other, each of the provable statements in ZFC. To describe how such a machine might be constructed, Z could iterate over the axioms and inference rules of ZFC, applying each in every possible way to each conclusion or pair of conclusions that had been reached so far. We might ask Z to halt if it ever reaches a contradiction; in other words, Z will halt if and only if it finds a proof of $0 = 1$. Because this machine will enumerate *every* provable statement in ZFC, it will run forever if and only if ZFC is consistent.

It follows that Z is a Turing machine for which the question of its behavior (whether or not it halts when run indefinitely) is equivalent to the consistency of ZFC.[1] Therefore, just as ZFC cannot prove its own consistency (assuming ZFC is consistent), ZFC also cannot prove that Z will run forever. In other words, the statement, "Z will run forever" is *independent of* ZFC.

This is interesting because, while the undecidability of the halting problem tells us that there cannot exist an algorithmic method for determining whether an *arbitrary* Turing machine loops or halts, Z is an example of a *specific* Turing machine whose behavior cannot be proven one way or the other using the foundation of modern mathematics. Mathematicians and computer scientists think of themselves as being able to determine how a given algorithm will behave if given enough time to stare at it; despite this intuition, Z is a machine whose behavior we can never prove without assuming axioms more powerful than those generally assumed in modern mathematics.

1.2 Turing Machines

There are many slightly different definitions of Turing machines. For example, some definitions allow the machine to have multiple tapes; others only allow it to have one; some allow an arbitrarily large alphabet, while others allow only two symbols, and so on. In most research regarding Turing machines, mathematicians don't concern themselves with which of these models to use, because any one can simulate the others (usually efficiently). However, because this work is concerned with upper-bounding the exact number of states required to perform certain tasks, it's important to define the model precisely. The model we choose here is traditional for the Busy Beaver function.

Formally, a k-state Turing machine is a 7-tuple $M = (Q, \Gamma, a, \Sigma, \delta, q_0, F)$, where:

Q is the set of k *states* $\{q_0, q_1, \ldots, q_{k-2}, q_{k-1}\}$
$\Gamma = \{a, b\}$ is the set of *tape alphabet symbols*
`a` is the *blank symbol*
$\Sigma =$ is the set of *input symbols*
$\delta = Q \times \Gamma \to (Q \cup F) \times \Gamma \times \{L, R\}$ is the *transition function*
q_0 is the *start state*
$F = \{\mathrm{HALT}, \mathrm{ERROR}\}$ is the set of *halting states.*

A Turing machine's *states* make up the Turing machine's easily-accessible, finite memory. The Turing machine's state is initialized to q_0.

The *tape alphabet symbols* correspond to the symbols that can be written on the Turing machine's infinite tape.

In this work, all Turing machines are run on the all-`a` input.

The *transition function* encodes the Turing machine's behavior. It takes two inputs: the current state of the Turing machine (an element of $Q \cup F$) and the symbol read off the tape (an element of

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[1] While we will talk about ZFC throughout this paper, rather than simple ZF set theory, this is simply a convention. For our purposes, the Axiom of Choice is irrelevant: the consistency of ZFC is equivalent to the consistency of simple ZF set theory, [14] and ZFC and ZF prove exactly the same arithmetical statements (which include, among other things, statements about whether Turing machines halt). [23]

Γ). It outputs three instructions: what state to enter (an element of $Q \cup F$), what symbol to write onto the tape (an element of Γ) and what direction to move the head in (an element of $\{L, R\}$). A transition function specifies the entire behavior of the Turing machine in all cases.

The *start state* is the state that the Turing machine is in at initialization.

A *halting transition* is a transition to a halting state, which causes the Turing machine to halt. While having three possible halting transitions is not necessary for our purposes, being able to differentiate between different types of halting (HALT and ERROR) is useful for testing.

1.3 The Busy Beaver Function

Consider the set of all Turing machines with k states, for some positive integer k. We call a Turing machine B a *k-state Busy Beaver* if when run on the empty tape as input, B halts, and also runs for at least as many steps before halting as all other halting k-state Turing machines. [22]

In other words, a Busy Beaver is a Turing machine that runs for at least as long as all other halting Turing machines with the same number of states. Another common definition for a Busy Beaver is a Turing machine that writes as many 1's on the tape as possible; because the number of 1's written is a somewhat arbitrary measure, it is not used in this work.

The *Busy Beaver function*, written $BB(k)$, equals the number of steps it takes for a k-state Busy Beaver to halt. The Busy Beaver function has many striking properties. To begin with, it is not *computable*; in other words, there does not exist an algorithm that takes k as input and returns $BB(k)$, for arbitrary values of k. This follows directly from the undecidability of the halting problem. Suppose an algorithm existed to compute the Busy Beaver function; then given a k-state Turing machine M as input, we could compute $BB(k)$ and run M for $BB(k)$ steps. If, after $BB(k)$ steps, M had not yet halted, we could safely conclude that M would never halt. Thus, we could solve the halting problem, which we know is impossible.

By the same argument, $BB(k)$ must grow faster than any computable function. (To check this, assume that some computable function $f(k)$ grows faster than $BB(k)$, and substitute $f(k)$ for $BB(k)$ in the rest of the proof.) In particular, the Busy Beaver grows even faster than (for instance) the Ackermann function, a well-known fast-growing function.

Because finding the value of $BB(k)$ for a given k requires so much work (one must fully explore the behavior of all k-state Turing machines), few explicit values of the Busy Beaver function are known. The known values are [4, 16]:

$$BB(1) = 1$$
$$BB(2) = 6$$
$$BB(3) = 21$$
$$BB(4) = 107$$

For $BB(5)$, $BB(6)$, and $BB(7)$ only lower bounds are known: $BB(5) \geq 47{,}176{,}870$, $BB(6) > 7.4 \times 10^{36{,}534}$, and $BB(7) > 10^{10^{10^{10^{10^{7}}}}}$. Additionally, $BB(22)$ is known to be larger than Graham's Number (a famous huge number from Ramsey theory, obtained by iterating the Ackermann function 64 times) [19][8][9]. Researchers have worked on pinning down the value of $BB(5)$ exactly, and some consider it to be possibly within reach.

Another way to discuss the Busy Beaver sequence is to say that modern mathematics has established a *lower bound* of 4 on the highest provable Busy Beaver value. In this paper, we prove the first known *upper bound* on the highest provable Busy Beaver value in ZFC; that is, we give a value of k, namely 7,910, such that the value of $BB(k)$ cannot be proven in ZFC.

Intuitively, one might expect that while no algorithm may exist to compute $BB(k)$ for *all* values of k, we could find the value of $BB(k)$ for any *specific* k using a procedure similar to the one we used to find the value of $BB(k)$ for $k \leq 4$. The reason this is not so is closely tied to the existence of a machine like the Gödelian machine Z, as described in Section 1.1. Suppose that Z has k states. Because Z's behavior (whether it halts or loops) cannot be proven in ZFC, it follows that the value of $BB(k)$ also can't be proven in ZFC; if it could, then a proof would exist of Z's behavior in ZFC. Such a proof would consist of a *computation history* for Z, which is an explicit step-by-step description of Z's behavior for a certain number of steps. If Z halts, then a computation history leading up to Z's halting would be the entire proof; if Z loops, then a computation history that takes $BB(k)$ steps, combined with a proof of the value of $BB(k)$, would constitute a proof that Z will run forever.

In this paper we construct a machine like Z, for which a proof that Z runs forever would imply that ZFC was consistent. In doing so, we give an explicit upper bound on the highest Busy Beaver value provable in ZFC assuming the consistency of a slightly stronger set theory. Our machine, which we shall refer to as Z hereafter, contains 7,910 states. Therefore, we will never be able to prove the value of $BB(7{,}910)$ without assuming more powerful axioms than those of ZFC. This upper bound is presumably very far from tight, but it is a first step.

Even to achieve a state count of 7,910, we will need three nontrivial ideas: Harvey Friedman's order-theoretic statements, *on-tape processing*, and *introspective encoding*. Without all three ideas, we found that the state count would be in the tens of thousands, hundreds of thousands, or even millions. We briefly introduce these ideas in the following subsection, and explore them in much greater detail in Section 8. The implementation of these ideas constitutes this paper's main technical contribution.

1.4 Parsimony

In most algorithmic study, efficiency is the primary concern. In designing Z, however, parsimony is the only thing that matters. One historical analogue is the practice of "code-golfing": a recreational pursuit adopted by some programmers in which the goal is to produce a piece of code in a given programming language, using as few characters as possible. Many examples of code-golfing can be found at [26]. The goal of designing a Turing machine with as few states as possible to accomplish a certain task, without concern for the machine's efficiency or space usage, can be thought of as code-golfing with a particularly low-level programming language.

Part of the charm of Turing machines is that they give us a "standard reference point" for measuring complexity, unencumbered by the details of more sophisticated programming languages. Also, with Turing machines, there can be no suspicion that we engineered a programming formalism just for the purpose of code-golfing, or for making the concepts we want artificially simple to describe. This is why we prefer Turing machines as a tool for measuring complexity; not because they are particularly special, but simply because they are so primitive that their specifics will interfere minimally with what we mean by an algorithm being "complicated."

In this paper, we use three ideas for generating parsimonious Turing machines: Harvey Friedman's mathematical statements, *on-tape processing*, and *introspective* Turing machines. The last of these ideas was proposed, under a different name and with some variations, by Ben-Amram and Petersen in 2002 [3]. These three ideas are explained in more detail in Subsections 3.1, 8.1, and 8.3, respectively, but we summarize them very briefly here.

The first idea is simply to use the research done by Friedman into finding simple-to-express statements that are equivalent to the consistency of various axiomatic systems. In particular, we use a statement discovered by Friedman to be equivalent to the consistency of a set theory stronger than ZFC (and whose consistency, therefore, would imply the consistency of ZFC).[2] [10]

The second idea, on-tape processing, is a way to encode high-level commands into a Turing machine parsimoniously. Instead of converting commands to groups of states directly, which incurs a multiplicative overhead based on how large these groups need to be, on-tape processing begins by writing the commands onto the tape, using as efficient an encoding as possible. Then, once the commands are on the tape, the commands are processed by a single group of states that understands how to interpret them.

The third idea, introspective Turing machines, is a way to write long strings onto the tape using as few states as possible. The idea is to encode information one of each state's transitions, instead of encoding information in each state's write field. This is advantageous because there are many choices for which state to point a transition to, but only two choices for which bit to write. Therefore, more information can be encoded in each state using this method.

1.5 Implementation Overview

To generate descriptions of Turing machines with nice mathematical properties entirely by hand is a daunting task. Rather than approach the problem directly, we created tools for generating parsimonious Turing machines while presenting an interface that is comfortably familiar to most programmers (and to us!).

We created two tools. At the top level is the Laconic programming language, whose syntax and capabilities are similar to those of most programming languages, such as Java or Python. Beneath it we created a lower-level language called Turing Machine Descriptor (TMD). TMD is quite unlike most programming languages, and is better thought of as a convenient way to describe a multi-tape, 3-symbol Turing machine plus a function stack. The style of multi-tape Turing machine used in TMD is the commonly used "one-tape-at-a-time" abstraction: only one tape at a time can be interacted with, for reading, writing, and moving the head. Laconic compiles down to a TMD program, and TMD compiles down to a description of a single-tape, 2-symbol Turing machine. This process is illustrated in Figure 1.

We recommend that programmers hoping to use our tools to generate their own encodings of mathematical statements or algorithms as Turing machines use Laconic. Laconic's interface is perfect for somebody hoping to write in a "traditional" language. On the other hand, if the programmer wishes to improve upon Laconic's compilation process, writing code directly in TMD is likely to be the better option.

2 Related Work

Gregory Chaitin raised the problem of proving a version of our result in his book *The Limits of Mathematics.* [7] He wrote:

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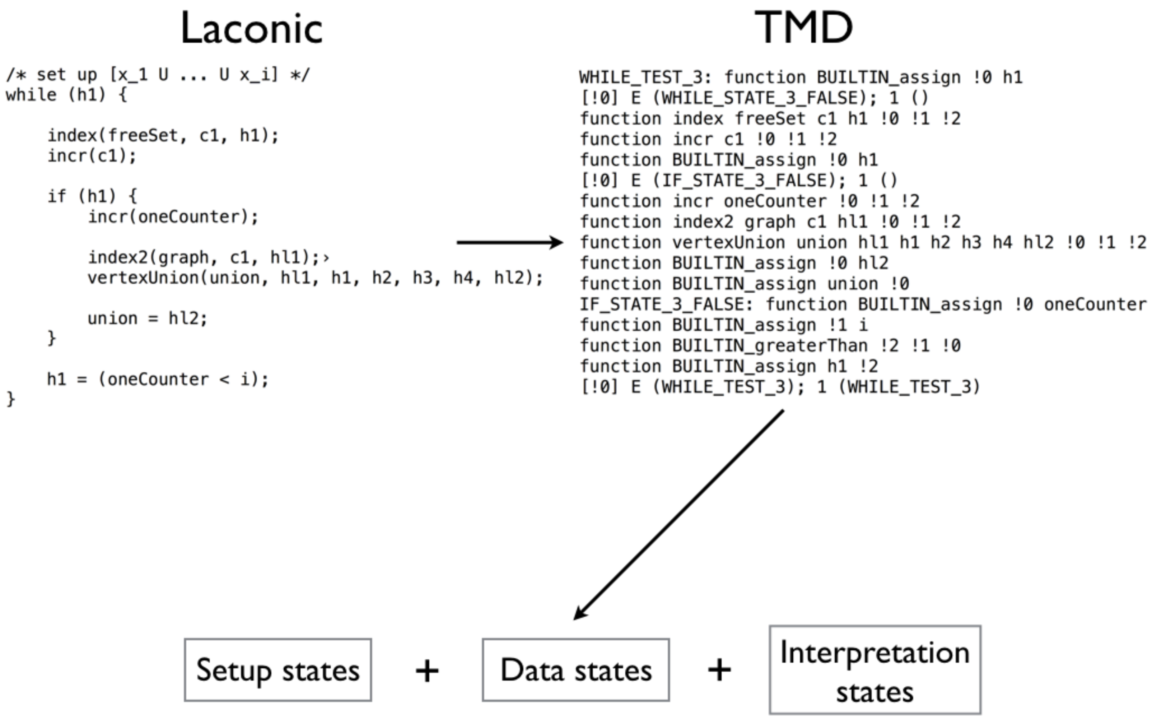
[2] It is unclear whether using Friedman's statements in fact does decrease the state usage of the Turing machines created, or whether a direct approach would perform better. [1] However, we find it much easier to reason about programs encoding graph-theoretic statements than programs that enumerate proofs using first-order logic!



```
/* set up [x_1 U ... U x_i] */
while (h1) {

    index(freeSet, c1, h1);
    incr(c1);

    if (h1) {
        incr(oneCounter);

        index2(graph, c1, hl1);›
        vertexUnion(union, hl1, h1, h2, h3, h4, hl2);

        union = hl2;
    }

    h1 = (oneCounter < i);
}
```

```
WHILE_TEST_3: function BUILTIN_assign !0 h1
[!0] E (WHILE_STATE_3_FALSE); 1 ()
function index freeSet c1 h1 !0 !1 !2
function incr c1 !0 !1 !2
function BUILTIN_assign !0 h1
[!0] E (IF_STATE_3_FALSE); 1 ()
function incr oneCounter !0 !1 !2
function index2 graph c1 hl1 !0 !1 !2
function vertexUnion union hl1 h1 h2 h3 h4 hl2 !0 !1 !2
function BUILTIN_assign !0 hl2
function BUILTIN_assign union !0
IF_STATE_3_FALSE: function BUILTIN_assign !0 oneCounter
function BUILTIN_assign !1 i
function BUILTIN_greaterThan !2 !1 !0
function BUILTIN_assign h1 !2
[!0] E (WHILE_TEST_3); 1 (WHILE_TEST_3)
```

Figure 1: A visual overview of the compilation process.

> I would like to have somebody program out Zermelo-Fraenkel set theory in my version of LISP, which is pretty close to normal LISP as far as this task is concerned, just to see how many bits of complexity mathematicians normally assume . . . If you programmed ZF, you'd get a really sharp incompleteness result. It wouldn't say that you can get at most $H(ZF) + 15328$ bits of [Chaitin's halting probability] Ω, it would say, perhaps, at most 96000 bits! We'd have a much more definite incompleteness theorem.

We did not program ZF set theory in LISP, but we programmed it in an even simpler language—thereby answering Chaitin's call for an explicit number of bits to attach to the complexity of ZF set theory. (As many as required to fully describe our Turing machine—or more precisely, 157,819.)

This paper is not the first to attempt to quantify the complexity of arithmetical statements. Calude and Calude [6] define a register machine of their own design, and provide quantifications of the complexity of Legendre's conjecture, Fermat's last theorem, Goldbach's conjecture, Dyson's conjecture, the Riemann hypothesis, and the four color theorem.[3] In addition, Koza [15] and Pargellis [21] each invent instruction sets that are particularly well-suited to representing self-reproducing programs simply, and show that starting from a "primordial soup" of such instructions distributed about a large memory, along with an increasing number of program threads, a rich ecosystem of increasingly efficient self-reproducing programs start to dominate the "landscape."

Also similar to the work of this paper is the famous search for a universal Turing machine. A *universal* Turing machine is a Turing machine that can simulate another Turing machine, when provided a description of the other machine on its input tape. The smallest universal Turing machine is a 2-state, 3-symbol Turing machine, found by Smith [24].

This paper differs from the previous work in two ways: firstly, it is the first to give explicit, relatively small machines whose behavior is provably independent of the standard axioms of modern mathematics. Secondly, to our knowledge, this paper is the first concrete study of parsimony to use Turing machines themselves as the model of computation—rather than (for example) a new programming language proposed by the authors, or a complex on-tape description of Turing machines! We consider it important to use the weakest and most common model of computation for complexity comparisons across different mathematical statements. This is because the more powerful and complex the model of computation used, the more of the complexity of the algorithm can be "shunted" onto the model of computation, and the greater the potential distortion created by the choice of model. As a reductio ad absurdum, we could imagine a programming language that included "test the Riemann hypothesis" and "test the consistency of ZFC" as primitive operations. Along a similar vein, very small universal Turing machines typically use a very complex description format for the input machine. In this way, the complexity can again be "shunted" away from the Turing machine itself. By using the "weakest" model of computation that is commonly known, and by requiring that the input tape at initialization be empty, we hope to avoid this pitfall and make it easier to interpret our results in a model-independent way.

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[3] Because Fermat's last theorem and the four color theorem have been proved, their "complexity" is now known to be 1—the minimum number of states in a Turing machine that runs forever.

3 A Turing Machine that Cannot Be Shown to Run Forever Using ZFC

We present a 7,910-state Turing machine whose behavior is *independent of ZFC*; it is not possible to prove that this machine halts or doesn't halt using the axioms of ZFC, assuming that a stronger set theory is consistent. It's therefore impossible to prove the value of $BB(7{,}910)$ to be any given value without assuming axioms more powerful than ZFC, assuming that ZFC is consistent.

For an explicit listing of this machine, see Appendix C.

We call this machine Z. One way to build this machine would be to start with the axioms of ZFC and apply the inference rules of first-order logic repeatedly in each possible way so as to enumerate every statement ZFC could prove, and to halt if ever a contradiction was found. While this method is conceptually simple, to actually construct such a machine would lead to a huge number of states, because it would require writing a program to manipulate the axioms of ZFC and the inference rules of first-order logic, and then compiling that program all the way down to Turing machine states.

3.1 Friedman's Mathematical Statement

Thankfully, a simpler method exists for creating Z. Friedman [10] was able to derive a graph-theoretic statement whose truth implies the consistency of ZFC, and which will be false if ZFC is inconsistent.[4] Here is Friedman's statement (the notation will be explained in the rest of this section):

Statement 1. *For all* $k, n, r > 0$, *every order invariant graph on* $[\mathbb{Q}]^{\leq k}$ *has a free* $\{x_1, \ldots, x_r, ush(x_1), ..., ush(x_r)\}$ *of complexity* $\leq (8knr)!$, *each* $\{x_1, \ldots, x_{(8kni)!}\}$, *for* $i > 0$ *and* $(8kni!) \leq r$, *reducing* $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$. *[10]*

If s is a set, the operation $(.)^{\leq k}$ refers to the set of all subsets of s with size at most k.

A graph on $[\mathbb{Q}]^{\leq k}$ is an irreflexive symmetric relation on $[\mathbb{Q}]^{\leq k}$. In other words, it can be thought of as a graph whose vertices are elements of $[\mathbb{Q}]^{\leq k}$, and whose edges are undirected, connect pairs of vertices, and never connect vertices to themselves.

A *free* set is a set such that no pair of elements in that set are connected by an edge.

A number of *complexity* at most c refers to a number that can be written as a fraction a/b, where a and b are both integers with absolute value less than or equal to c. A set has complexity at most c if all the numbers it contains have complexity at most c.

An *order invariant graph* is a graph containing a countably infinite number of nodes. In particular, it has one node for each finite set of rational numbers. The only numbers relevant to the statement are numbers of complexity $(8knr)!$ or smaller. In every description of nodes that follows, the term *node* refers both to the object in the order invariant graph and to the set of numbers that it represents.

In an order invariant graph, two nodes (a, b) have an edge between them if and only if each other pair of nodes (c, d) that is *order equivalent* with (a, b) has an edge between them. Two pairs

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[4] In fact, Friedman's statement is equivalent to the consistency of SRP ("stationary Ramsey property"), which is a system of axioms more powerful than ZFC. Because SRP is strictly more powerful than ZFC (it in fact consists of ZFC plus some additional axioms), the consistency of SRP implies the consistency of ZFC, and the inconsistency of ZFC implies the inconsistency of SRP.

of nodes (a, b) and (c, d) are *order equivalent* if a and c are the same size and b and d are the same size and if for all $1 \leq i \leq |a|$ and $1 \leq j \leq |b|$, the i-th element of a is less than the j-th element of b if and only if the i-th element of c is less than the j-th element of d.

To give some trivial examples of order invariant graphs: the graph with no edges is order invariant, as is the complete graph. A less trivial example is a graph on $[\mathbb{Q}]^{\leq 2}$, in which each node corresponds to a set of two rational numbers of a given complexity, and there is an edge between two nodes if and only if their corresponding sets a and b satisfy $|a| = |b| = 2$ and $a_1 < b_1 < a_2 < b_2$. (Because edges are undirected in order invariant graphs, such an edge will exist if *either* assignment of the vertices to a and b satisfies the inequality above.)

The *ush()* function takes as input a set and returns a copy of that set with all non-negative numbers in that set incremented by 1.

For vertices x and y, $x \leq_{rlex} y$ if and only if $x = y$ or $x_{|x|-i} < y_{|y|-i}$ where i is the least integer such that $x_{|x|-i} \neq y_{|y|-i}$.[5] (The $\leq_{rlex}$ operation creates a lexicographic ordering over the vertices, weighting the last and largest elements of those vertices most heavily. Like with lexicographic orderings, if the two vertices are identical but one is longer, the shorter one comes first.)

Finally, a set of vertices X *reduces* a set of vertices Y if and only if for all $y \in Y$, there exists $x \in X$ such that either $x = y$ or $x \leq_{rlex} y$ and an edge exists between x and y.

3.2 Implementation Methods

To create Z, we needed to design a Turing machine that halts if Statement 1 is false, and loops if Statement 1 is true. Such a Turing Machine's behavior is necessarily independent of ZFC, because the truth or falsehood of Statement 1 is independent of ZFC, assuming the consistency of SRP. [10] SRP is an extension of ZFC by certain relatively mild large cardinal hypotheses, and is widely regarded by set theorists as consistent. For more information about SRP, see [13].

To design such a Turing machine, we wrote a Laconic program which encodes Friedman's statement, then compiled the program down to a description of a single-tape, 2-symbol Turing machine. What follows is an extremely brief description of the design of the Laconic program; for the documented Laconic code itself, along with a detailed explanation of the full compilation process, see [25].

Our Laconic program begins by looping over all non-negative values for k, n, and r. For each trio (k, n, r), our program generates a list N of all numbers of complexity at most $(8knr)!$. These numbers represent the vertices in our putative order invariant graph. Because Laconic does not support floating-point numbers, the list is entirely composed of integers; it is a list of all numbers that can be written in the form $(((8knr)!)!)\frac{i}{j}$, where i and j are integers satisfying $-(8knr)! \leq i \leq (8knr)!$ and $1 \leq j \leq (8knr)!$. (Note that any number that can be expressed in this form is necessarily an integer, because of the large scaling factor in front.)

After we generate N, we generate the nodes in a potential order invariant graph by adding to N all possible lists of k or fewer numbers from N. We call this list of lists V.

We iterate over all binary lists of length $|V|^2$. Any such list E represents a possible set of edges in the graph. To be more precise, we say that an edge exists between node i and node j (represented by V_i and V_j respectively) if and only if $E_{i|V|+j}$ is 1.

For any graph (V, E), we say that it is "valid" if the following three conditions hold:

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[5] Friedman recommended in private communication that we use the $\leq_{rlex}$ comparator to compare vertices, instead of comparing their maximum elements as described in his manuscript.

1. No node has an edge to itself.
2. If an edge exists between node i and node j, an edge also exists between node j and node i.
3. The graph has a free $\{x_1, \ldots, x_r, \mathrm{ush}(x_1), ..., \mathrm{ush}(x_r)\}$, each $\{x_1, \ldots, x_{(8kni)!}\}$ reducing $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$.

For each list of nodes V, we loop over every possible binary list E, and if no pair (V, E) yields a valid graph, we halt.

When verifying the validity of a graph, checking the first two conditions is trivial, but the third merits further explanation. In order to verify that a given graph (V, E) has a free $\{x_1, \ldots, x_r, ush(x_1), ..., ush(x_r)\}$, each $\{x_1, \ldots, x_{(8kni)!}\}$ reducing $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$, we look at every possible subset of the nodes in V. For each subset, we verify that it has length r, that $\mathrm{ush}(x_1), ..., \mathrm{ush}(x_r)$ all exist in V, and for each i such that $(8kni)! \leq r$, that $\{x_1, \ldots, x_{(8kni)!}\}$ reduces $[x_1 \cup \cdots \cup x_i \cup \{0, \ldots, n\}]^{\leq k}$. Once we have found such a subset, we know that the third conditon is satisfied.

4 A Turing Machine that Encodes Goldbach's Conjecture

We present a 4,888-state Turing machine that *encodes Goldbach's conjecture*; in other words, to know whether this machine halts is to know whether Goldbach's conjecture is true. It is therefore impossible to prove the value of $BB(4{,}888)$ without simultaneously proving or disproving Goldbach's conjecture.[6]

Recall that Goldbach's conjecture is as follows:

Statement 2. Every even integer greater than 2 can be expressed as the sum of two primes.

Because Goldbach's conjecture is so simple to state, the Laconic program encoding the statement is also quite simple. It can be found in Appendix A. A detailed explanation of the compilation process, documentation for the Laconic language, and an explicit description of this Turing machine are available at [25].

5 A Turing Machine that Encodes Riemann's Hypothesis

We present a 5,372-state Turing machine that *encodes Riemann's hypothesis*; in other words, to know whether this machine halts is to know whether Riemann's hypothesis is true. An explicit description of this machine can be found at [25]

Riemann's hypothesis is traditionally stated as follows:

Statement 3. The Riemann zeta function has its zeros only at the negative even integers and the complex numbers with real part 1/2.

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[6] We note that our tools were primarily meant to encode complex statements into Turing machines, such as the Statement 1. Goldbach's conjecture is quite simple, and as such, it is probably possible to make a dramatically smaller machine through a more direct approach. [1]

5.1 Equivalent Statement

Instead of encoding the Riemann zeta function into a Laconic program, it is simpler to use the following statement, which was shown by Lagarias [5] to be equivalent to the Riemann hypothesis:

Statement 4. *For all integers $n \geq 1$,*

$$\left(\left(\sum_{k\leq\delta(n)} \frac{1}{k}\right) - \frac{n^2}{2}\right)^2 < 36n^3$$

The function $\delta(n)$ used in Statement 4 is defined as follows:

$$\begin{gathered}\eta(j) = p \text{ if } j = p^k,\ p \text{ is prime, } k \text{ is a positive integer}\\ \eta(j) = 1 \text{ otherwise}\\ \delta(x) = \prod_{n<x}\prod_{j\leq n}\eta(j)\end{gathered}$$

5.2 Implementation Methods

Statement 4 is equivalent to the following statement, which contains only positive integers[7]:

$$l(n) < r(n)$$

for all positive integers n, where

$$l(n) = a(n)^2 + b(n)^2$$

$$r(n) = 36n^3(\delta(n)!)^2 + 2a(n)b(n)$$

.

$$a(n) = \sum_{k\leq\delta(n)} \frac{\delta(n)!}{k}$$

$$b(n) = \frac{n^2\delta(n)!}{2}$$

To check the Riemann hypothesis, our program computes $a(n)$, $b(n)$, $l(n)$, and $r(n)$, in that order, for each possible value of n. If $l(n) \geq r(n)$, our program halts.

6 Laconic

Laconic is a programming language designed to be both user-friendly and easy to compile down to parsimonious Turing machine descriptions.

Laconic is a strongly-typed language that supports recursive functions. Laconic compiles to an intermediate language called TMD. TMD programs are spread across multiple files and grouped into directories. TMD directories are meant to represent sequences of commands that could be

---

[7] Although it is not immediately obvious, $\frac{\delta(n)!}{k}$ is necessarily an integer for all $k \leq \delta(n)$, and $\frac{\delta(n)!}{2}$ is an integer for all $n > 1$.

given to a multi-tape, 3-symbol Turing machine, using the Turing machine abstraction that allows the machine to read and write from one head at a time.

For an example of a Laconic program, see Appendix A. For an illustration of the compilation process, see Figure 1.

7 TMD

TMD is a programming language designed to help the user describe the behavior of a multi-tape, 3-symbol Turing machine with a function stack. Each tape is infinite in one direction and supports three symbols: `_`, `1`, and `E`. The blank symbol is `_`: that is, `_` is the only symbol that can appear on the tape an infinite number of times. The tape must always have the form $\texttt{\_}?(1|E)^{+}\texttt{\_}^{\infty}$; in other words, each tape must always contain a string of `1`'s and `E`'s of size at least 1, possibly preceded by a `_` symbol, and necessarily followed by an infinite number of copies of the `_` symbol.

What is the purpose of having a language like TMD as an intermediary between Laconic and a description of a single-tape machine? The concept of tapes in a multi-tape Turing machine and the concept of variables in standard imperative programming languages map to one another very nicely. The idea of the Laconic-to-TMD compiler is to encode the value of each variable on one tape. Then, each Laconic command that manipulates the value of one or more variables compiles down to a TMD function call that manipulates the tapes that correspond to those variables appropriately.

As an example, consider the following Laconic command:

```
a=b*c;
```

This Laconic command assigns the value of `b*c` to the variable `a`. It compiles down to the following TMD function call:

```
function BUILTIN_multiply a b c
```

This function call will result in `BUILTIN_multiply` being run on the three tapes `a`, `b`, and `c`. This will cause the symbols on tape `a` to take on a representation of an integer whose value is equal to bc.

In turn, the TMD code compiles directly to a string of bits that are written onto the tape at the start of the Turing machine's execution.

A TMD directory consists of three types of files:

1. The `functions` file. This file contains a list of the names of all the functions used by the TMD program. The top function in the file is pushed onto the stack at initialization. When this top function returns, the Turing machine halts.
2. The `initvar` file. This file contains the non-blank symbols that start in each register (or tape) at initialization.
3. Any files used to describe TMD functions. These files all end in a `.tfn` extension and only have any relevance to the compiled program if they show up in the functions file.

8 Compilation and Processing

There are two ways to think about the layout of the tape symbols: with a 4-symbol alphabet ({`_`, `1`, `H`, `E`}, blank symbol `_`), and with a 2-symbol alphabet ({`a`, `b`}, blank symbol `a`). The 2-symbol alphabet version is the one that's ultimately used for the results in this paper, since we advertised a Turing machine that used only two symbols. However, in nearly all parts of the Turing machine, the 2-symbol version of the machine is a direct translation of the 4-symbol version, according to the following mapping:

`_` ↔ `aa`
`1` ↔ `ab`
`H` ↔ `ba`
`E` ↔ `bb`

The sections that follow sometimes refer to the `ERROR` state. Transitions to the `ERROR` state should never be taken under any circumstances, and are useful for debugging purposes.

8.1 Concept

A directory of TMD functions is converted at compilation time to a string of bits to be written onto the tape, along with other states designed to interpret these bits. The resulting Turing machine has three main components, or *submachines*:

1. The *initializer* sets up the basic structure of the variable registers and the function stack.
2. The *printer* writes down the binary string that corresponds to the compiled TMD code.
3. The *processor* interprets the compiled binary, modifying the variable registers and the function stack as necessary.

The Turing machine's control flow proceeds from the initializer to the the printer to the interpreter. In other words, initializer states point only to initializer states or to printer states, printer states point only to printer states or to interpreter states, and interpreter states point only to interpreter states or the `HALT` state.

This division of labor, while seemingly straightforward, actually constitutes an important idea. The problem of the compiler is to convert a higher-level representation—a machine with many tapes, a larger alphabet, and a function stack—to the lower-level representation of a machine with a single tape, a 2-symbol alphabet and no function stack. The immediately obvious solution, and the one taught in every computability theory class as a proof of the equivalence of different kinds of Turing machines, is to have every "state" in the higher-level machine compile down to many states in the lower-level machine.

While simple, this approach is suboptimal in terms of the number of states. As is nearly always true when designing systems to be parsimonious, the clue that improvement is possible lies in the presence of repetition. Each state transition in the higher-level machine is converted to a group of lower-level states with the same basic structure. Why not instead explain how to perform this conversion exactly once, and then apply the conversion many times?

This idea is at the core of the division of labor described previously. We begin by writing a description of the higher-level machine onto the tape, and then "run" the higher-level machine by reading what is on the tape with a set of states that understands how to interpret the encoded higher-level machine. We refer to this idea as *on-tape processing.*

In this paper, we use TMD as the representation of the higher-level machine.[8] The printer writes the TMD program onto the tape, and the processor executes it. As a result of using this scheme, we incur a constant *additive* overhead—we have to include the processor in our final Turing machine—but we avoid the constant *multiplicative* overhead required for the naïve scheme.

Is this additive overhead small enough to be worth it? We found that it is. Our implementation of the processor requires 3,860 states. (See Section 8.5 for a detailed breakdown of the state cost by submachine.) In contrast to this additive overhead of 3,860, the naïve approach incurs a large multiplicative overhead that depends in part on how many states must be used to represent each higher-level state transition, and in part on how efficient an encoding scheme can be devised for the on-tape approach. The following table compares the performance of on-tape processing to the performance of an implementation that used the naïve approach. The comparison is shown for three kinds of machines: a machine that halts if and only if Goldbach's conjecture is false, a machine that halts if and only if the Riemann hypothesis is false, and a machine whose behavior is independent of ZFC.

Program	States (Naïve)	States (On-Tape Processing)
Goldbach	7,902	4,888
Riemann	36,146	5,372
ZFC	340,943	7,910

As can be seen from this table, on-tape interpretation results in huge gains, particularly in large and complex programs.

The subsections that follow describe each of the three submachines—the initializer, the printer, and the processor—in greater detail.

8.2 The Initializer

The initializer starts by writing a counter onto the tape which encodes how many registers there will be in the program. Using the value in that counter, it creates each register, with demarcation patterns between registers, and unique identifiers for each register. Each register's value begins with the pattern of non-blank symbols laid out in the `initvar` file. The initializer also creates the program counter, which starts at 0, and the function stack, which starts out with only a single function call to the top function in the `functions` file.

Figure 2 is a detailed diagram describing the tape's state when the initializer passes control to the printer.

---

[8] Note that instead of TMD, the on-tape processing scheme could be used for any language, assuming the designer provides both a processor and an encoding for that language. We chose TMD because it made the interpreter easy to write, but other minimalist languages, like Unlambda [17], Brainf*ck [20], or Iota and Jot [2], might be good candidates for parsimonious designs, with the additional advantage of being already known to some programmers! Thanks to Luke Schaeffer for this point.

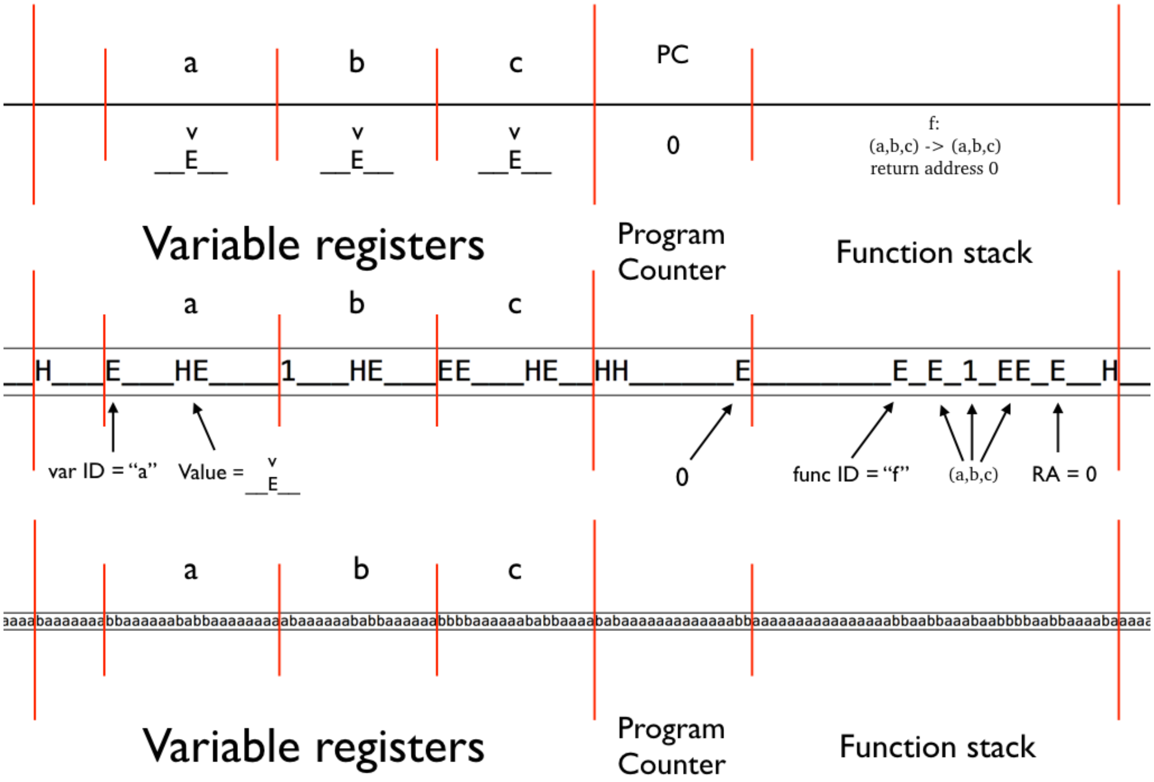

Figure 2: The state of the Turing machine tape after the initializer completes. The TMD program being expressed in Turing machine form is described in full in Appendix B. The top bar is a high-level description of what each part of the Turing machine tape represents. The middle bar is an encoding of the tape in the standard 4-symbol alphabet; the bottom bar is simply the translation of that tape into the 2-symbol alphabet. For a more detailed explanation of how to interpret the tape patterns, see [25].

8.3 The Printer

8.3.1 Specification

The printer writes down a long binary string which encodes the entirety of the TMD program onto the tape.

Figure 3 shows the tape's state when the printer passes control to the processor.

8.3.2 Introspection

Writing down a long binary string onto a Turing machine tape in a parsimonious fashion is not as straightforward as it might initially appear. The first idea that comes to mind is simply to use one state per symbol, with each state pointing to the next, as shown in Figure 4.

On closer examination, however, this approach is quite wasteful for all but the smallest binary files. Every `a` transition points to the next state in the sequence, and none of the `b` transitions are used at all! Indeed, the only information-bearing part of the state is the single bit contained in the choice of which symbol to write. But in theory, far more information than that could be encoded in each state. In a machine with n states, each state could contain $2(\log_2(n)+1)$ bits of information, because each of its two transitions could point to any of the n states, and write either an `a` or a `b` onto the tape. Of course, this is only in theory; in practice, to extract the information contained in therefore Turing machine's states and translate it into bits on the tape is nontrivial.

We will use a scheme originally conceived by Ben-Amram and Petersen [3] and refined further and suggested to us by Luke Schaeffer. It does not achieve the optimal theoretical encoding described above, but is relatively simple to implement and understand, and is within a factor of 2 of optimal for large binary strings. Schaeffer named Turing machines that use this idea *introspective.*

Introspection works as follows. If the binary string contains k bits, then let w be the *word size.* The word size w takes the largest value it can such that $w2^w \leq k$. We can split the binary string into $n_w = \left\lceil \frac{k}{w} \right\rceil$ *words* of w bits each (we can pad the last word with the blank symbol). In our scheme, each word in the bit-string is represented by a *data state.* Each data state points to the state representing the next word in the sequence for its `a` transition, but which state the `b` transition points to encodes the next word. Every `b` transition points to one of the last 2^w data states, thereby encoding w bits of information.

Of course, the encoding is useless until we specify how to extract the encoded bit-string from the data states. The extraction scheme works as follows. To query the i^{th} data state for the bits it encodes, we run the data states on the string $\mathtt{a}^{i-1}\mathtt{b}\mathtt{a}^\infty$ (a string of $i-1$ `a`'s followed by a `b` in the i^{th} position). After running the data states on that string, what remains on the tape is the string $\mathtt{b}^{i-1}\mathtt{a}\mathtt{b}^r\mathtt{a}^\infty$, assuming that the i^{th} data state pointed to the r^{th}-to-last data state. Thus, what we're left with is essentially a unary encoding of the "value" of the word in binary. Thus, the job of the extractor is to set up a binary counter which removes one `b` at a time and increments the counter appropriately. Then, afterward, the extractor reverts the tape back to the form $\mathtt{a}^i\mathtt{b}\mathtt{a}^\infty$, shifts all symbols on the tape over by w bits, and repeats the process. Finally, when the state beyond the last data state sees a `b` on the tape, we know that the process has completed, and we can pass control to the processor. Figure 5 shows the whole procedure.

How much have we gained by using introspection for encoding the program binary, instead of the naïve approach? It depends on how large the program binary is. Using introspection

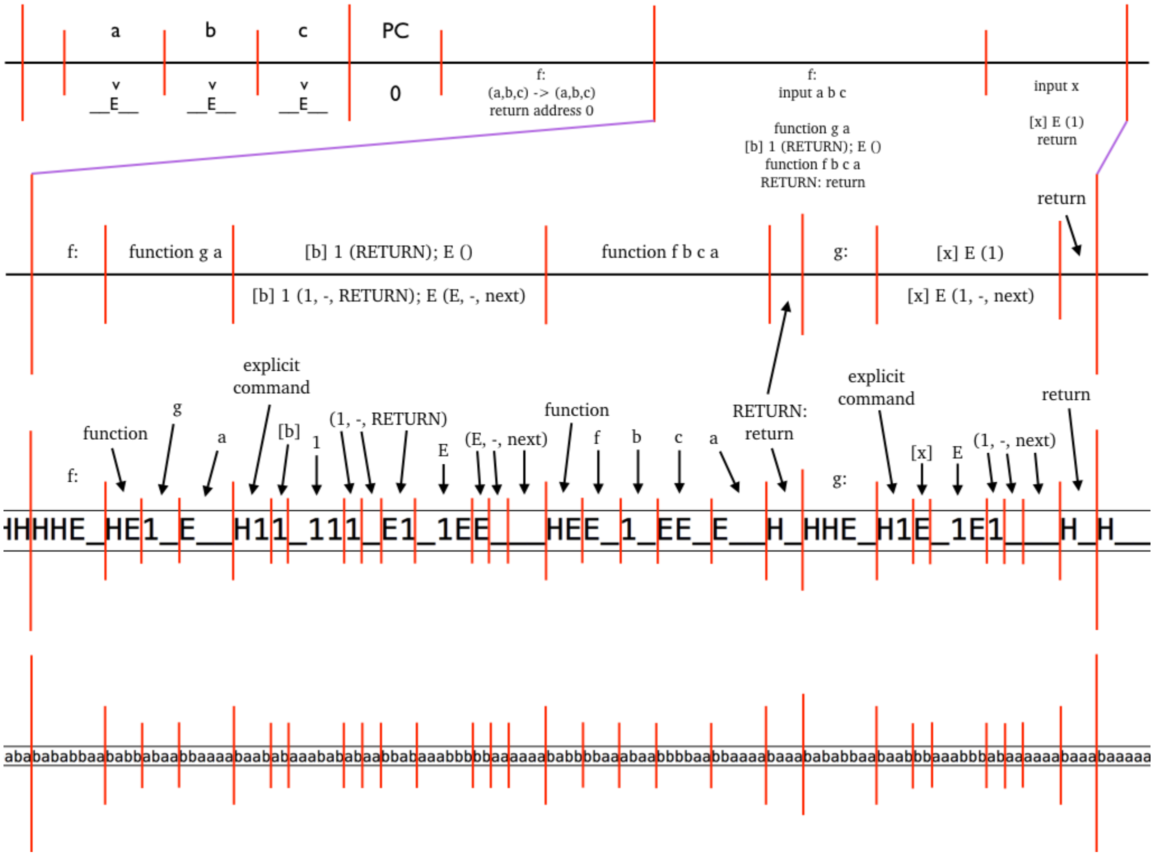

Figure 3: The state of the Turing machine tape after the printer completes. The TMD program being expressed in Turing machine form is described in full in Appendix B. The top bar is a high-level description of the entire tape; unfortunately, at this point there are so many symbols on the tape that it is impossible to see everything at once. For a detailed view of the first two-thirds of the tape (registers, program counter, and stack), see Figure 2. The bottom three bars show a zoomed-in view of the program binary. From the top, the second bar gives a high-level description of what each part of the program binary means; the third bar gives the direct correspondence between 4-symbol alphabet symbols on the tape and their meaning in TMD; the fourth and final bar gives the translation of the third bar into the 2-symbol alphabet. For a more detailed explanation of the encoding of TMD into tape symbols, see [25].

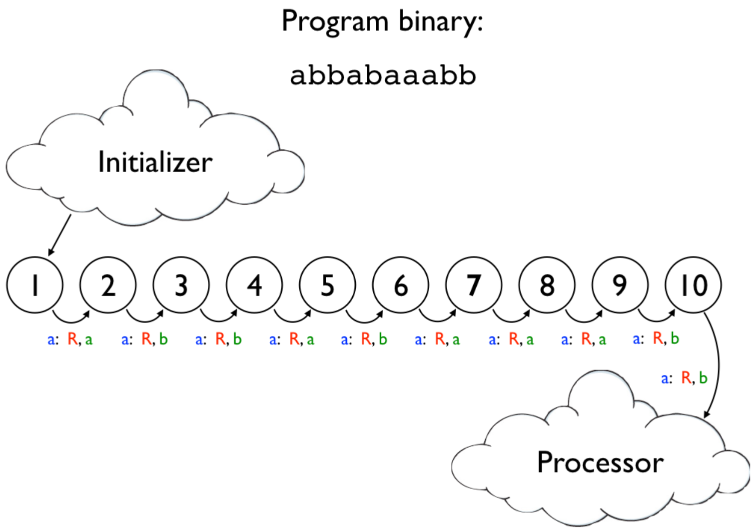

Figure 4: A naïve implementation of the printer. In this example, the hypothetical program is ten bits long, and the printer uses ten states, one for each bit. In the diagram, the blue symbol is the symbol that is read on a transition, the red letter indicates the direction the head moves, and the green symbol indicates the symbol that it written. Note the lack of transitions on reading a `b`; this is because in this implementation, the printer will only ever read the blank symbol, which is `a`, since the head is always proceeding to untouched parts of the tape. It therefore makes no difference what behavior the Turing machine adopts upon reading a `b` in states 1-10 (and therefore `b` transitions are presumed to lead to the `ERROR` state)

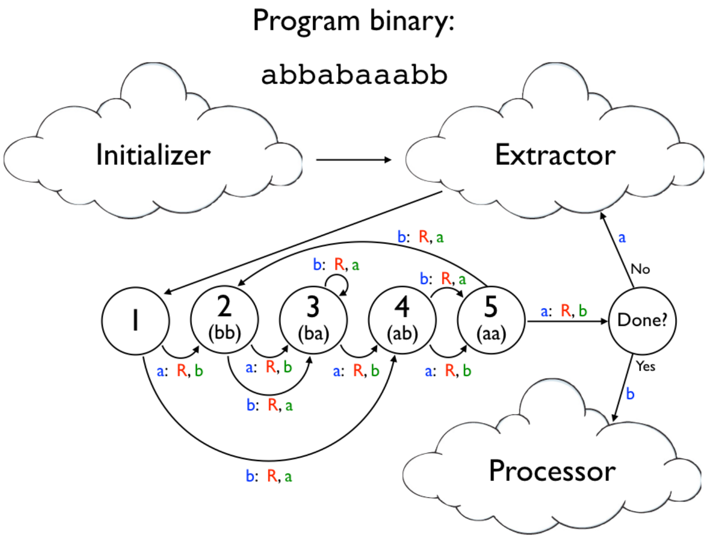

Figure 5: An introspective implementation of the printer. In this example, the hypothetical program is $k = 10$ bits long, and so the word size must be 2 (since $w = 2$ is the largest w such that $w2^w \leq 10$). There are therefore $n_w = \left\lceil \frac{k}{w} \right\rceil = 5$ data states, each encoding two bits. The `b` transitions carry the information about the encoding; note that each one only points to one of the last four data states. The last four data states have in parentheses what word we mean to encode if we point to them.

incurs an $O(\log k)$ *additive* overhead, because we have to include the extractor in our machine. (Our implementation of the extractor takes $10w + 17$ states. It's possible to build a constant-size extractor, but it's not worth it for our value of w) In return, we save a *multiplicative* factor of w (which scales with $\log k$) on the number of data states needed.

This is plainly not worth it for the 10-bit example binary shown in Figs. 4 and 5. For that binary, we require 69 additional states for the extractor in order to save 5 data states. For real programs, however, it is worth it, as can be seen from the following table.

Program	Binary Size	w	n_w	Extractor Size	States (Naïve)	States (Introspective)
Example TMD	116	4	29	57	116	86
Goldbach	4,964	9	552	107	4,964	659
Riemann	9,532	10	1,024	117	9,532	1,141
ZFC	38,864	11	3,534	127	38,864	3,661

One minor detail concerns the numbers presented for the Riemann program. Ordinarily, with a binary of size 9,532, we would opt to split the program into 1,060 words of 9 bits each plus a 107-state extractor, since 9 is the greatest w such that $w2^w < 9{,}532$. But because 9,532 is so close to the "magic number" 10,240, it's actually more parsimonious to pad the program with copies of the blank symbol until it's 10,240 bits long, and split it into 1,024 words of 10 bits each plus a 117-state extractor.

8.4 The Processor

The processor's job is to interpret the code written onto the tape and modify the variable registers and function stack accordingly. The processor does this by the following sequence of steps:

START:

1. Find the function call at the top of the stack. Mark the function f in the code whose ID matches that of the top function call.
2. Read the current program counter. Mark the line of code l in f whose line number matches the program counter.
3. Read l. Depending on what type of command l is, carry out one of the following three lists of tasks.

IF l IS AN EXPLICIT TAPE COMMAND:

1. Read the variable name off l. Index the variable name into the list of variables in the top function on the stack. This list of variables corresponds to the mapping between the function's local variables and the register names.
2. Match the indexed variable to its corresponding register r. Mark r. Read the symbol s_r to the right of the head marker in that register.
3. Travel back to l, remembering the value of s_r using states. Find and mark the reaction x corresponding to the symbol. See what symbol s_w should be written in response to reading s_r.

4. Travel back to r, remembering the value of s_w using states. Replace s_r with s_w.

5. Travel back to x. See which direction d the head should move in response to reading s_r.

6. Travel back to r, remembering the value of d using states. Move the head marker accordingly.

7. Travel back to x. See if a jump is specified. If a jump is specified, copy the jump address onto the program counter. Otherwise, increment the program counter by 1.

8. Go back to START.

IF l IS A FUNCTION CALL:

1. Write the function's name to the top of the stack.

2. For each variable in the function call, index the variable name into the list of variables in the top function on the stack. This list of variables corresponds to the mapping between the function's local variables and the register names. Push the corresponding register names in the order that they correspond to the variables in the function call.

3. Copy the current program counter to the return address of the newborn function call at the top of the stack.

4. Replace the current program counter with 0 (meaning "read the first line of code").

5. Go back to START.

IF l IS A RETURN STATEMENT:

1. Replace the current program counter with f's return address.

2. Increment the program counter by 1.

3. Erase the call to f from the top of the stack.

4. Check if the stack is now empty. If so, halt.

5. Go back to START.

8.5 Cost Analysis

It's worthwhile to analyze the relative contributions of the initializer, the printer, and the processor to the machine's final state count. The following table lists the number of states in each submachine for each of the four different TMD programs under discussion.

Program	Initializer	Printer	Processor	Total
Example TMD	349	86	3,860	4,295
Goldbach	369	659	3,860	4,888
Riemann	371	1,141	3,860	5,372
ZFC	389	3,661	3,860	7,910

As can be seen from this table, the processor makes the largest contribution to all four programs. Improving the processor, therefore, is probably the best approach for improving upon the bounds we present. Equally clear, however, is that for programs more complicated than the ones presented here, the cost of the printer will grow almost linearly but the cost of the processor will stay the same. The cost of the initializer grows very slightly with the complexity of programs because of the need to initialize additional registers.

Improving the printer, and with it the TMD and Laconic languages, is probably the best approach for reducing state count for very large and complex programs.

9 Future Work

This paper still leaves a three orders-of-magnitude gap between the smallest n, namely 7,910, for which $BB(n)$ is known to be independent of ZF set theory, and the largest n, namely 4, for which $BB(n)$ is known to be determinable. We regard it as a fascinating problem to pin down the truth here: for example, is it conceivable that $BB(10)$ or even $BB(6)$ might be independent of ZF? If so, that would arguably force a qualitative change in our understanding of the Gödel incompleteness phenomenon—showing that incompleteness from strong set theories rears its head for much simpler arithmetical questions than had previously been known.

A more immediate question is how much further Z's state count can be reduced. It seems very likely that Z's state count can, will (assuming further inquiry) be reduced. There are a variety of ways that this might happen, but the most promising approach is probably to adapt the processor-printer model to use a better language than TMD—a language whose processor contains fewer states, whose binary encodings of programs will require fewer bits, or ideally, both. A few ideas have been suggested [1], but the most popular suggestion is some sort of Lambda-Calculus-based processor.

Other future work might involve further use of our Laconic language to upper-bound the 'complexities' of mathematical statements and algorithms, in as standardized and model-independent a way as possible. Perhaps Laconic could be used to measure the complexity of other well-known conjectures, or even to compare different algorithms for solving the same problem to each other (e.g., to try to quantify the notion that an insertion sort is simpler than a merge sort)!

10 Acknowledgements

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[A link to a GitHub repository containing all programs and Turing machines related to this paper, with accompanying documentation.]

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[A place where programmers go for recreational code golfing]

Appendices

A Example Laconic Program: Goldbach's Conjecture

The following is an example Laconic program, which compiles down to the Turing machine G mentioned in Section 4 (which halts if and only Goldbach's Conjecture is false).

```
func zero(x) {
    x = 0;
    return;
}
```

```
func one(x) {
    x = 1;
    return;
}

func incr(x) {
    x = x + 1;
    return;
}

/* Computes x modulo y */
func modulus(x, y, out) {
    out = x;

    while (out >= y) {
        out = out - y;
    }

    return;
}

func assignXtoYminusX(x, y) {
    x = y - x;
    return;
}

/* Figures out if x is prime, and puts the output in y */
/* Does not modify x, modifies y */
func isPrime(x, h, y) {
    if (x == 1) {
        zero(y);
        return;
    }

    y = 2;

    while (x > y) {
        modulus(x, y, h);

        if (h == 0) {
            zero(y);
            return;
        }
        incr(y);
    }

    return;
}

int evenNumber;
int primeCounter;
int isThisOnePrime;
int foundSum;
int h;

evenNumber = 2;
one(foundSum);

while (foundSum) {
    zero(foundSum);
    evenNumber = evenNumber + 2;
    one(primeCounter);

    while (primeCounter < evenNumber) {
```

```
        isPrime(primeCounter, h, isThisOnePrime);

        if (isThisOnePrime) {
            assignXtoYminusX(primeCounter, evenNumber);
            isPrime(primeCounter, h, isThisOnePrime);
            assignXtoYminusX(primeCounter, evenNumber);

            if (isThisOnePrime) {
                print evenNumber;
                print primeCounter;

                one(foundSum);
            }
        }

        incr(primeCounter);
    }
}

halt;
```

For detailed documentation of the Laconic programming language, see [25]. To find this file specifically, navigate to `parsimony/src/laconic/laconic_files/goldbach.lac` at [25].

B Example TMD Program

The following is an example TMD directory, which compiles down to a binary string to be written on a Turing machine tape. It's the example used in illustrations throughout this paper, most notably in the example compilation shown in Figs. 2 and 3. The program calls itself recursively three times until the starting symbol on each tape, `E`, is replaced with a `1`, at which point the program halts.

This TMD directory is called `example_tmd_dir`, and contains four files: `f.tmd`, `g.tmd`, `initvar`, and `functions`.

`f.tmd`:

```
input a b c

// Recursively writes a 1 on every tape.

function g a
[b] 1 (RETURN); E ()
function f b c a
RETURN: return
```

`g.tmd`:

```
input x

// Writes a 1 on the input tape.

[x] E (1)
return
```

`functions`:

```
f
g
```

`initvar`:

E

For detailed documentation of the TMD programming language, see [25]. To find this directory specifically, navigate to `parsimony/src/tmd/tmd_dirs/example_tmd_dir/` at [25].

C Explicit Listing of Z

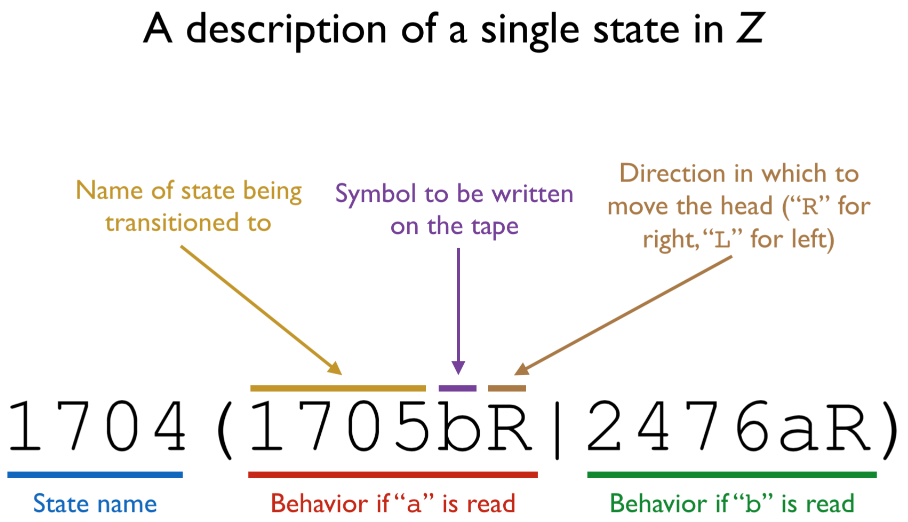

Figure 6: This figure explains how to read a description of a single state. Note that "`ERROR-`" or "`HALT--`" denote transitions to the `ERROR` or `HALT` states, respectively (no further information is provided because what symbol is written and which direction the head moves are at that point irrelevant).

We present below an explicit listing of Z. For a more easily readable version of Z, complete with descriptive state names, see [25].

We ran this Turing machine for 10,000,000,000 steps (more than half a day on our simulators) and within that time it did not halt. We note, however, that Z was designed for parsimony rather than efficiency, and that this "experiment" is of little consequence! We similarly ran a Turing machine designed to test the conjecture that all perfect squares are less than 5, and it ran for 2,895,083,899 steps (a couple hours on our simulator) before it found the counterexample 9 and halted.

Figure 6 explains how to interpret the description shown below. In addition, note the following:

1. The tape has a 2-symbol alphabet, with tape symbols {`a`, `b`} and blank symbol `a` (in other words, `a` is the only symbol that can appear an infinite times on the tape).
2. The start state of Z is `0000`.

3. Z will never transition to the `ERROR` state. Any transition to the `ERROR` state could be replaced by a transition to any other state (including `HALT`) and the Turing machine's behavior would remain identical.

4. Z contains only one transition to the `HALT` state, out of state `7862`.

```
0000(0001bR|ERROR-) 0001(0004bR|ERROR-) 0002(0003bR|ERROR-) 0003(0012aR|0012bR) 0004(0005aR|ERROR-) 0005(0006bR|ERROR-) 0006(0007bR|ERROR-) 0007(0008bR|ERROR-) 0008(0009bR|ERROR-) 0009(0010bR|ERROR-)
0010(0011bR|ERROR-) 0011(0002bR|ERROR-) 0012(0013aR|ERROR-) 0013(0014aR|0014bR) 0014(0015aR|ERROR-) 0015(0057aR|0057bR) 0016(0017bR|ERROR-) 0017(0018bR|ERROR-) 0018(0019aR|ERROR-) 0019(0020aR|0020bR)
0020(0021aR|ERROR-) 0021(0022aR|0022bR) 0022(0023aR|ERROR-) 0023(0024aR|0024bR) 0024(0025aR|ERROR-) 0025(0067aR|0067bR) 0026(0027aR|0032bR) 0027(0028aL|0030bL) 0028(0029aL|0029bL) 0029(0026aL|0026bL)
0030(0031aL|0031bL) 0031(0026aL|0026bL) 0032(0033aL|0035bL) 0033(0034aL|0034bL) 0034(0037aL|0037bL) 0035(0036aL|0036bL) 0036(0026aL|0026bL) 0037(0038aR|0041bR) 0038(0044aR|0039bL) 0039(0040bR|0040bR)
0040(0049aL|0049bL) 0041(ERROR-|0042bL) 0042(0043aL|0043aL) 0043(0037aL|0037bL) 0044(0045aR|0048bR) 0045(ERROR-|0046aL) 0046(0047aR|0047aR) 0047(0049aL|0049bL) 0048(0071aR|ERROR-) 0049(0050aR|0051bR)
0050(0049aR|0049bR) 0051(0052aR|0049bR) 0052(0053aR|0054bR) 0053(0052aR|0052bR) 0054(0055aL|0052bR) 0055(0056aR|0056aR) 0056(0012aR|0012bR) 0057(0058aR|ERROR-) 0058(0059aR|0059bR) 0059(0060aR|ERROR-)
0060(0061aR|0061bR) 0061(0062aR|ERROR-) 0062(0063aR|0063bR) 0063(0064aR|ERROR-) 0064(0065aR|0065bR) 0065(0066aR|ERROR-) 0066(0016aR|0016bR) 0067(0068bR|ERROR-) 0068(0069bR|ERROR-) 0069(0070bL|ERROR-)
0070(0026aL|0026bL) 0071(0072aR|0075bR) 0072(0071aR|0073bL) 0073(0074aR|0074bR) 0074(0078aL|0078bL) 0075(ERROR-|0076bL) 0076(0077aR|0077bR) 0077(0078aL|0078bL) 0078(0079aR|0082bR) 0079(0080aL|0078bR)
0080(0081bR|0081bR) 0081(0083aR|0083bR) 0082(0078aR|0078bR) 0083(0084aR|0085bR) 0084(0083aR|0083bR) 0085(0086aL|0083bR) 0086(0087aL|0087bL) 0087(0088aL|0088bL) 0088(0089aR|0094bR) 0089(0090aL|0092bL)
0090(0091aR|0091bR) 0091(0099aL|0099bL) 0092(0093aL|0093bL) 0093(0088aL|0088bL) 0094(0095aL|0097bL) 0095(0096aL|0096bL) 0096(0088aL|0088bL) 0097(0098aL|0098bL) 0098(0088aL|0088bL) 0099(0100aR|0105bR)
0100(0101aL|0103bL) 0101(0102aL|0102bL) 0102(0099aL|0099bL) 0103(0104aR|0104bR) 0104(0110aL|0110bL) 0105(0106aL|0108bL) 0106(0107aR|0107aR) 0107(0139aL|0139bL) 0108(0109aR|0109bR) 0109(0110aL|0110bL)
0110(0111aR|0116bR) 0111(0112bL|0114bL) 0112(0113bL|0113bL) 0113(0130aL|0130bL) 0114(0115bL|0115bL) 0115(0110aL|0110bL) 0116(ERROR-|0117bL) 0117(0118aR|0118aR) 0118(0119aL|0119bL) 0119(0120aR|0125bR)
0120(0121aL|0123bL) 0121(0122aR|0122bR) 0122(0130aL|0130bL) 0123(0124aL|0124bL) 0124(0119aL|0119bL) 0125(0126aL|0128bL) 0126(0127aL|0127bL) 0127(0119aL|0119bL) 0128(0129aL|0129bL) 0129(0119aL|0119bL)
0130(0131aR|0136bR) 0131(0132aL|0134bL) 0132(0133aL|0133bL) 0133(0130aL|0130bL) 0134(0135aR|0135bR) 0135(0088aL|0088bL) 0136(ERROR-|0137bL) 0137(0138aR|0138bR) 0138(0088aL|0088bL) 0139(0140aR|0143bR)
0140(0139aR|0141bL) 0141(0142aR|0142bR) 0142(0146aL|0146bL) 0143(ERROR-|0144bL) 0144(0145aR|0145bR) 0145(0146aL|0146bL) 0146(0147aR|0150bR) 0147(0148aL|0146bR) 0148(0149aR|0149bR) 0149(0071aL|0071bL)
0150(0151aL|0146bR) 0151(0152aR|0152aR) 0152(0153aR|0153bR) 0153(0154aR|ERROR-) 0154(0155aR|0155bR) 0155(0156bL|ERROR-) 0156(0157aL|0157bL) 0157(0158aR|0163bR) 0158(0159aL|0161bL) 0159(0160aL|0160bL)
0160(0157aL|0157bL) 0161(0162aR|0162bR) 0162(0166aL|0166bL) 0163(0197aR|0164bL) 0164(0165aR|0165bR) 0165(0166aL|0166bL) 0166(0167aR|0172bR) 0167(0168aL|0170bL) 0168(0169bL|0169bL) 0169(0177aL|0177bL)
0170(0171aL|0171bL) 0171(0166aL|0166bL) 0172(0173aL|0175bL) 0173(0174aL|0174bL) 0174(0166aL|0166bL) 0175(0176aL|0176bL) 0176(0166aL|0166bL) 0177(0178aR|0183bR) 0178(0179aL|0181bL) 0179(0180aL|0180bL)
0180(0177aL|0177bL) 0181(0182aR|0182bR) 0182(0186aL|0186bL) 0183(ERROR-|0184bL) 0184(0185aR|0185bR) 0185(0186aL|0186bL) 0186(0187aR|0192bR) 0187(0188aL|0190bL) 0188(0189aR|0189bR) 0189(0157aL|0157bL)
0190(0191aL|0191bL) 0191(0186aL|0186bL) 0192(0193aL|0195bL) 0193(0194aL|0194bL) 0194(0186aL|0186bL) 0195(0196aL|0196bL) 0196(0186aL|0186bL) 0197(0198aR|0199bR) 0198(0197aR|0197bR) 0199(0200aR|0197bR)
0200(0201aR|0204bR) 0201(0202aL|0197bR) 0202(0203bR|0203bR) 0203(0205aR|0205bR) 0204(0200aR|0197bR) 0205(0206aR|ERROR-) 0206(0207aR|0207bR) 0207(0208aR|ERROR-) 0208(0209aR|0209bR) 0209(0210aR|ERROR-)
0210(0219aR|0219bR) 0211(0212bR|ERROR-) 0212(0213bR|ERROR-) 0213(0214aR|ERROR-) 0214(0233aR|0233bR) 0215(0216bR|ERROR-) 0216(0217bR|ERROR-) 0217(0218bL|ERROR-) 0218(0263aL|0263bL) 0219(0220aR|ERROR-)
0220(0221aR|0221bR) 0221(0222aR|ERROR-) 0222(0223aR|0223bR) 0223(0224aR|ERROR-) 0224(0225aR|0225bR) 0225(0226aR|ERROR-) 0226(0227aR|0227bR) 0227(0228aR|ERROR-) 0228(0229aR|0229bR) 0229(0230aR|ERROR-)
0230(0231aR|0231bR) 0231(0232aR|ERROR-) 0232(0211aR|0211bR) 0233(0234bR|ERROR-) 0234(0237bR|ERROR-) 0235(0236bR|ERROR-) 0236(0245aR|0245bR) 0237(0238aR|ERROR-) 0238(0239bR|ERROR-) 0239(0240bR|ERROR-)
0240(0241bR|ERROR-) 0241(0242bR|ERROR-) 0242(0243bR|ERROR-) 0243(0244aR|ERROR-) 0244(0235bR|ERROR-) 0245(0246aR|ERROR-) 0246(0247aR|0247bR) 0247(0248aR|ERROR-) 0248(0249aR|0249bR) 0249(0250aR|ERROR-)
0250(0251aR|0251bR) 0251(0252aR|ERROR-) 0252(0253aR|0253bR) 0253(0254aR|ERROR-) 0254(0255aR|0255bR) 0255(0256aR|ERROR-) 0256(0257aR|0257bR) 0257(0258aR|ERROR-) 0258(0259aR|0259bR) 0259(0260aR|ERROR-)
0260(0261aR|0261bR) 0261(0262aR|ERROR-) 0262(0215aR|0215bR) 0263(0264aR|0269bR) 0264(0265aL|0267bL) 0265(0266aL|0266bL) 0266(0263aL|0263bL) 0267(0268aL|0268bL) 0268(0263aL|0263bL) 0269(0270aL|0272bL)
0270(0271aL|0271bL) 0271(0274aL|0274bL) 0272(0273aL|0273bL) 0273(0263aL|0263bL) 0274(0275aR|0278bR) 0275(0281aR|0276bL) 0276(0277bR|0277bR) 0277(0288aL|0288bL) 0278(ERROR-|0279bL) 0279(0280aL|0280aL)
0280(0274aL|0274bL) 0281(0282aR|0285bR) 0282(ERROR-|0283aL) 0283(0284aR|0284aR) 0284(0288aL|0288bL) 0285(0286aL|ERROR-) 0286(0287aR|0287aR) 0287(0300aR|0300bR) 0288(0289aR|0290bR) 0289(0288aR|0288bR)
0290(0291aR|0288bR) 0291(0292aR|0293bR) 0292(0291aR|0291bR) 0293(0294aL|0291bR) 0294(0295aR|0295aR) 0295(0296aR|0296bR) 0296(0297bR|ERROR-) 0297(0298bR|ERROR-) 0298(0299bL|ERROR-) 0299(0263aL|0263bL)
0300(0301aR|0302bR) 0301(0300aR|0300bR) 0302(0300aR|0303bR) 0303(0304aR|0305bR) 0304(0386aR|0386bR) 0305(0386aR|0386bR) 0306(0307bR|ERROR-) 0307(0308aR|0308bR) 0308(0309aR|0310bR) 0309(0308aR|0308bR)
0310(0311aL|0308bR) 0311(0312aL|0312bL) 0312(0313aL|0313bL) 0313(0314aR|0317bR) 0314(0338aR|0315bL) 0315(0316bL|0316bL) 0316(0313aL|0313bL) 0317(0338aR|0318bL) 0318(0319aR|0319aR) 0319(0360aL|0360bL)
0320(0321aR|0324bR) 0321(0320aR|0322aL) 0322(0323aR|0323aR) 0323(0329aR|0329bR) 0324(0325aL|0327aL) 0325(0326aR|0326aR) 0326(0347aR|0347bR) 0327(0328aR|0328aR) 0328(0338aR|0338bR) 0329(0330aR|0333bR)
0330(0331bL|0329bR) 0331(0332aR|0332aR) 0332(0320aR|0320bR) 0333(0334bL|0336bL) 0334(0335aR|0335aR) 0335(0347aR|0347bR) 0336(0337aR|0337aR) 0337(0338aR|0338bR) 0338(0339aR|0344bR) 0339(0340bL|0342bL)
0340(0341bR|0341bR) 0341(0320aR|0320bR) 0342(0343bR|0343bR) 0343(0329aR|0329bR) 0344(0345bL|0338bR) 0345(0346bR|0346bR) 0346(0347aR|0347bR) 0347(0348bL|ERROR-) 0348(0349aL|0349bL) 0349(0350aR|0355bR)
0350(0351aL|0353bL) 0351(0352aL|0352bL) 0352(0349aL|0349bL) 0353(0354aR|0354bR) 0354(0313aL|0313bL) 0355(0356aL|0358bL) 0356(0357aR|0357aR) 0357(0371aR|0371bR) 0358(0359aL|0359bL) 0359(0349aL|0349bL)
0360(0361aR|0366bR) 0361(0362aL|0364bL) 0362(0363aL|0363bL) 0363(0313aL|0313bL) 0364(0365aL|0365bL) 0365(0360aL|0360bL) 0366(0367aL|0369bL) 0367(0368aR|0368aR) 0368(0371aR|0371bR) 0369(0370aL|0370bL)
0370(0360aL|0360bL) 0371(0372aR|0375bR) 0372(0373aL|0371bR) 0373(0374bR|0374bR) 0374(0308aR|0308bR) 0375(0376aL|0371bR) 0376(0377aR|0377aR) 0377(0378aR|0378bR) 0378(0379bR|ERROR-) 0379(0380bR|ERROR-)
0380(0381aR|ERROR-) 0381(0382aR|0382bR) 0382(0383aR|ERROR-) 0383(0384aR|0384bR) 0384(0385bR|ERROR-) 0385(0389aR|0389bR) 0386(0387aR|0388bR) 0387(0306aR|0306bR) 0388(0306aR|0306bR) 0389(0390aR|ERROR-)
0390(0391bL|ERROR-) 0391(0395aR|ERROR-) 0392(0393aL|0392aL) 0393(0393aL|0394bR) 0394(4050aR|ERROR-) 0395(0396bR|2559aR) 0396(0397bR|3077aR) 0397(0398bR|2269aR) 0398(0399bR|2637aR) 0399(0400bR|3293aR)
0400(0401bR|2588aR) 0401(0402bR|3610aR) 0402(0403bR|2065aR) 0403(0404bR|2265aR) 0404(0405bR|1939aR) 0405(0406bR|3411aR) 0406(0407bR|2579aR) 0407(0408bR|2234aR) 0408(0409bR|2070aR) 0409(0410bR|2234aR)
0410(0411bR|2067aR) 0411(0412bR|2259aR) 0412(0413bR|2587aR) 0413(0414bR|3002aR) 0414(0415bR|3737aR) 0415(0416bR|1978aR) 0416(0417bR|2569aR) 0417(0418bR|3185aR) 0418(0419bR|2440aR) 0419(0420bR|2654aR)
0420(0421bR|2617aR) 0421(0422bR|3757aR) 0422(0423bR|2820aR) 0423(0424bR|3838aR) 0424(0425bR|2649aR) 0425(0426bR|3293aR) 0426(0427bR|2637aR) 0427(0428bR|3293aR) 0428(0429bR|2585aR) 0429(0430bR|2259aR)
0430(0431bR|2587aR) 0431(0432bR|3002aR) 0432(0433bR|3737aR) 0433(0434bR|1978aR) 0434(0435bR|2569aR) 0435(0436bR|3185aR) 0436(0437bR|2440aR) 0437(0438bR|2654aR) 0438(0439bR|2617aR) 0439(0440bR|3753aR)
0440(0441bR|2820aR) 0441(0442bR|3838aR) 0442(0443bR|2651aR) 0443(0444bR|3293aR) 0444(0445bR|2637aR) 0445(0446bR|3293aR) 0446(0447bR|2585aR) 0447(0448bR|3283aR) 0448(0449bR|2587aR) 0449(0450bR|3002aR)
0450(0451bR|3737aR) 0451(0452bR|1978aR) 0452(0453bR|2569aR) 0453(0454bR|3185aR) 0454(0455bR|2440aR) 0455(0456bR|2654aR) 0456(0457bR|2617aR) 0457(0458bR|3741aR) 0458(0459bR|2820aR) 0459(0460bR|3834aR)
0460(0461bR|2012aR) 0461(0462bR|3183aR) 0462(0463bR|1892aR) 0463(0464bR|1902aR) 0464(0465bR|3595aR) 0465(0466bR|3698aR) 0466(0467bR|2083aR) 0467(0468bR|3678aR) 0468(0469bR|3420aR) 0469(0470bR|2224aR)
0470(0471bR|2441aR) 0471(0472bR|2250aR) 0472(0473bR|2651aR) 0473(0474bR|2269aR) 0474(0475bR|2587aR) 0475(0476bR|3002aR) 0476(0477bR|2582aR) 0477(0478bR|2138aR) 0478(0479bR|2788aR) 0479(0480bR|3278aR)
0480(0481bR|2657aR) 0481(0482bR|3294aR) 0482(0483bR|2075aR) 0483(0484bR|3283aR) 0484(0485bR|2059aR) 0485(0486bR|3434aR) 0486(0487bR|2859aR) 0487(0488bR|3434aR) 0488(0489bR|2696aR) 0489(0490bR|2458aR)
0490(0491bR|2945aR) 0491(0492bR|3434aR) 0492(0493bR|2865aR) 0493(0494bR|2208aR) 0494(0495bR|1939aR) 0495(0496bR|1910aR) 0496(0497bR|2646aR) 0497(0498bR|1966aR) 0498(0499bR|2115aR) 0499(0500bR|3762aR)
0500(0501bR|2789aR) 0501(0502bR|3006aR) 0502(0503bR|2657aR) 0503(0504bR|3294aR) 0504(0505bR|2075aR) 0505(0506bR|3283aR) 0506(0507bR|2587aR) 0507(0508bR|2238aR) 0508(0509bR|2859aR) 0509(0510bR|1978aR)
0510(0511bR|2441aR) 0511(0512bR|2999aR) 0512(0513bR|3427aR) 0513(0514bR|2330aR) 0514(0515bR|3449aR) 0515(0516bR|3760aR) 0516(0517bR|3745aR) 0517(0518bR|1967aR) 0518(0519bR|1916aR) 0519(0520bR|3254aR)
0520(0521bR|2695aR) 0521(0522bR|1969aR) 0522(0523bR|1929aR) 0523(0524bR|2993aR) 0524(0525bR|2533aR) 0525(0526bR|2224aR) 0526(0527bR|3205aR) 0527(0528bR|2160aR) 0528(0529bR|2070aR) 0529(0530bR|2234aR)
0530(0531bR|2449aR) 0531(0532bR|3394aR) 0532(0533bR|2569aR) 0533(0534bR|3249aR) 0534(0535bR|2437aR) 0535(0536bR|2208aR) 0536(0537bR|3207aR) 0537(0538bR|3294aR) 0538(0539bR|2637aR) 0539(0540bR|2910aR)
0540(0541bR|3595aR) 0541(0542bR|3690aR) 0542(0543bR|2939aR) 0543(0544bR|3437aR) 0544(0545bR|2659aR) 0545(0546bR|3394aR) 0546(0547bR|2657aR) 0547(0548bR|3360aR) 0548(0549bR|3193aR) 0549(0550bR|3437aR)
0550(0551bR|2657aR) 0551(0552bR|3397aR) 0552(0553bR|2044aR) 0553(0554bR|2933aR) 0554(0555bR|2407aR) 0555(0556bR|3287aR) 0556(0557bR|3033aR) 0557(0558bR|1977aR) 0558(0559bR|2107aR) 0559(0560bR|2334aR)
0560(0561bR|3428aR) 0561(0562bR|2911aR) 0562(0563bR|2565aR) 0563(0564bR|3190aR) 0564(0565bR|2695aR) 0565(0566bR|2282aR) 0566(0567bR|3077aR) 0567(0568bR|1978aR) 0568(0569bR|2708aR) 0569(0570bR|2992aR)
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3940(3942aR|3941aR) 3941(3942aR|3941bR) 3942(3944aR|3943aR) 3943(3945bL|3944aR) 3944(3945aL|ERROR-) 3945(3946aL|3945bL) 3946(3947aR|3946bL) 3947(3948aR|3948aR) 3948(3949aR|3948bR) 3949(3951aR|3950aR)
3950(3952bL|3950bR) 3951(3952aL|ERROR-) 3952(3953aL|3952bL) 3953(3954aR|3953bL) 3954(3956aR|3955aR) 3955(3956bR|3955bR) 3956(3958aR|3957aR) 3957(3959bL|3957bR) 3958(3959aL|ERROR-) 3959(3960aL|3959bL)
3960(3961aR|3960bL) 3961(3963aR|3962aR) 3962(3963aR|3962bR) 3963(3965aR|3964aR) 3964(3966bR|3964bR) 3965(3966bL|ERROR-) 3966(3967aL|3966bL) 3967(3968aL|3967bL) 3968(3970aR|3969aR) 3969(3970bR|3969bR)
3970(3972aR|3971aR) 3971(3972bR|ERROR-) 3972(3974aL|3973bL) 3973(3974aR|3974bL) 3974(3976aR|3975aR) 3975(3977bR|3976bR) 3976(3977aR|3976bR) 3977(3979aL|3978bR) 3978(3980bL|3978bL) 3979(3980aL|ERROR-)
3980(3981aR|3980bL) 3981(3982aR|3981bL) 3982(3984aR|3983bR) 3983(3984aR|3983bR) 3984(3986aR|3985aR) 3985(3987bL|3985bR) 3986(3987aL|ERROR-) 3987(3988aR|3987bL) 3988(3989aR|3988bL) 3989(3991aR|3990aR)
3990(3991bR|3990bR) 3991(3993aR|3992aR) 3992(3994bL|3992bR) 3993(3994aL|ERROR-) 3994(3995aL|3994bL) 3995(3996aR|3995bL) 3996(3998aR|3997aR) 3997(3998bR|3997bR) 3998(4000aR|3999aR) 3999(4001bL|3999bR)
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4010(4014aR|4011aL) 4011(4012aL|4011bL) 4012(4017aL|4012bL) 4013(4009aR|4013bR) 4014(4015bL|ERROR-) 4015(4016aL|ERROR-) 4016(0395aR|4016aL) 4017(4019bL|4018aL) 4018(4021aL|4022bL) 4019(4022aL|4022bL)
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4050(4051aR|ERROR-) 4051(4052aR|4052bR) 4052(4053bL|ERROR-) 4053(4054aL|4054bL) 4054(4055aR|4060bR) 4055(4056aL|4058bL) 4056(4057aL|4057bL) 4057(4054aL|4054bL) 4058(4059aL|4059bL) 4059(4054aL|4054bL)
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4070(4071aL|4071aR) 4071(4054aL|4054bL) 4072(4073aR|4076bR) 4073(4072aR|4074aR) 4074(4075aR|4075aR) 4075(4081aR|4081bR) 4076(4077aL|4079aL) 4077(4078aR|4078aR) 4078(4108aR|4108bR) 4079(4080aR|4080aR)
4080(4099bR|4099bR) 4081(4082bR|4085bR) 4082(4083bL|4081bR) 4083(4084aR|4084aR) 4084(4072bR|4072bR) 4085(4086bL|4088bL) 4086(4087aR|4087aR) 4087(4090aR|4090bR) 4088(4089aR|4089aR) 4089(4099aR|4099bR)
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4140(4144aR|4139bR) 4141(4142aL|4139bR) 4142(4143aR|4143aR) 4143(4149aR|4149bR) 4144(4145aR|4146bR) 4145(4144aR|4139bR) 4146(4147aL|4139bR) 4147(4148aL|4148bL) 4148(4152aL|4152bL) 4149(4150aR|4151bR)
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4790(4730aL|4730bL) 4791(4792aR|4797bR) 4792(4793aL|4795bL) 4793(4794aR|4794bR) 4794(4741aL|4741bL) 4795(4796aL|4796bL) 4796(4730aL|4730bL) 4797(4798aL|4800bL) 4798(4799aL|4799bL) 4799(4730aL|4730bL)
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4820(4821aL|4821bL) 4821(4815aL|4815bL) 4822(4823aL|4823bL) 4823(4815aL|4815bL) 4824(4825aR|4828bR) 4825(ERROR-|4826aL) 4826(4827bR|4827bR) 4827(4833aR|4833bR) 4828(4829aL|4831aL) 4829(4830aR|4830bR)
4830(4851aL|4851bL) 4831(4832bR|4832bR) 4832(4840aR|4840bR) 4833(4834aR|4835bR) 4834(ERROR-|4833bR) 4835(4836bL|4838bL) 4836(4837aR|4837aR) 4837(4851aL|4851bL) 4838(4839aR|4839aR) 4839(4840aR|4840bR)
4840(4841aR|4844bR) 4841(ERROR-|4842bL) 4842(4843bR|4843bR) 4843(4833aR|4833bR) 4844(4845bL|4840bR) 4845(4846bR|4846bR) 4846(4851aL|4851bL) 4847(ERROR-|4848bR) 4848(4854aR|4854bR) 4849(ERROR-|4850bR)
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4890(4891bL|ERROR-) 4891(4892aR|4897bR) 4892(4893aL|4895bL) 4893(4894aL|4894bL) 4894(4891aL|4891bL) 4895(4896aL|4896bL) 4896(4891aL|4891bL) 4897(4802aR|4898bL) 4898(4899aL|4899bL) 4899(4891aL|4891bL)
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4920(4921aR|4922bR) 4921(4923aR|4920bR) 4922(4920aR|4920bR) 4923(4924aR|4929bR) 4924(4925aL|4927bL) 4925(4926bL|4926bL) 4926(4932aL|4932bL) 4927(4928aR|4928bR) 4928(4920aL|4920bL) 4929(ERROR-|4930bL)
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4940(4949aL|4949bL) 4941(4942aL|4942bL) 4942(4932aL|4932bL) 4943(4944aR|4945bR) 4944(4946aR|4946bR) 4945(4946aR|4946bR) 4946(4947aR|4948bR) 4947(4917aR|4917bR) 4948(4917aR|4917bR) 4949(4950aR|4953bR)
4950(4958aR|4951bL) 4951(4952aL|4952bL) 4952(4949aL|4949bL) 4953(4954aL|4956bL) 4954(4955aL|4955bL) 4955(4949aL|4949bL) 4956(4957aL|4957bL) 4957(4949aL|4949bL) 4958(4959aR|4962bR) 4959(ERROR-|4960aL)
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4970(4971aR|4971aR) 4971(4981aL|4981bL) 4972(4973aR|4973aR) 4973(4974aR|4974bR) 4974(4975aR|4978bR) 4975(ERROR-|4976bL) 4976(4977bR|4977bR) 4977(4967aR|4967bR) 4978(4979bL|4974bR) 4979(4980bR|4980bR)
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5100(5117aL|5117bL) 5101(5102aR|5102aR) 5102(5110aR|5110bR) 5103(5104aR|5105bR) 5104(ERROR-|5103bR) 5105(5106bL|5108bL) 5106(5107aR|5107aR) 5107(5117aL|5117bL) 5108(5109aR|5109aR) 5109(5110aR|5110bR)
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5120(5121aR|5122bR) 5121(5123aR|5120bR) 5122(5120aR|5120bR) 5123(5124bR|ERROR-) 5124(5125aL|5125bL) 5125(5126aR|5127bR) 5126(5125aR|5125bR) 5127(5125aR|5128bL) 5128(5129aR|5129bR) 5129(5130aL|5130bL)
5130(5131aR|5132bR) 5131(5165aR|5130bR) 5132(5130aR|5130bR) 5133(5134aR|ERROR-) 5134(5200bR|ERROR-) 5135(5136aR|5141bR) 5136(5137aL|5139bL) 5137(5138aL|5138bL) 5138(5135aL|5135bL) 5139(5140aL|5140bL)
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5160(5161aR|5161bR) 5161(7699aL|7699bL) 5162(5163bL|5163bL) 5163(5238aL|5238bL) 5164(ERROR-|6887bR) 5165(5166aR|5167bR) 5166(5168aR|5130bR) 5167(5130aR|5130bR) 5168(5169aR|5172bR) 5169(5170aL|5130bR)
5170(5171bR|5171bR) 5171(5133aR|5133bR) 5172(5130aR|5130bR) 5173(5174aR|5177bR) 5174(5175bL|5173bR) 5175(5176aR|5176aR) 5176(5200aR|5200bR) 5177(5178bL|5180bL) 5178(5179aR|5179aR) 5179(5182aR|5182bR)
5180(5181aR|5181aR) 5181(5191aR|5191bR) 5182(5183aR|5188bR) 5183(5184aL|5186aL) 5184(5185bR|5185bR) 5185(5200aR|5200bR) 5186(5187bR|5187bR) 5187(5173aR|5173bR) 5188(5209aR|5189aL) 5189(5190bR|5190bR)
5190(5191aR|5191bR) 5191(5192aR|5197bR) 5192(5193bL|5195bL) 5193(5194bR|5194bR) 5194(5200aR|5200bR) 5195(5196bR|5196bR) 5196(5173aR|5173bR) 5197(5198bL|5191bR) 5198(5199bR|5199bR) 5199(5182aR|5182bR)
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5210(5211aL|5213aL) 5211(5212bR|5212bR) 5212(5200aR|5200bR) 5213(5214bR|5214bR) 5214(5173aR|5173bR) 5215(5218aR|5216aL) 5216(5217bR|5217bR) 5217(5191aR|5191bR) 5218(5219bR|ERROR-) 5219(5220aL|5220bL)
5220(5221aR|5224bR) 5221(5222aL|ERROR-) 5222(5223aR|5223bR) 5223(5227aL|5227bL) 5224(5225aL|ERROR-) 5225(5226aL|5226bL) 5226(5220aL|5220bL) 5227(5228aR|5233bR) 5228(5229aL|5231bL) 5229(5230aL|5230bL)
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5240(5241aL|5241bL) 5241(5238aL|5238bL) 5242(5243aL|5243bL) 5243(5238aL|5238bL) 5244(5245aL|5247bL) 5245(5246aR|5246aR) 5246(5249aL|5249bL) 5247(5248aL|5248bL) 5248(5238aL|5238bL) 5249(5250aR|5251bR)
5250(5249aR|5249bR) 5251(5252aR|5249bR) 5252(5253aR|5258bR) 5253(5254aL|5256aL) 5254(5255aL|5255bL) 5255(5292aL|5292bL) 5256(5257bL|5257bL) 5257(5261aL|5261bL) 5258(ERROR-|5259aL) 5259(5260bL|5260bL)
5260(5263aL|5263bL) 5261(ERROR-|5262aR) 5262(5267bL|ERROR-) 5263(ERROR-|5264bR) 5264(5278bL|ERROR-) 5265(5266bR|ERROR-) 5266(5289aR|5289bR) 5267(5268aR|5273bR) 5268(5269aL|5271bL) 5269(5270aL|5270bL)
5270(5267aL|5267bL) 5271(5272aL|5272bL) 5272(5267aL|5267bL) 5273(5274bL|5276bL) 5274(5275aR|5275aR) 5275(5265aR|5265bR) 5276(5277aL|5277bL) 5277(5267aL|5267bL) 5278(5279aR|5284bR) 5279(5280aL|5282bL)
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5290(5289aR|5289bR) 5291(5252aR|5289bR) 5292(5293aR|5296bR) 5293(5301aR|5294bL) 5294(5295aL|5295bL) 5295(5292aL|5292bL) 5296(5297aL|5299bL) 5297(5298aL|5298bL) 5298(5292aL|5292bL) 5299(5300aL|5300bL)
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5320(5321bR|5321bR) 5321(5322aL|5322bL) 5322(5323aR|5326bR) 5323(5324aL|5322bR) 5324(5325bL|5325bL) 5325(5327aL|5327bL) 5326(5322aR|5322bR) 5327(5328aR|5333bR) 5328(5329aL|5331bL) 5329(5330aL|5330bL)
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5350(5351aL|5351bL) 5351(5346aL|5346bL) 5352(5353aL|5355bL) 5353(5354aL|5354bL) 5354(5346aL|5346bL) 5355(5356aL|5356bL) 5356(5346aL|5346bL) 5357(5358bL|ERROR-) 5358(5359aL|5359bL) 5359(5360aR|5365bR)
5360(5361aL|5363bL) 5361(5362aL|5362bL) 5362(5359aL|5359bL) 5363(5364aL|5364bL) 5364(5359aL|5359bL) 5365(5366aL|5368bL) 5366(5367aL|5367bL) 5367(5387aL|5387bL) 5368(5369aL|5369bL) 5369(5359aL|5359bL)
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6100(6104aL|6104bL) 6101(ERROR-|6102bL) 6102(6103aL|6103bL) 6103(6104aL|6104bL) 6104(ERROR-|6105aR) 6105(6106bR|ERROR-) 6106(6107aR|6108bR) 6107(6112aR|6112bR) 6108(6112aR|6112bR) 6109(6110aR|6111bR)
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6150(ERROR-|6151aR) 6151(6152bR|ERROR-) 6152(6153aR|6154bR) 6153(6158aR|6158bR) 6154(6158aR|6158bR) 6155(6156aR|6157bR) 6156(6141aR|6155bR) 6157(6155aR|6155bR) 6158(6159aR|6160bR) 6159(6161aR|6161bR)
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7100(7103aR|7103bR) 7101(7102bR|ERROR-) 7102(7106aR|7106bR) 7103(7104aR|7105bR) 7104(7103aR|7103bR) 7105(7109aR|7103bR) 7106(7107aR|7108bR) 7107(7106aR|7106bR) 7108(7114aR|7106bR) 7109(7110aR|7111bR)
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7780(7781aR|7786bR) 7781(7782aL|7784bL) 7782(7783aL|7783bL) 7783(7780aL|7780bL) 7784(7785aL|7785bL) 7785(7780aL|7780bL) 7786(7787aL|7789bL) 7787(7788aL|7788bL) 7788(7818aL|7818bL) 7789(7790aL|7790bL)
7790(7780aL|7780bL) 7791(7792aR|7797bR) 7792(7793bL|7795bL) 7793(7794aR|7794aR) 7794(7827aL|7827bL) 7795(7796aL|7796bL) 7796(7791aL|7791bL) 7797(ERROR-|7798bL) 7798(7799aL|7799aL) 7799(7800aL|7800bL)
7800(7801aR|7806bR) 7801(7802bL|7804bL) 7802(7803bR|7803bR) 7803(7827aL|7827bL) 7804(7805bL|7805bL) 7805(7791aL|7791bL) 7806(ERROR-|7807bL) 7807(7808aL|7808bL) 7808(7800aL|7800bL) 7809(7810aR|7815bR)
7810(7811bL|7813bL) 7811(7812aR|7812aR) 7812(7830aL|7830bL) 7813(7814aL|7814bL) 7814(7809aL|7809bL) 7815(ERROR-|7816bL) 7816(7817aL|7817aL) 7817(7818aL|7818bL) 7818(7819aR|7824bR) 7819(7820bL|7822bL)
7820(7821bR|7821bR) 7821(7830aL|7830bL) 7822(7823bL|7823bL) 7823(7809aL|7809bL) 7824(ERROR-|7825bL) 7825(7826aL|7826bL) 7826(7818aL|7818bL) 7827(7828aR|7829bR) 7828(7827aR|7827bR) 7829(7833aR|7827bR)
7830(7831aR|7832bR) 7831(7830aR|7830bR) 7832(7838aR|7830bR) 7833(7834aR|7835bR) 7834(7833aR|7833bR) 7835(7836bL|7833bR) 7836(7837aR|7837aR) 7837(7760aR|7760bR) 7838(7839aR|7840bR) 7839(7838aR|7838bR)
7840(7841bL|7838bR) 7841(7842bR|7842bR) 7842(7760aR|7760bR) 7843(7844aR|7849bR) 7844(7845aL|7847aL) 7845(7846aL|7846bL) 7846(7852aL|7852bL) 7847(7848aL|7848aL) 7848(7843aL|7843bL) 7849(ERROR-|7850aL)
7850(7851aL|7851aL) 7851(7843aL|7843bL) 7852(7853aR|7858bR) 7853(7854aL|7856aL) 7854(7855aL|7855bL) 7855(7861aL|7861bL) 7856(7857aL|7857aL) 7857(7843aL|7843bL) 7858(ERROR-|7859aL) 7859(7860aL|7860aL)
7860(7843aL|7843bL) 7861(7862aR|7863bR) 7862(HALT--|7864bR) 7863(ERROR-|7864bR) 7864(7865aR|7870bR) 7865(7866aL|7868bL) 7866(7867aL|7867bL) 7867(7864aL|7864bL) 7868(7869aL|7869bL) 7869(7864aL|7864bL)
7870(7871aL|7873bL) 7871(7872aL|7872aL) 7872(7875aL|7875bL) 7873(7874aL|7874bL) 7874(7864aL|7864bL) 7875(7876aR|7881bR) 7876(7877bL|7879bL) 7877(7878bR|7878bR) 7878(7884aL|7884bL) 7879(7880bL|7880bL)
7880(7875aL|7875bL) 7881(ERROR-|7882bL) 7882(7883aR|7883aR) 7883(7884aL|7884bL) 7884(7885aR|7886bR) 7885(7887aR|7884bR) 7886(7884aR|7884bR) 7887(7888aR|7889bR) 7888(7887aR|ERROR-) 7889(ERROR-|7890bL)
7890(7891aR|7891bR) 7891(7892aL|7892bL) 7892(7893aR|7894bR) 7893(7895aR|7892bR) 7894(7892aR|7892bR) 7895(7896aR|7897bR) 7896(7898aR|7892bR) 7897(7892aR|7892bR) 7898(7899aR|7902bR) 7899(7900aL|7892bR)
7900(7901aR|7901bR) 7901(7903aL|7903bL) 7902(7892aR|7892bR) 7903(7904aR|7905bR) 7904(7903aR|ERROR-) 7905(ERROR-|7906bL) 7906(7907aL|7907bL) 7907(7908aL|7908bL) 7908(7909bL|ERROR-) 7909(4381aL|4381bL)
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