# Xiaomi Redmi 6 Pro Schematic Diagram





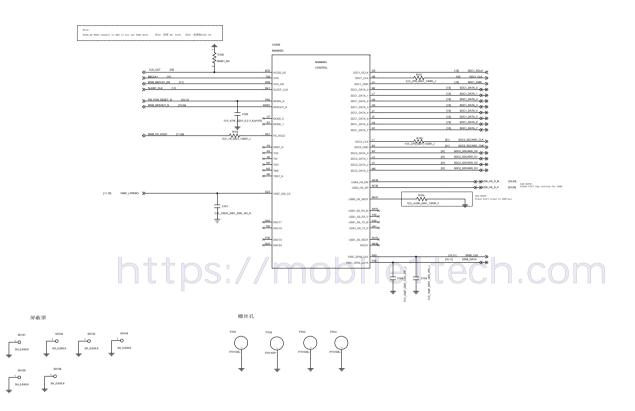
Xiaomi Smartphone



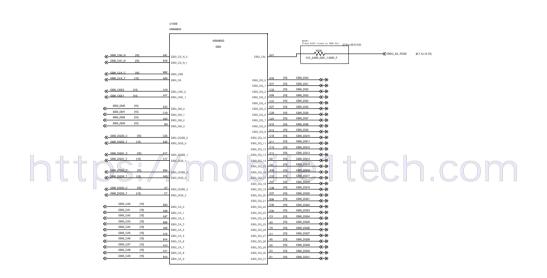


# Schematic

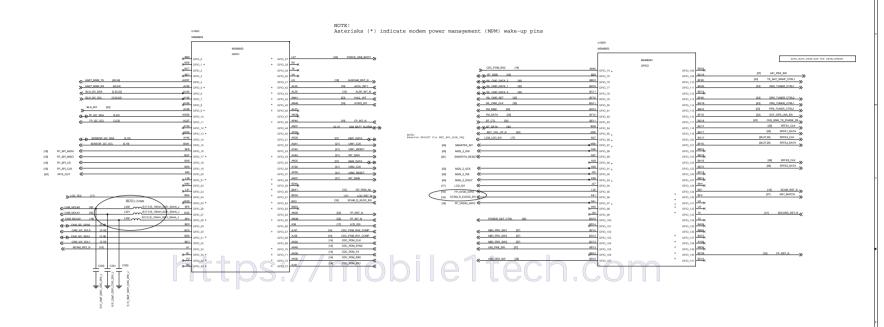




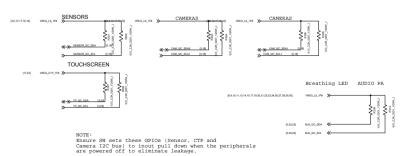
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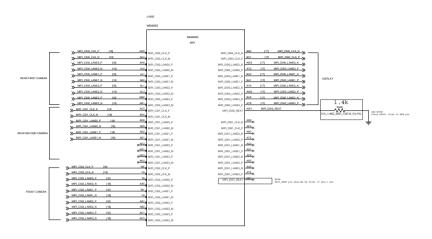


0900_37	FORCED_COSE_BOOT	
GP00_106	NOOG_DOMMAN	
0900_108	APR.,9027,9108,9108	

BOOT_COMPTO(3:1)	BOOT_COMPTO
68.000	80C1 -> 80C2 -> 08M2.0
69.001	2002 -> 2001-> 0382.0
69410	20C1 -> 1282.0
68411	1992.4

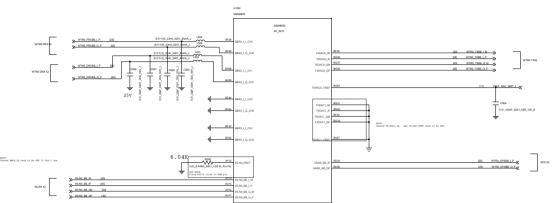
Default Boot Config (Gb000) is SDC1(#MMC)





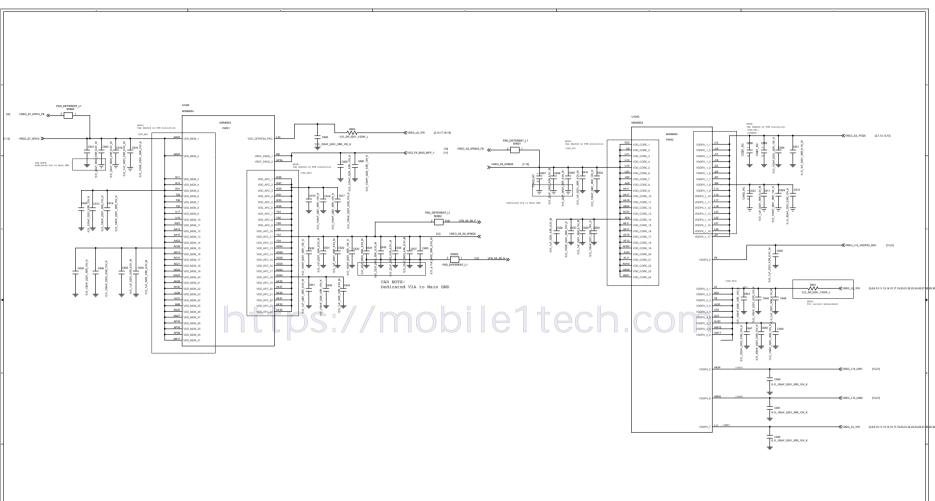
CSI Pin Name	CSI DPHY 4-lane	CSI DPHY 2+1 Mode	CSI CPHY 3Phase Mod
CSIO_3PHASE_PINO CSIO_3PHASE_PINO CSIO_3PHASE_PINZ CSIO_3PHASE_PINZ CSIO_3PHASE_PINA CSIO_3PHASE_PINA CSIO_3PHASE_PINA CSIO_3PHASE_PINA CSIO_3PHASE_PINA CSIO_3PHASE_PINA CSIO_3PHASE_PINA CSIO_3PHASE_PINA	CSIO_CLKP CSIO_CLKN CSIO_DPO CSIO_DPO CSIO_DNO CSIO_DPI CSIO_DNI CSIO_DP2 CSIO_DN2 CSIO_DP3 CSIO_DN3	CSIO_ZLANE_CLKP CSIO_ZLANE_DPO CSIO_ZLANE_DPO CSIO_ZLANE_DPO CSIO_ZLANE_DP1 CSIO_ZLANE_DP1 CSIO_ZLANE_DN1 CSIO_ILANE_DN0 CSIO_ILANE_DN0 CSIO_ILANE_DN0 CSIO_ILANE_CLKP CSIO_ILANE_CLKP CSIO_ILANE_CLKP	NC CSI0_TRI0_B CSI0_TRI0_B CSI0_TRI0_C CSI0_TRI1_A CSI0_TRI1_A CSI0_TRI1_C CSI0_TRI1_C CSI0_TRI1_C CSI0_TRI1_C CSI0_TRI2_B CSI0_TRI2_B CSI0_TRI2_B CSI0_TRI2_C
CSII_3PHASE_PIN0 CSII_3PHASE_PIN1 CSII_3PHASE_PIN2 CSII_3PHASE_PIN3 CSII_3PHASE_PIN4 CSII_3PHASE_PIN6 CSII_3PHASE_PIN6 CSII_3PHASE_PIN6 CSII_3PHASE_PIN7 CSII_3PHASE_PIN7 CSII_3PHASE_PIN9	CSI1_CLKP CSI1_CLKN CSI1_DP0 CSI1_DN0 CSI1_DN0 CSI1_DN1 CSI1_DP1 CSI1_DP2 CSI1_DP2 CSI1_DP3 CSI1_DP3	CSI1_2LANE_CLKP CSI1_2LANE_DP0 CSI1_2LANE_DP0 CSI1_2LANE_DP1 CSI1_2LANE_DP1 CSI1_2LANE_DP1 CSI1_1LANE_DP0 CSI1_1LANE_DP0 CSI1_1LANE_DN0 CSI1_1LANE_CLKP CSI1_1LANE_CLKN	NC CSI1_TRI0_A CSI1_TRI0_B CSI1_TRI0_C CSI1_TRI1_A CSI1_TRI1_B CSI1_TRI1_C CSI1_TRI1_C CSI1_TRI2_B CSI1_TRI2_B CSI1_TRI2_B
	CS12_CLKP CS12_CLKN CS12_DP0 CS12_DP0 CS12_DN0 CS12_DP1 CS12_DN1 CS12_DP2 CS12_DP2 CS12_DP3 CS12_DP3 CS12_DP3	CSI2_2LANE_CLKP CSI2_2LANE_CLKN CSI2_2LANE_DP0 CSI2_2LANE_DP0 CSI2_2LANE_DP1 CSI2_2LANE_DP1 CSI2_2LANE_DP1 CSI2_2LANE_DP0 CSI2_1LANE_DP0 CSI2_1LANE_DP0 CSI2_1LANE_CLKP CSI2_1LANE_CLKN	NC CS12_TR10_A CS12_TR10_B CS12_TR10_C CS12_TR11_A CS12_TR11_B CS12_TR11_B CS12_TR11_C CS12_TR12_A CS12_TR12_B CS12_TR12_B



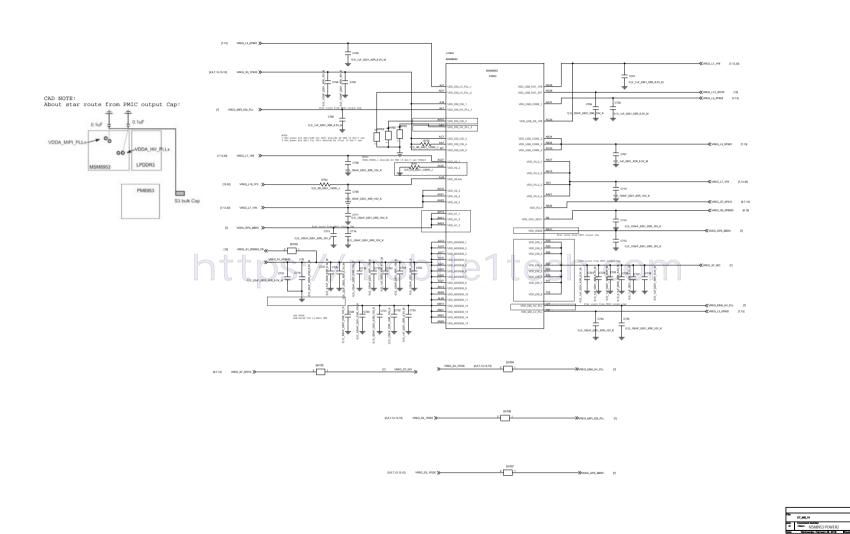


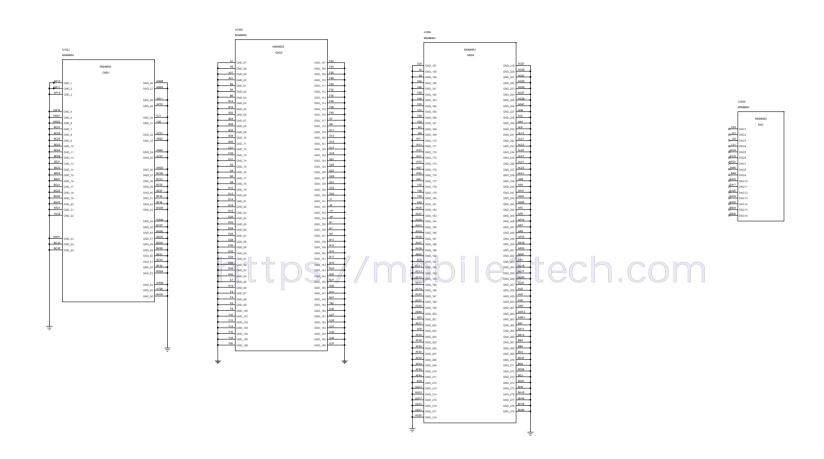








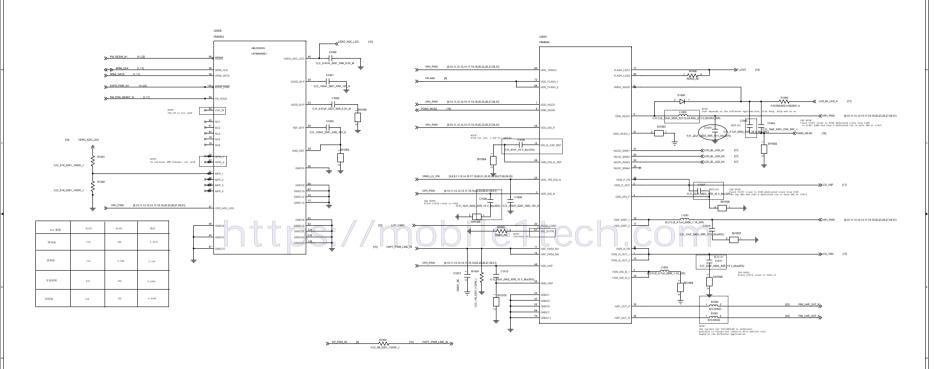




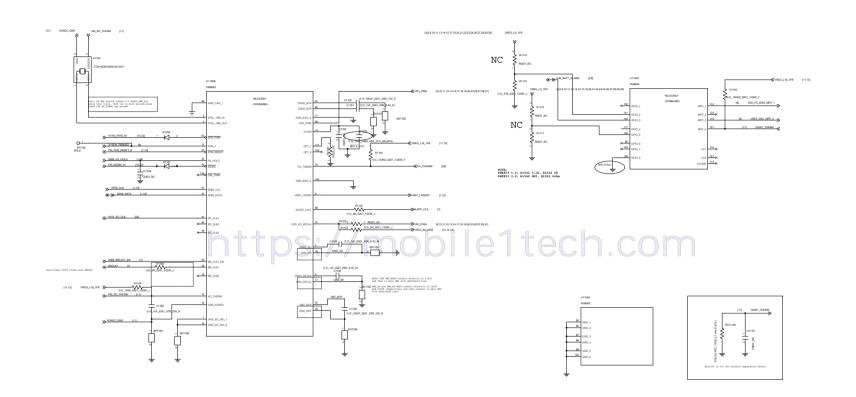


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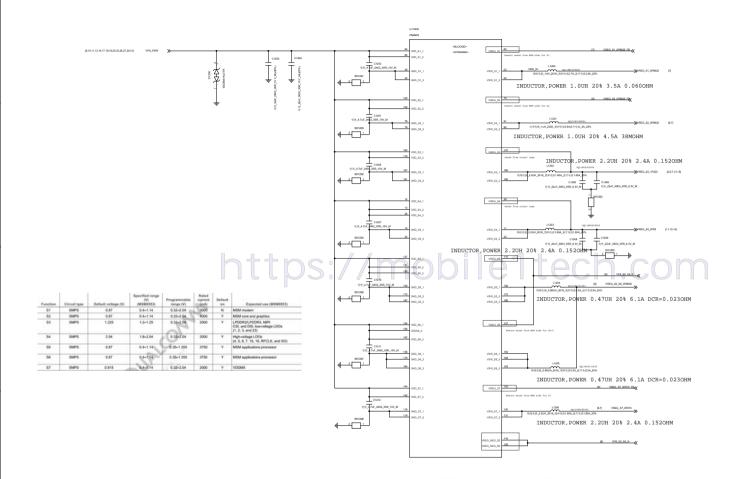




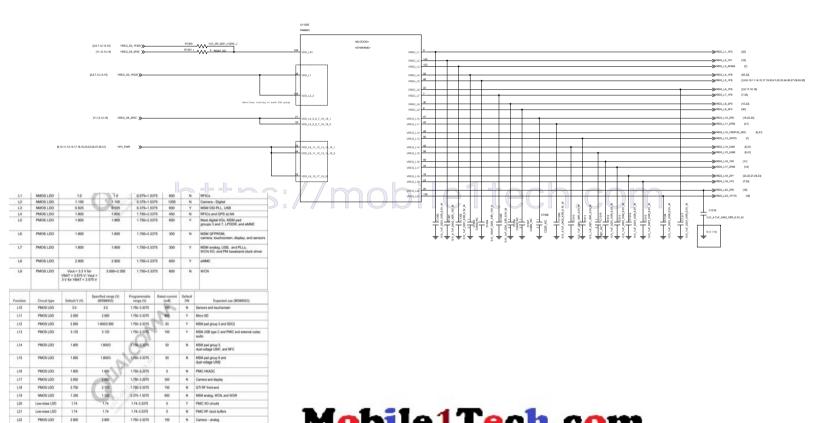




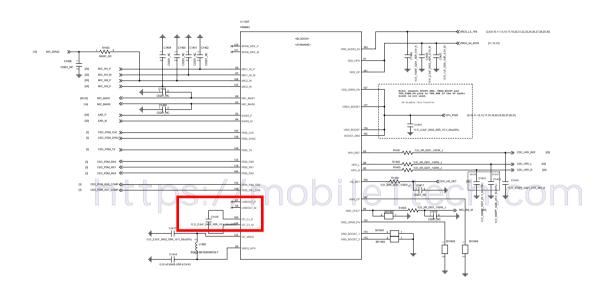
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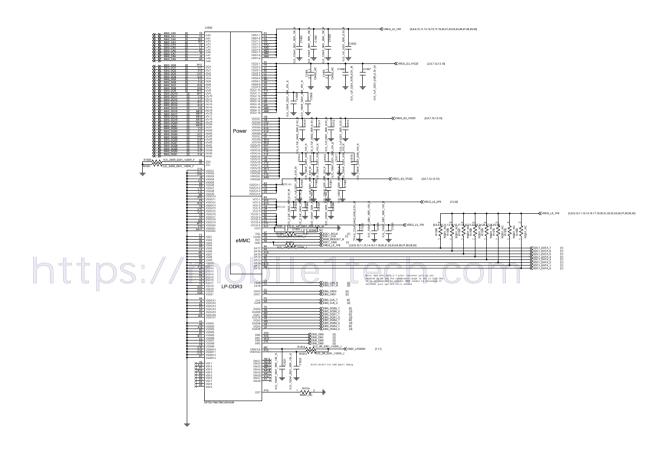










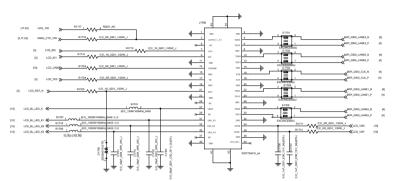


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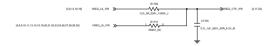
Note: If best BMI practices are followed for MIDI CMI/DMI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints.

Extreme care must be taken that no stubs are created by doing so.

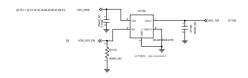


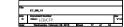


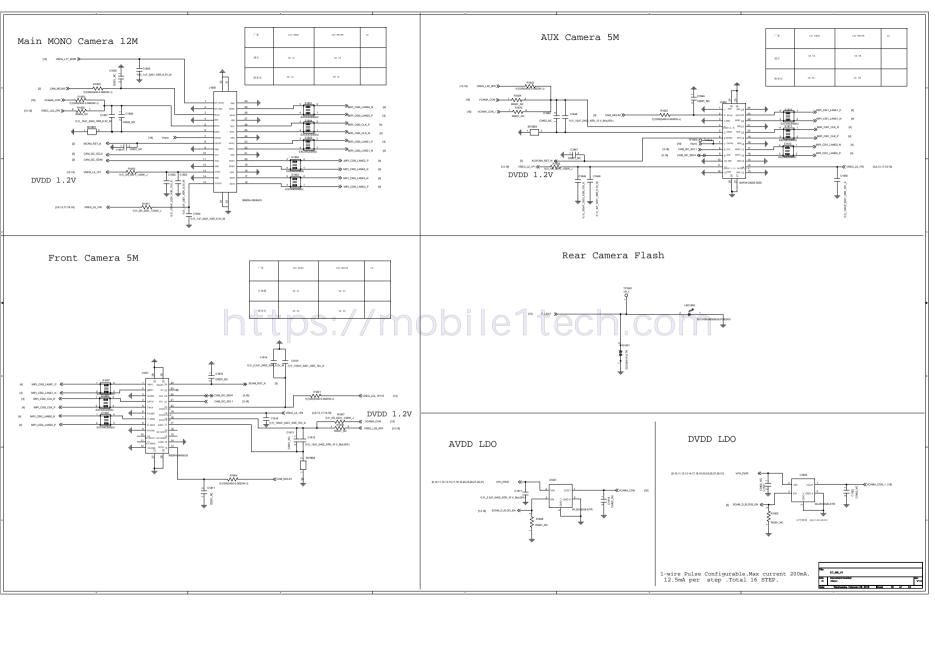
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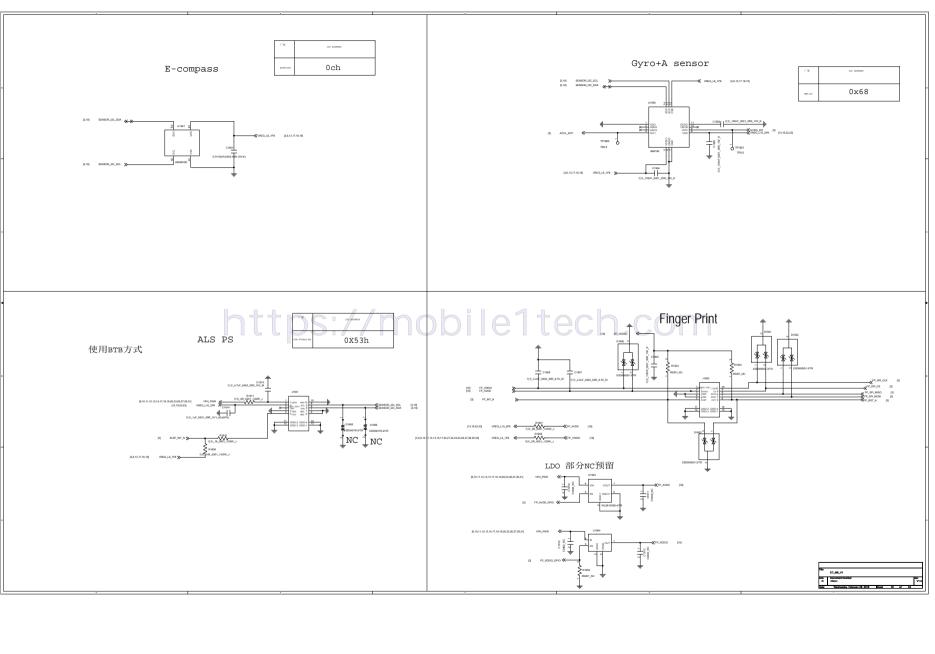


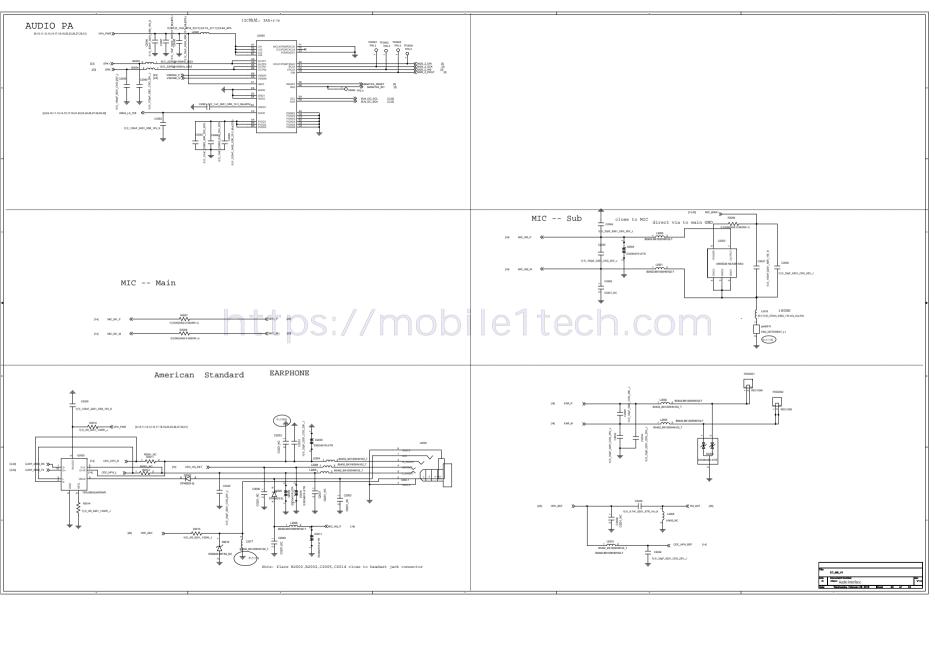
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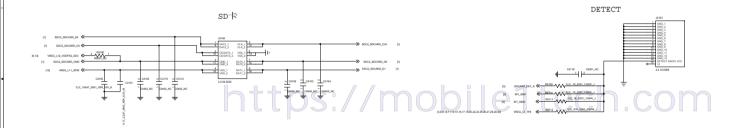






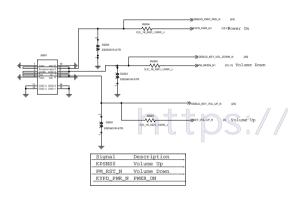


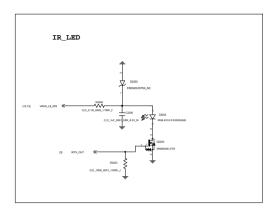




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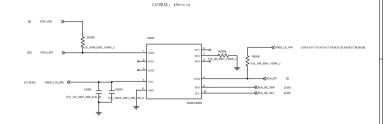




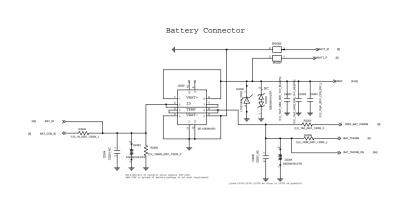


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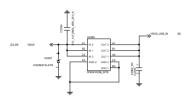
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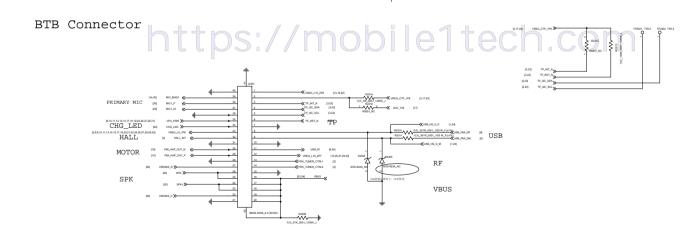




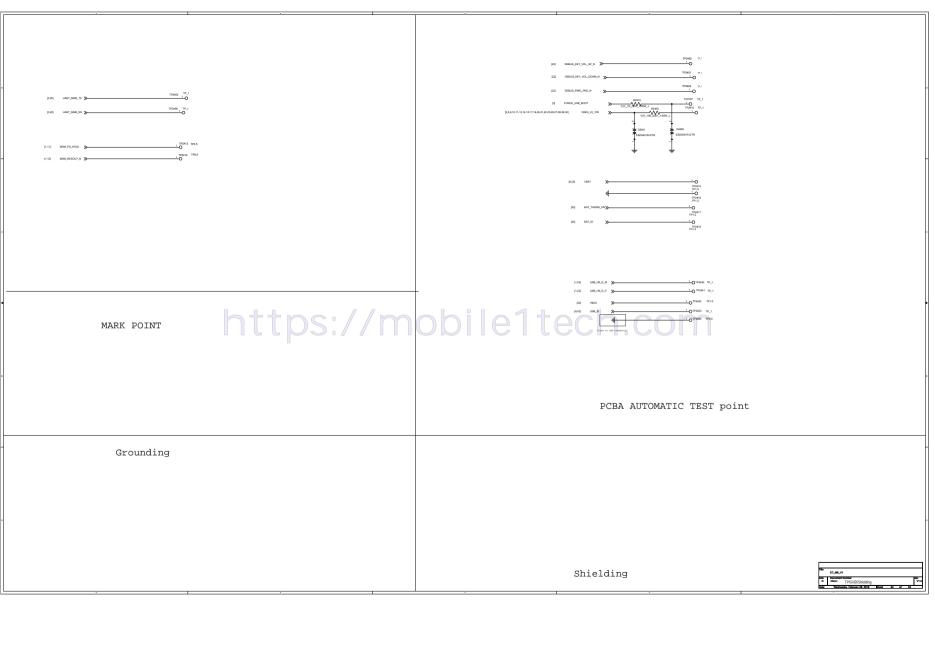


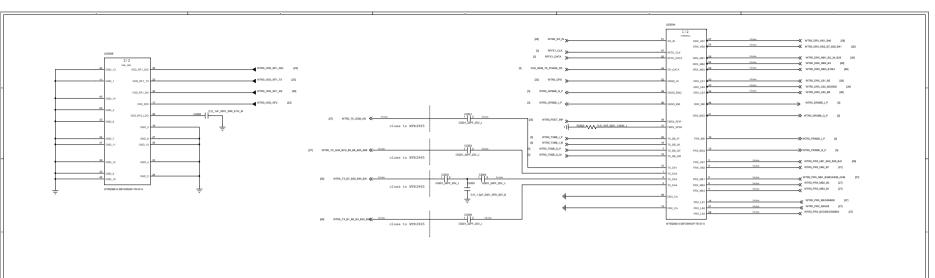
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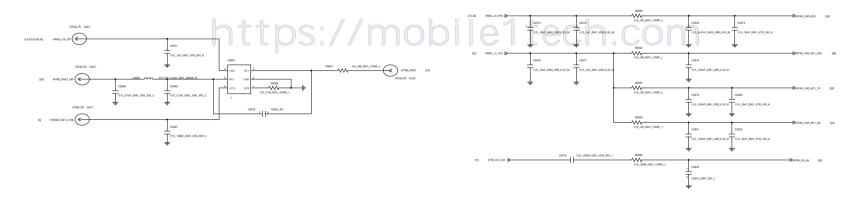




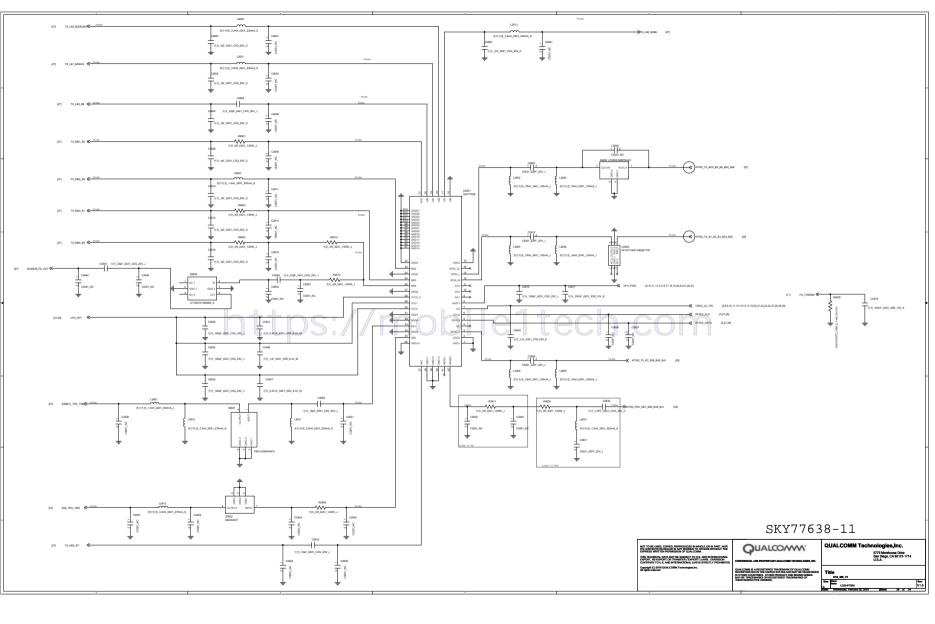


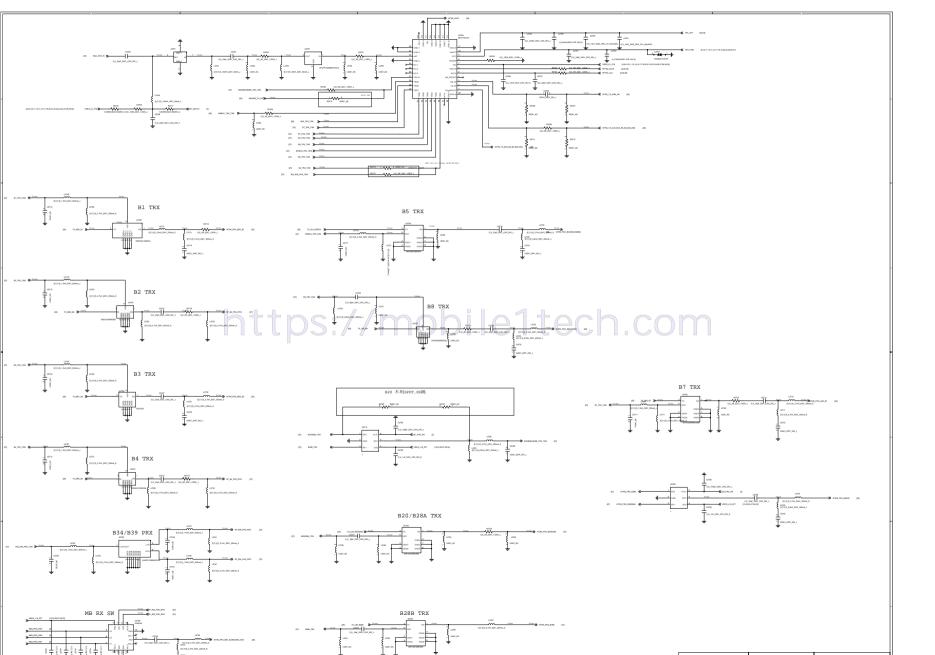


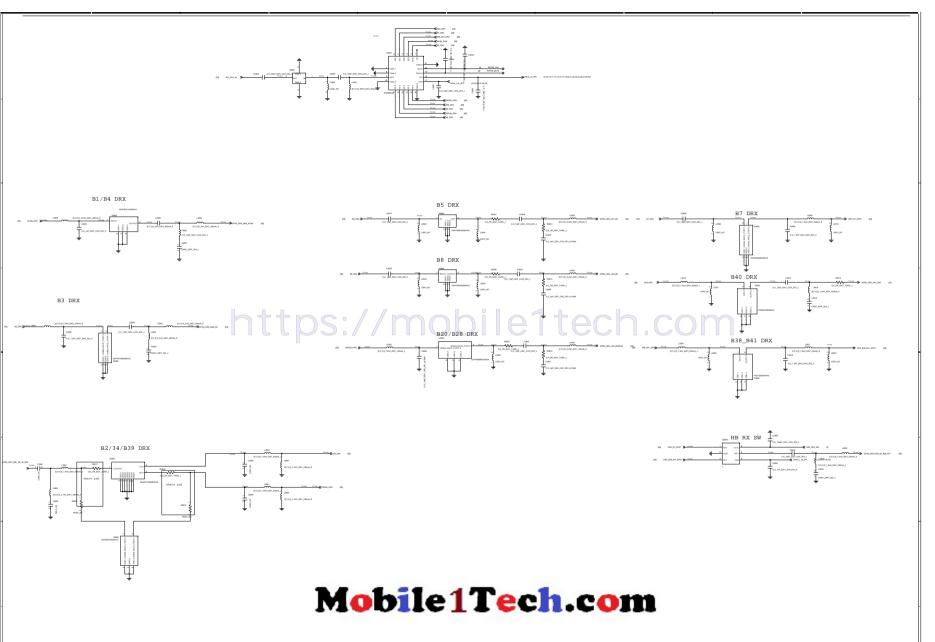


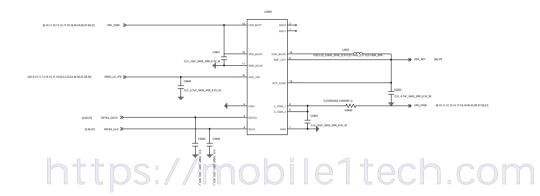


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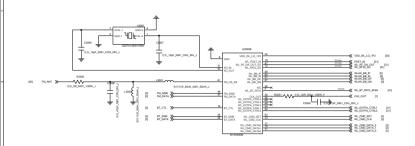


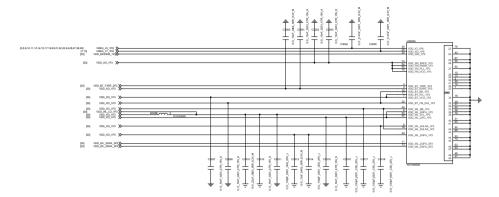


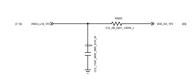


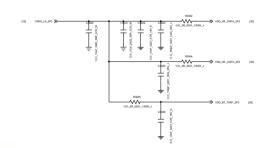
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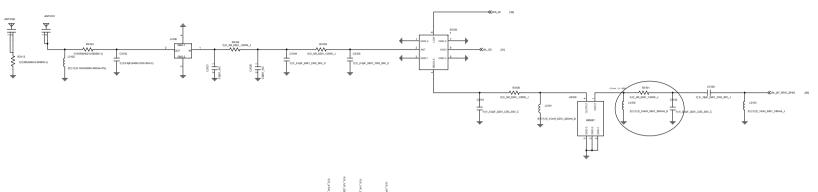


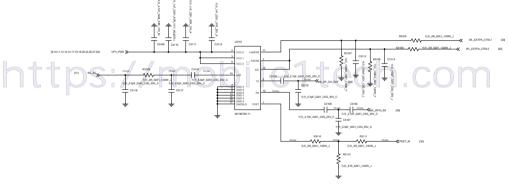


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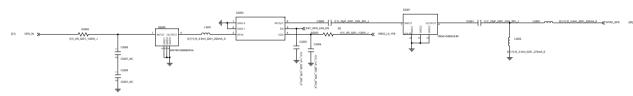
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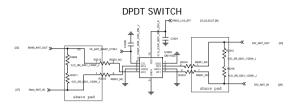
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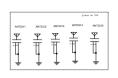


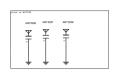
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R3201	DNI	0ohm
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