

GT-I9500_MAIN_REV 0.7

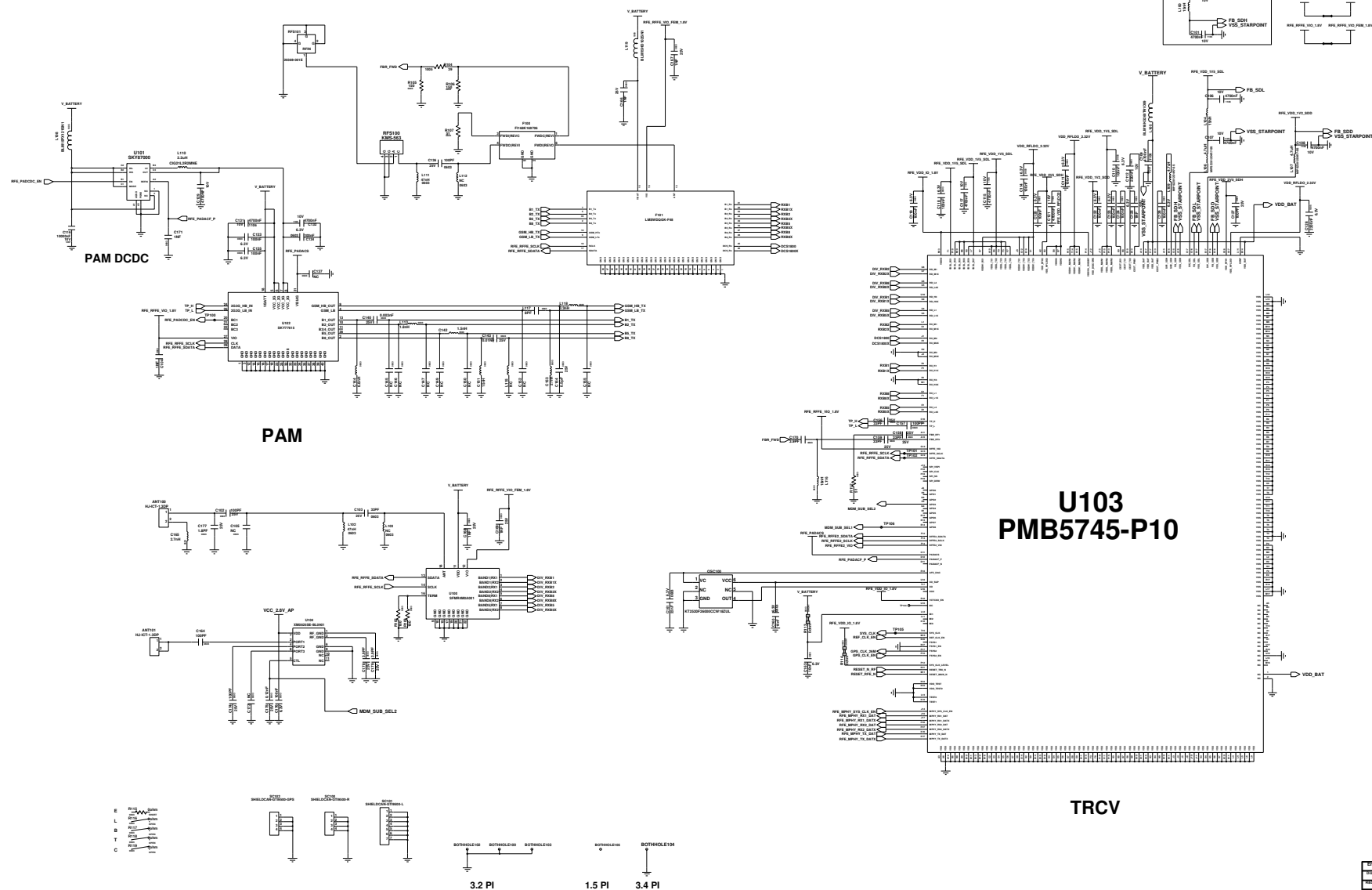
2013.02.14

REFERENCE

sheet01 :	GSM / WCDMA	100
sheet03 :	CP(XMM6360)	300
sheet05 :	AP(ADONIS)	5000
sheet06 :	AP PMIC, IF PMIC	600
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Engineer	SAMSUNG ELECTRONICS	
Drawn by	TITLE	LT
RED ORG	GT-I9500	
ROD ORG		
PROF ORG		
DR ORG		
Check by	Drawn by	Sheet Number
13	14	15
Drawing Number		SHEET 0007
16		17

2013.02.14



U103
PMB5745-P10

TRCV

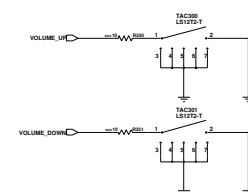
Engineer	RF	
Drawn by:		
RED CHK:		
DOC CTR: CHK:		
INFO ENG: CHK:		
GT-19500_REV0.7	TITLE:	
GT-19500	Revision Number	
GT-19500	REV	

CONNECTOR



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user02|ecad2|10.244.89.250|20130316-134651
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CP Part (XMM6360)



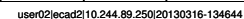
The diagram shows a circuit for checking the hardware version of a CP module. It features a 000 component connected to a +5V supply through a 1k resistor. The output of the 000 component is connected to a 1k resistor, which is then connected to a 1k resistor leading to ground. The output of this second 1k resistor is connected to a 1k resistor leading to ground. The output of this third 1k resistor is connected to a 1k resistor leading to ground. The output of this fourth 1k resistor is connected to a 1k resistor leading to ground. The output of this fifth 1k resistor is connected to a 1k resistor leading to ground. The output of this sixth 1k resistor is connected to a 1k resistor leading to ground. The output of this seventh 1k resistor is connected to a 1k resistor leading to ground. The output of this eighth 1k resistor is connected to a 1k resistor leading to ground. The output of this ninth 1k resistor is connected to a 1k resistor leading to ground. The output of this tenth 1k resistor is connected to a 1k resistor leading to ground.

boot ctp	DSP_AUDIO_IN1	HIF_MON1	HIF_MON2	TREB_IN	Description
01	0	0	0	1	DOR2NH2(EMC)
03	0	0	1	1	CDC 2(16RX - BTX)
04	0	1	0	0	CDC 2(16RX - 16TX)
05	0	1	0	1	HAND_FLASH(NANDCTRL)
09	1	0	0	1	EMMC(SMMC1)
12	1	1	0	0	FLASHLESS_BOOT: BB chip set to C0
13	1	1	0	1	FLASHLESS_BOOT: BB chip set to C1
14	1	1	1	0	FLASHLESS_BOOT: BB chip set to C2
15	1	1	1	1	FLASHLESS_BOOT: BB chip set to C3

boot ctp	DSP_AUDIO_IN1	HIF_MON1	HIF_MON2	TREB_IN	Description
01	0	0	0	1	DOR2NH2(EMC)
03	0	0	1	1	CDC 2(16RX - BTX)
04	0	1	0	0	CDC 2(16RX - 16TX)
05	0	1	0	1	HAND_FLASH(NANDCTRL)
09	1	0	0	1	EMMC(SMMC1)
12	1	1	0	0	FLASHLESS_BOOT: BB chip set to C0
13	1	1	0	1	FLASHLESS_BOOT: BB chip set to C1
14	1	1	1	0	FLASHLESS_BOOT: BB chip set to C2
15	1	1	1	1	FLASHLESS_BOOT: BB chip set to C3

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user02|ecad2|10.244.89.250|20130316-134642
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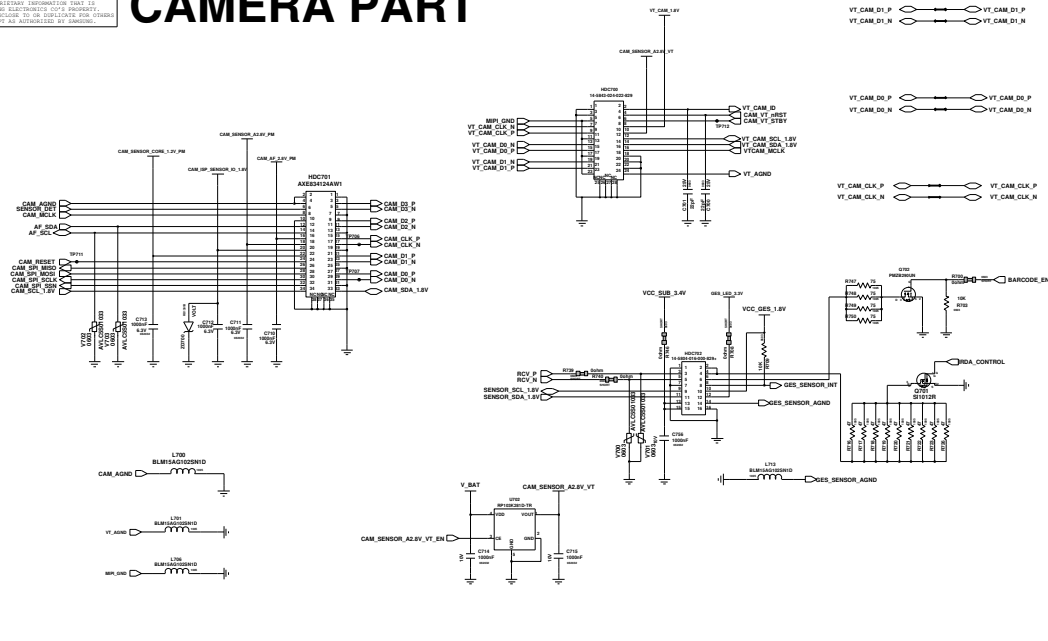
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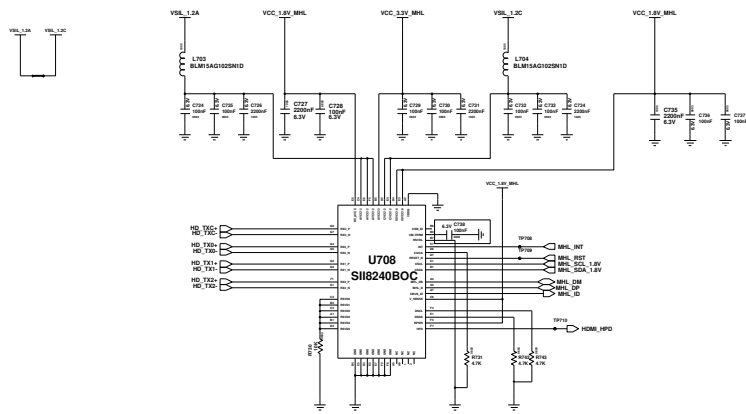


Engineer		
Drawn by:		
Rev CTR:	TITLE:	
DOC CTR CTR:	GT-I9500_REV0.7	
INFO ENGNT CTR:		
Original	USA CTR:	Revision Number
		000001

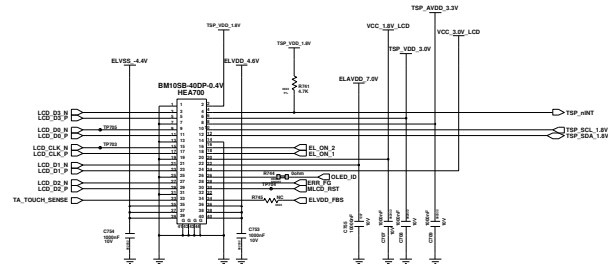
CAMERA PART



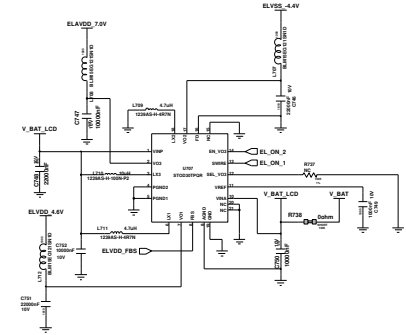
MHL PART



LCD Conector(MIPI)



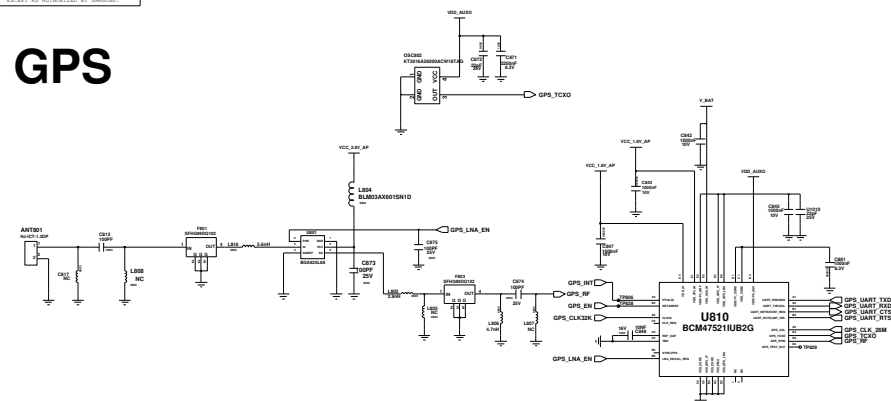
LCD DCDC



Author	DATE
Drawn by	DATE
Checked by	DATE
Approved by	DATE
GT-I9500_REV0.7	

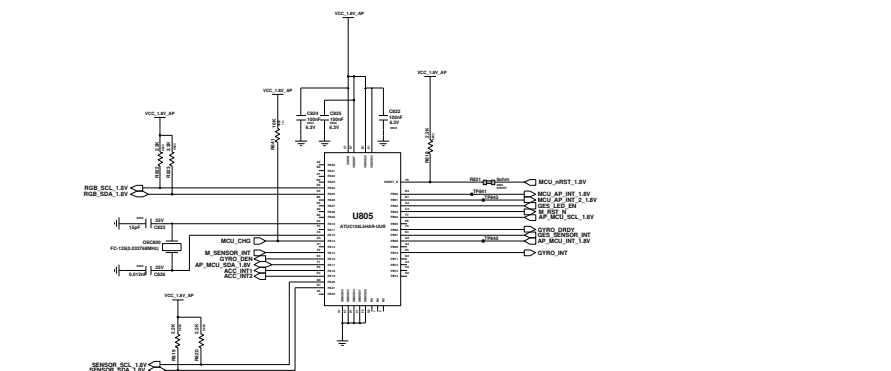
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GPS



GPS(BCM47521)

SENSOR HUB

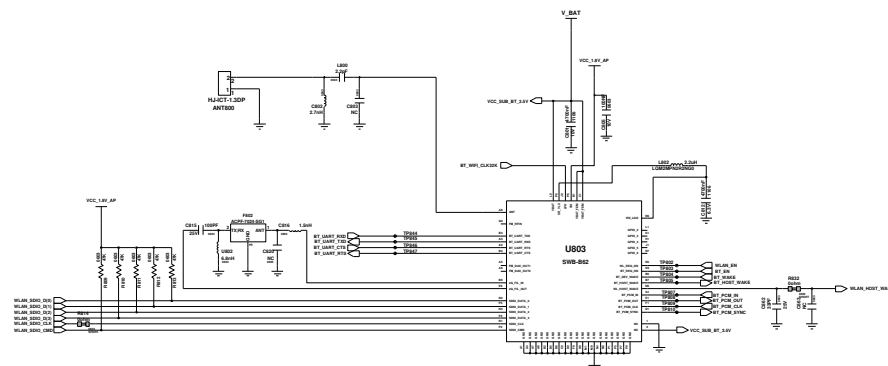


Barometer Sensor

Magnetic Sensor

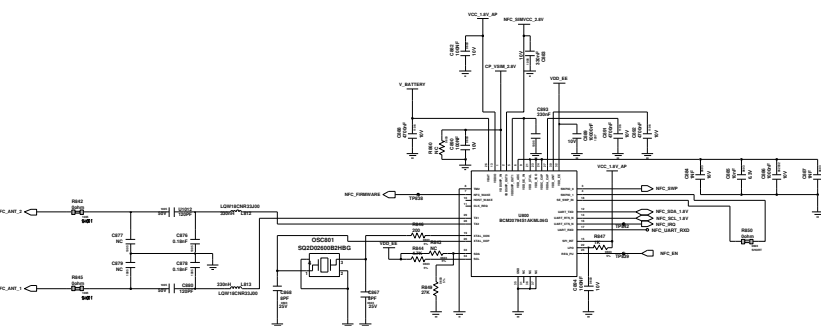
6 Axes Sensor (GYRO,ACCEL)

BT/WIFI

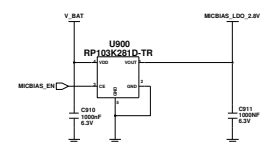


BT / WIFI

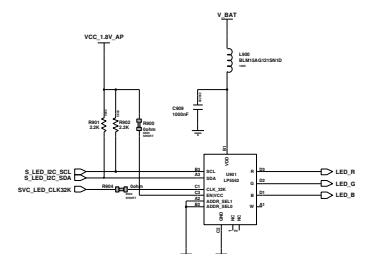
NFC



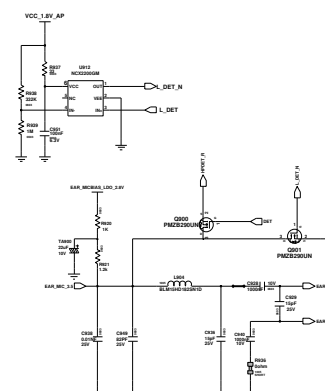
Engineer		
Drawn by:		
Rev CTR:	TITLE:	1
Doc CTR CTR:	GT-I9500_REV0.7	
MFG Engr CTR:		
QA CTR:	Revision Number	00000



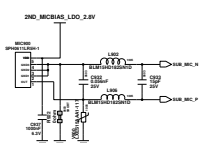
MIC BIAS LDOs



Light-Guide LED Driver

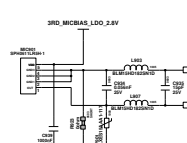


Ear MIC

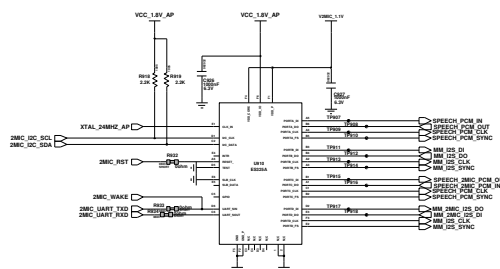


Main MIC

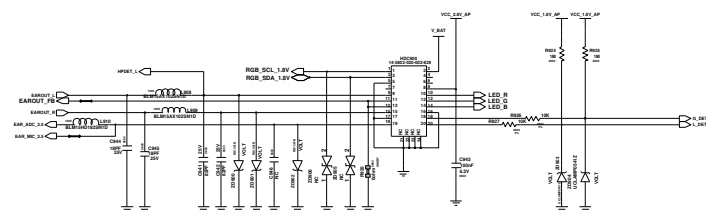
2nd MIC



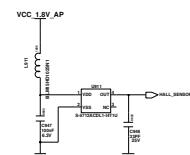
3rd MIC



2MIC IC



E/J + RGB + SVC LED



HALL SENSOR

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