

Samsung **SM-J410F** Schematic Diagram



Diagram
Schematic



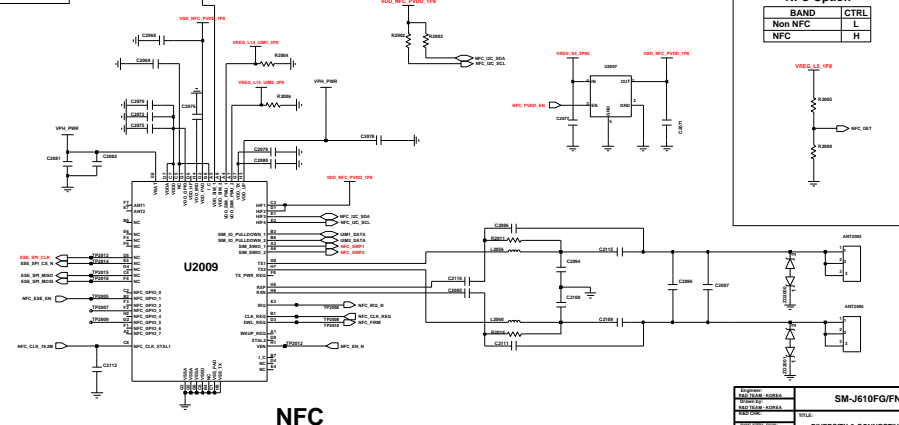
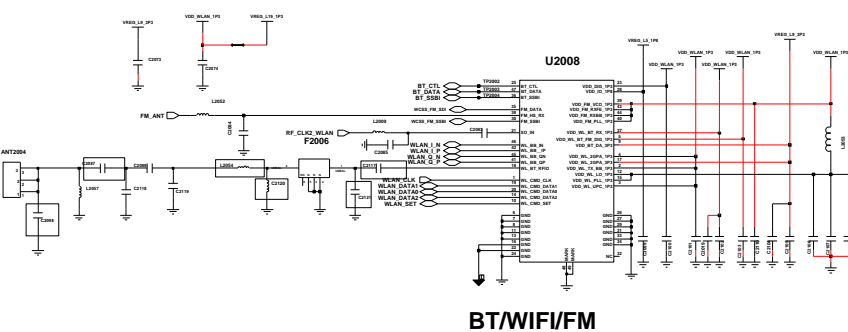
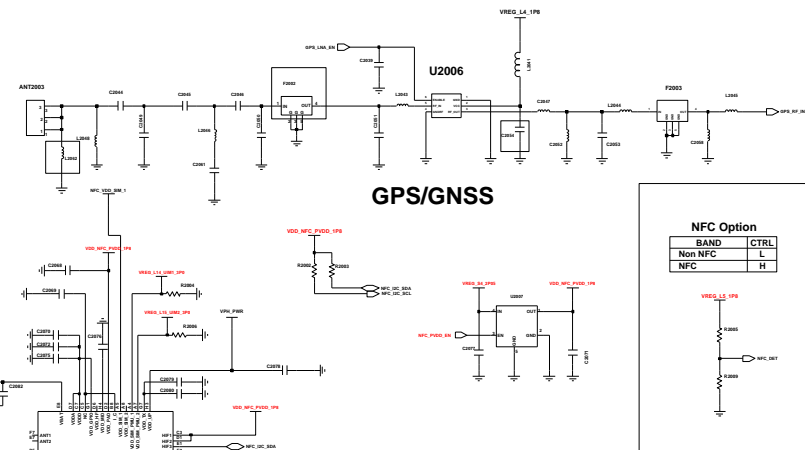
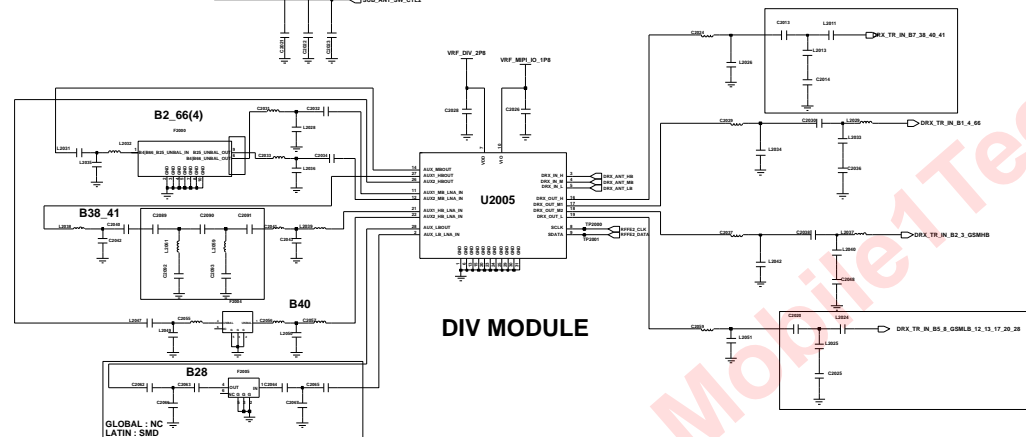
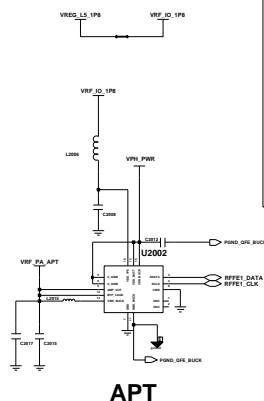
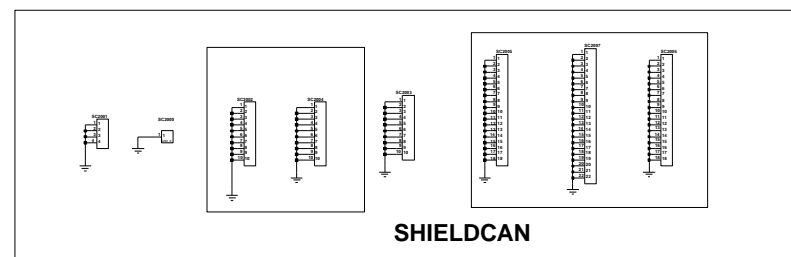
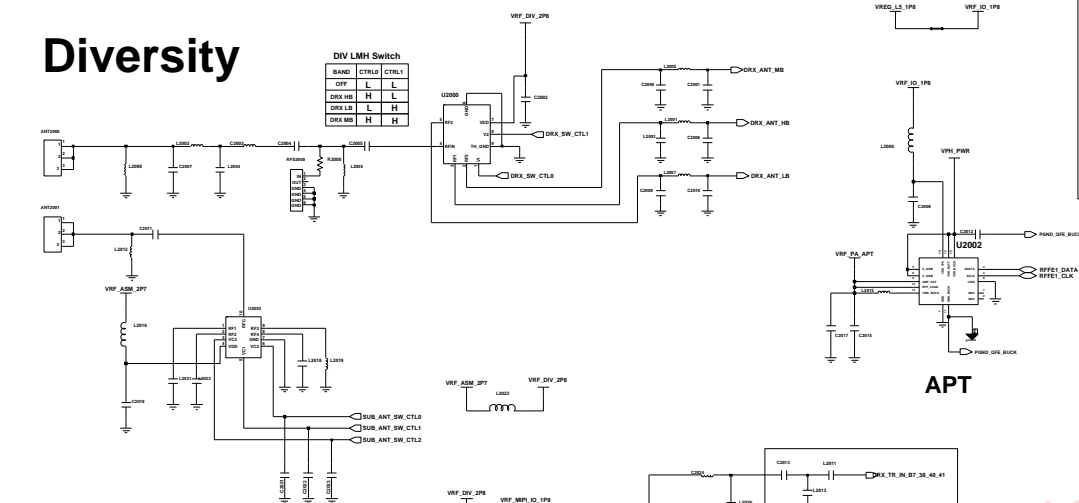
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A											A
B											
C											
D											
E											E
F											F
G											G
H											H
1	2	3	4	5	6	7	8	9	10	11	12

SM-J410F/G REV0.2B

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- SHEET 1 : MAIN RF PART**
- SHEET 2 : DIVERSITY,CONNECTIYBITY**
- SHEET 3 : BASEBAND(MSM8917)**
- SHEET 4 : PMIC(PM8937), MUIC,INTERFACE**
- SHEET 5 : MEMORY, AUDIO**
- SHEET 6 : CAMERA, DISPLAY, SENSOR, KEY**

DIV LMH Switch		
BAND	CTRL0	CTRL1
OFF	L	L
DRX HB	H	L
DRX LB	L	H
DRX MB	H	H



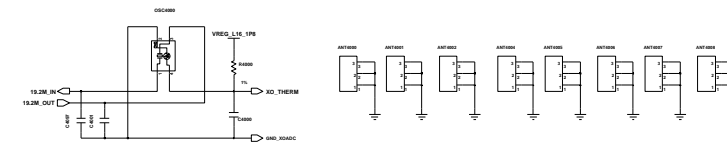
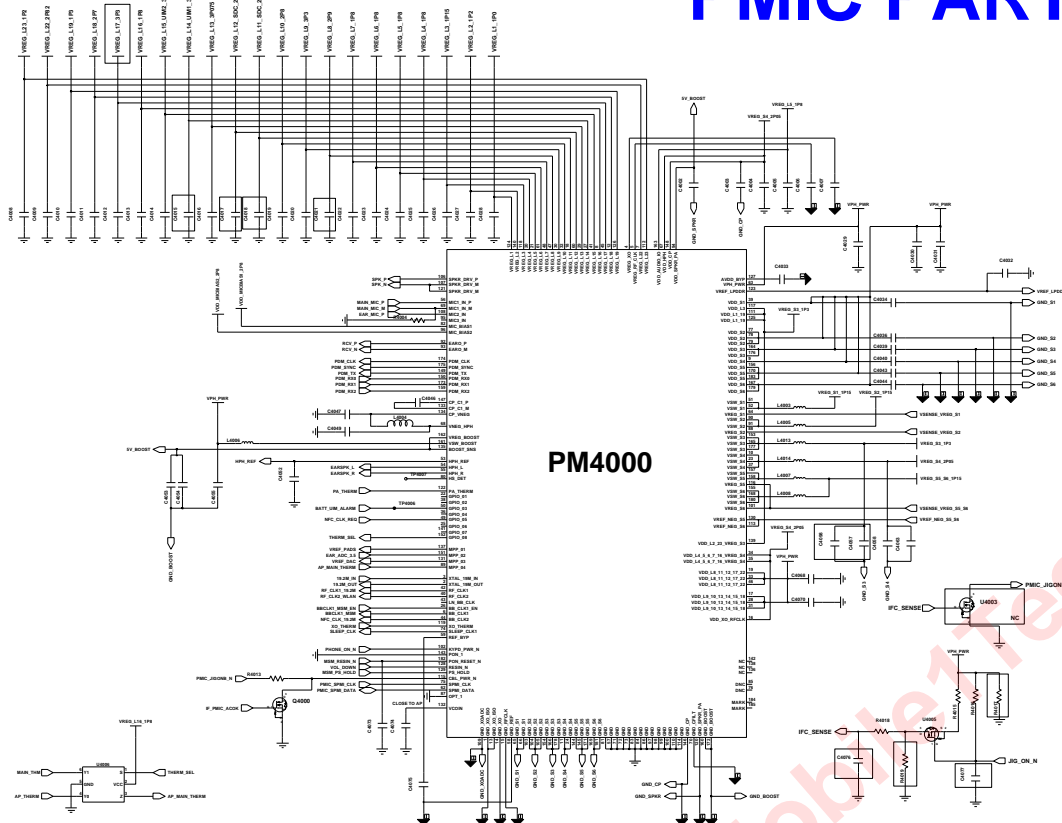
NFC Option	
BAND	CTF
Non NFC	L
NFC	H

Engineer:	RAO TEAM - KOREA	SM-J610FG/FM	
Drawn by:	RAO TEAM - KOREA		
QC'd CHK:			
QC'd CTRL CHK:			
MFG ENG'N CHK:			
TITLE:		DIVERSITY & CONNECTIVITY	
Original	UK ELEC	REV	Drawing Number

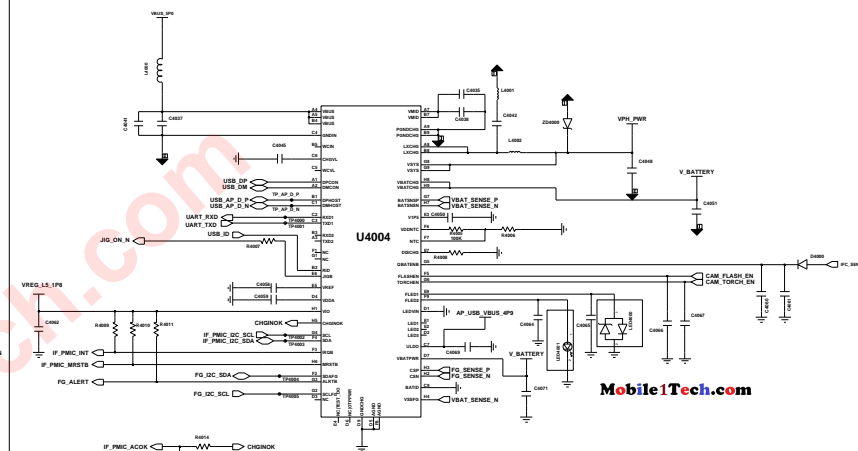
PCB H/W Revision

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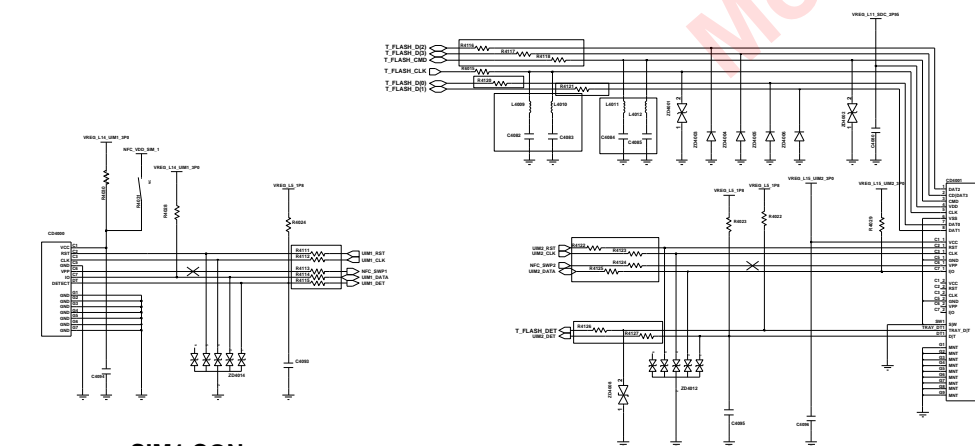
PMIC PART



19.2MHz OSC

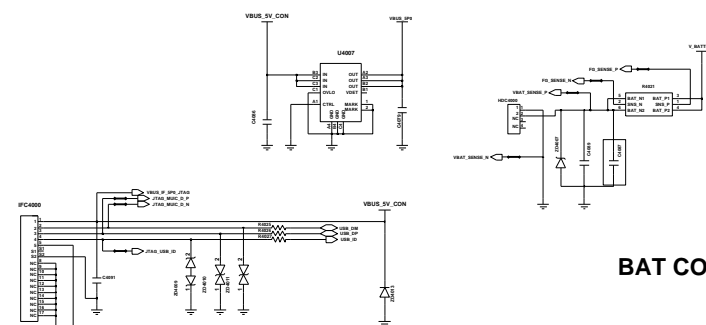


IFPMIC



SIM1 CON

SIM2 / SD CON



BAT CON

IFCON

Engineer	SM-J610F/G/FN	
Drawn by:		
P&ID CHK:	TITLE:	30
LOGIC CTRL. CHK:	PMIC, IFPMIC, SIM&SD CON	
WIRING DIAGRAM CHK:		
QA CHK:	Revision Number:	000

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AUDIO PART

RECEIVER

SPEAKER

EAR MIC

E/P ANTI POP UP IC

MIC

3.5PI EARJACK CON

MEMORY(16GB eMMC + 8Gb LPDDR3)

UME5000

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Engineer
SM-J510F/G/FN
MEMORY & AUDIO

[illegible]

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AUDIO PART

The schematic diagram illustrates the audio section of a device, featuring several key components and their interconnections:

- UME5000**: A central component connected to multiple power supply rails (VDDA_1.8V, VDDA_1.2V, VDDA_0.9V, VDDA_0.6V, VDDA_0.3V) and a large array of memory chips (LPDDR3).
- MEMORY(16GB eMMC + 8Gb LPDDR3)**: The storage section, including eMMC and LPDDR3 memory modules.
- RECEIVER**: A circuit for receiving signals, involving components like L1007, L1008, and L1009.
- SPEAKER**: A speaker driver circuit connected to ANTSD01 and ANTSD02 antennas.
- EAR MIC**: An ear microphone circuit utilizing U2000 and U2001 components.
- E/P ANTI POP UP IC**: A component for anti-pop-up protection, connected to EAR_ADC_3.3 and EAR_DETECT_BALLUP.
- MIC**: A standard microphone input circuit.
- 3.5PI EARJACK CON**: A 3.5mm jack connector for external audio devices.

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Engineer	SM-J510F/G/FN
DESIGNER	TITLE
CHECKED BY	MEMORY & AUDIO
DATE	DRAWING NUMBER
REV	OK

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AUDIO PART

RECEIVER

SPEAKER

EAR MIC

E/P ANTI POP UP IC

MIC

3.5PI EARJACK CON

MEMORY(16GB eMMC + 8Gb LPDDR3)

UME5000

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Engineer
SM-J510F/G/FN
MEMORY & AUDIO

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