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# SM-J110F\_REV0.4

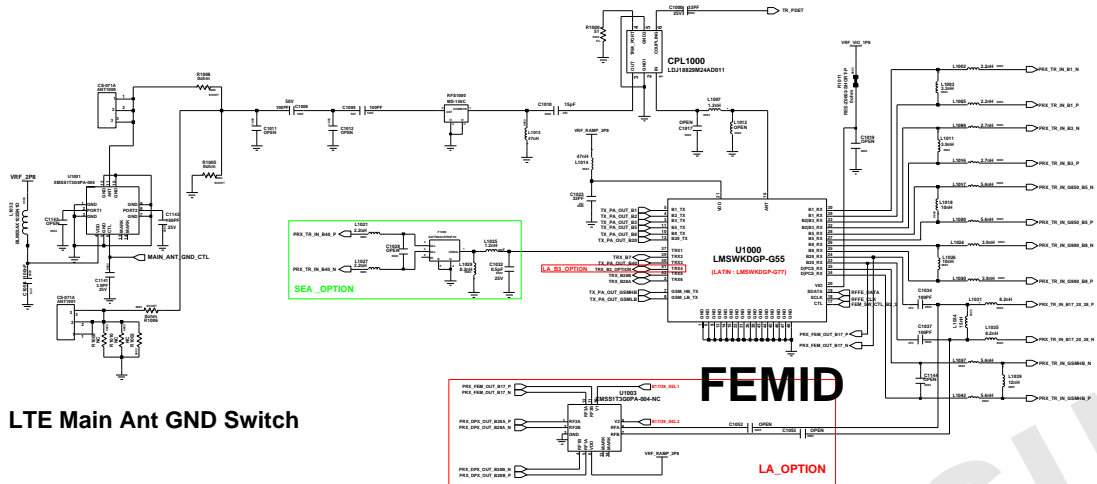
## REFERENCE

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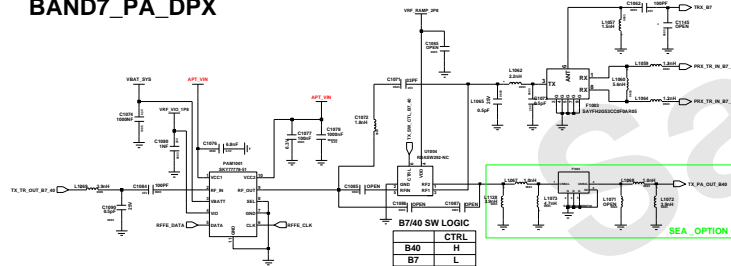
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Drawn by	Song	Sheet	
FIELD CHK		TITLE	A2
DOC CTRL CHK			
WPS ENGR CHK			

Changed by	Date Changed	Time Changed	SA CHK	REV	Drawing Number	Sheet	0 of 8
user43	Thursday, August 27, 2009	1:18:10 pm			11	12	

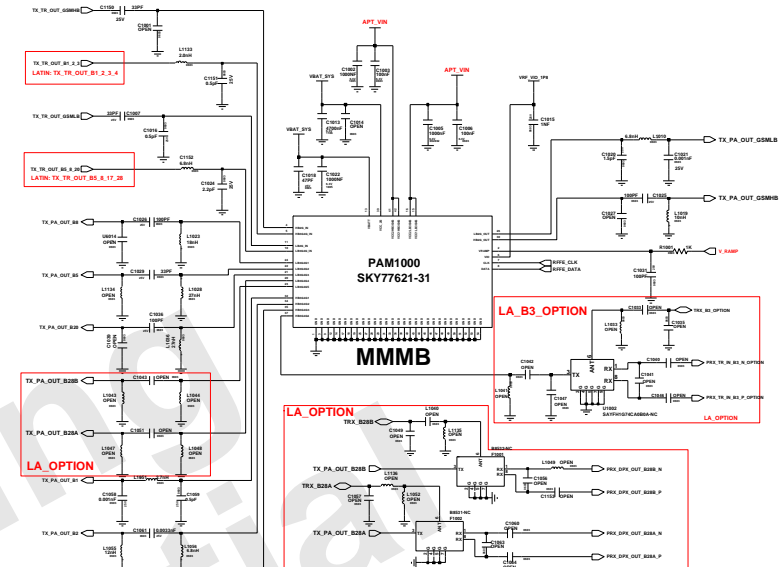
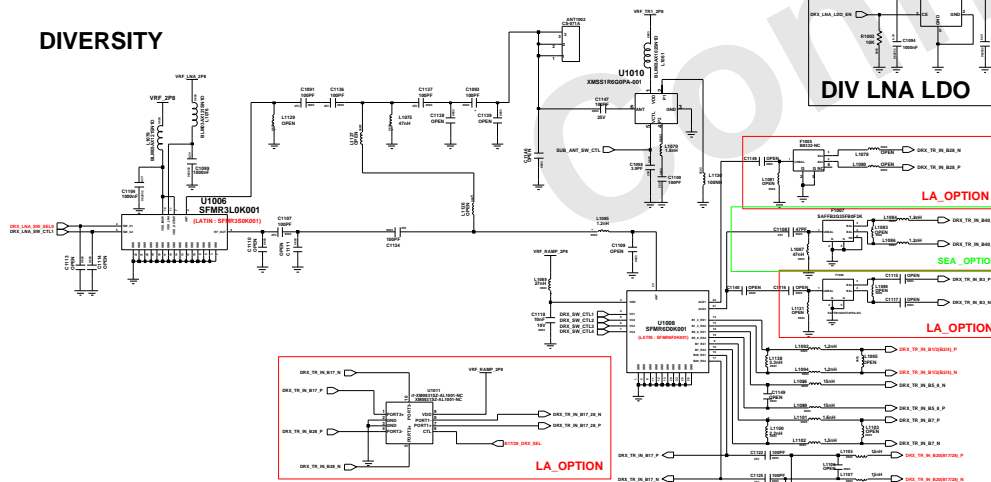
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## BAND7\_PA\_DPX



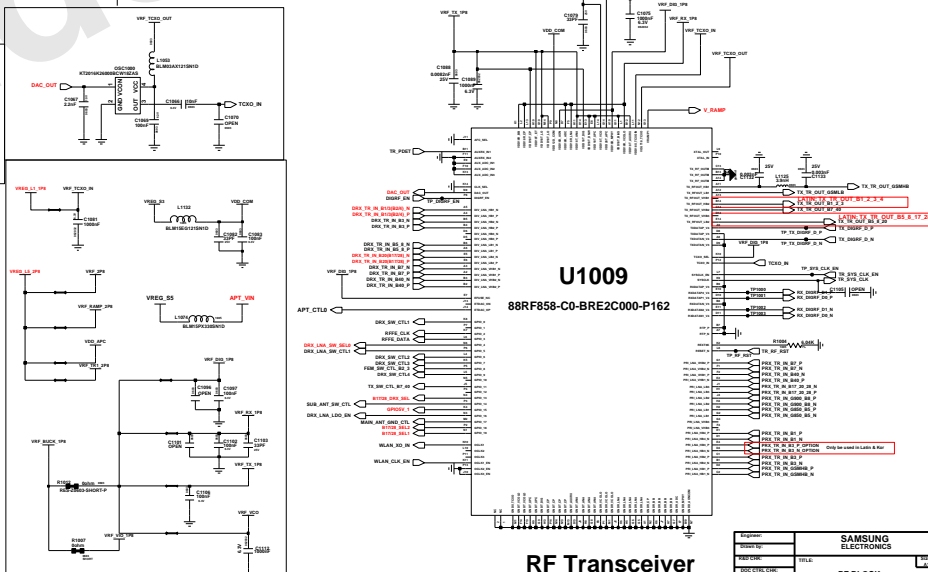
## DIVERSITY



EU  
2G : QUAD  
3G : WCDMA B1/5/8  
4G : B1/3/5/7/8/20

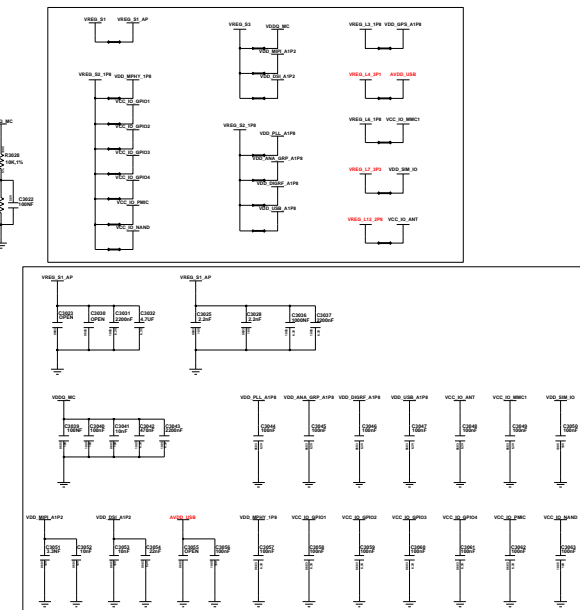
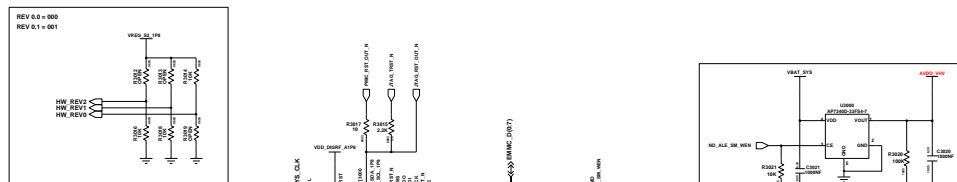
SEA  
2G : QUAD  
3G : WCDMA B1/5/8  
4G : B1/3/5/7/8/20/TD40

LATIN  
2G : QUAD  
3G : WCDMA B1/2/4/5/8  
4G : B1/2/3/4/5/7/11/12B/2/28B

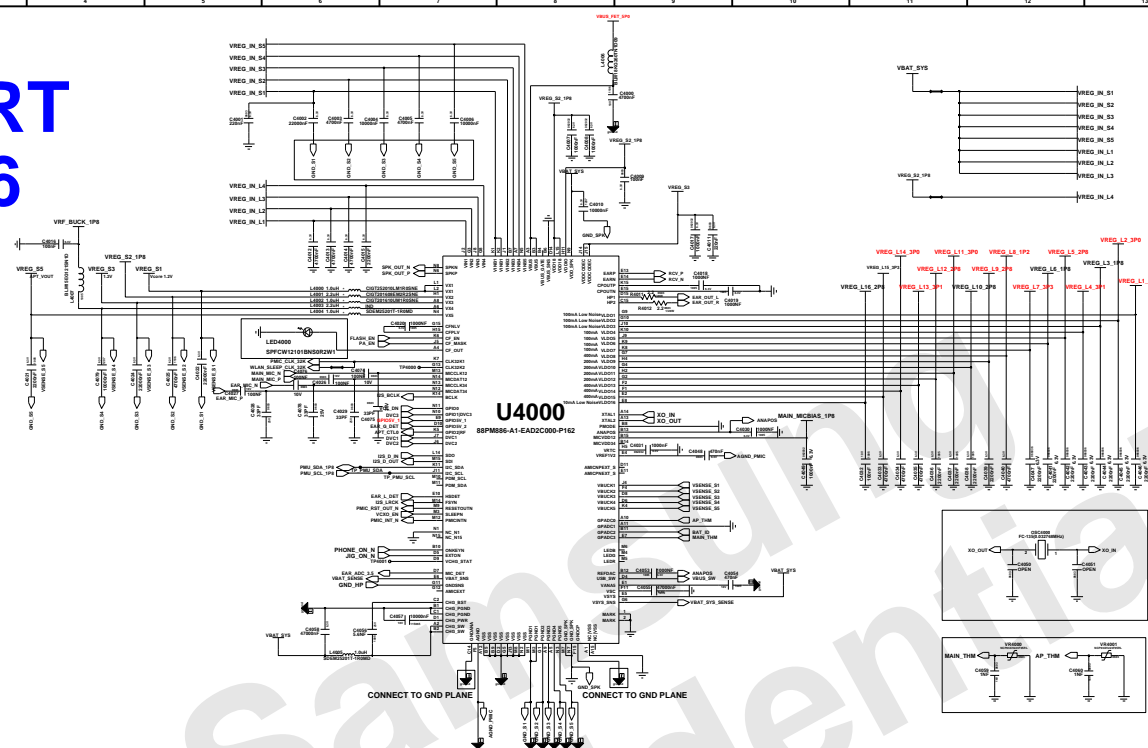
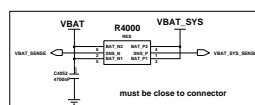




# eMCP



Changed by	Date Changed	Time Changed	QA CHK	REV	Drawing Number	Sheet	4/8
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	Supply	Default [V]	Setting [V]	I <sub>Load</sub> [mA]	Default En
BUCK1	VDD_CORE	1.1V	= SAME	350mA	ON
BUCK2	IDO	1.8V	= SAME	750mA	ON
BUCK3	VDD_RF_6mmic	1.2V	= SAME	150mA	ON
BUCK4	VDD_RF	1.8V	= SAME	750mA	ON
BUCK5	APT	3.3V	=	150mA	OFF
LD01	VDDTCKO	1.8V	= SAME	100mA	ON
LD02	TSP	1.8V	3.0V	160mA	ON
LD03	GPS	1.8V	= SAME	100mA	OFF
LD04	USE	3.1V	= SAME	100mA	ON
LD05	RF switch, FEM	2.8V	=	150mA	ON
LD06	Micro SD ID	1.8V	= SAME	100mA	ON
LD07	USBM1	3.3V	= 1.8V/3.0V	190mA	OFF
LD08	SM_CORE	1.2V	= SAME	450mA	OFF
LD09	SM_VF	2.8V	= SAME	200mA	OFF
LD010	IM_ANA	2.8V	= SAME	200mA	OFF
LD011	LCD	2.8V	= 3.0V	200mA	OFF
LD012	USBM2	2.8V	= 1.8V/3.0V	200mA	OFF
LD013	6mmic CORE.GPS	3.1V	= SAME	400mA	ON
LD014	Micro SD VCC	3.0V	= SAME	450mA	ON
LD015	WFI	3.3V	= SAME	450mA	OFF
LD016	EAR, MIC, Sias	1.8V	= 2.8V	15mA	OFF

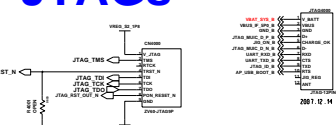
## USER INTERFACE PART

# USB Connector

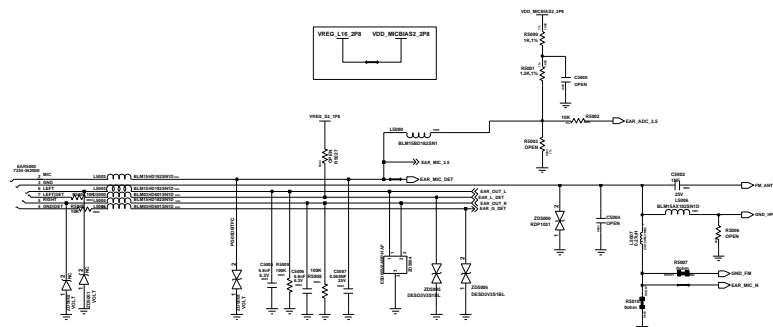
# Battery Connector

The diagram illustrates the internal architecture of the MUC board. At the top, a box represents the MUC board's external pins: VDDA\_1V8, VDD\_MUC\_1V8, VDDA\_5V, and VDD\_MUC\_1V8. Below this, the internal components are shown. The UART section includes UART\_BOOT\_CIN, UART\_RX, and UART\_TX pins, connected to a UART component. The USB section includes USB\_D+, USB\_D-, and USB\_GND pins, connected to a USB component. The MUC\_SDA\_I/F section includes MUC\_SDA\_I/F pins, connected to a MUC\_SDA\_I/F component. The power supply section includes VDDA\_1V8, VDDA\_5V, and VDD\_MUC\_1V8 pins, connected to a power supply component. The diagram also shows the connection to the MUC board's power supply and ground.

## JTAGs

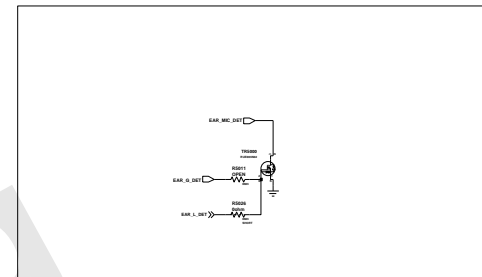
[illegible]

Engineer	PMIC / USER INTERF.
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R&D CHK:	
QC/CTRL CHK:	
MFG ENGNGR CHK:	TITLE:  SM-G7105_REV0.3

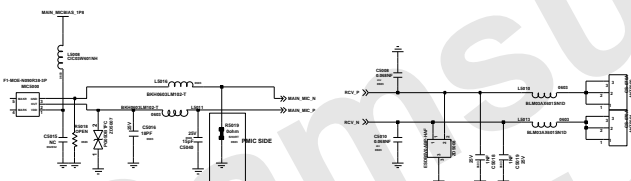


Earjack

Ear MIC

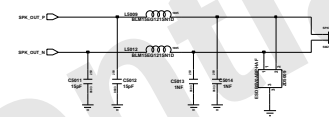


EARPHONE Detect

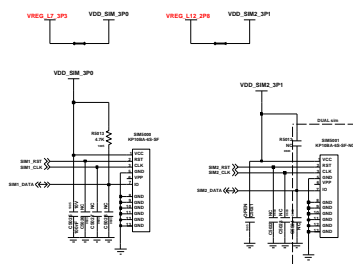


Main MIC

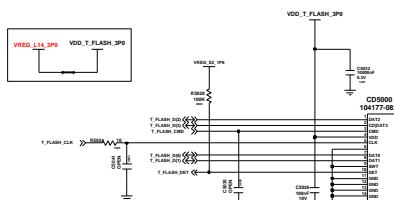
Receiver



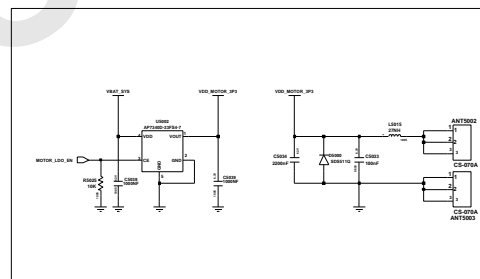
Speaker



SIM CON(SINGLE)

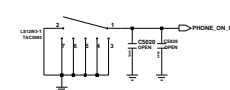


MicroSD Card CON

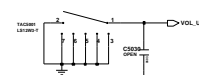


Vibrator PAD

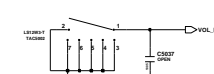
## KEY PART



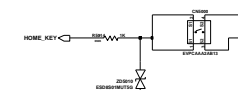
Power\_ON



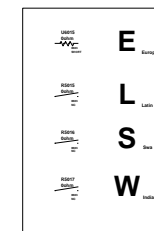
VOL\_UP



VOL\_DOWN

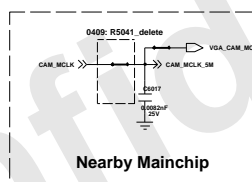
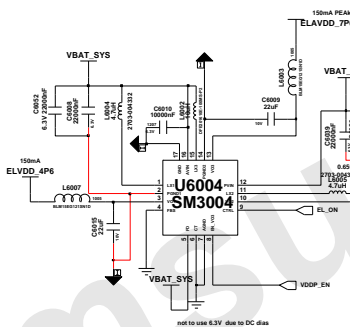


Home Key

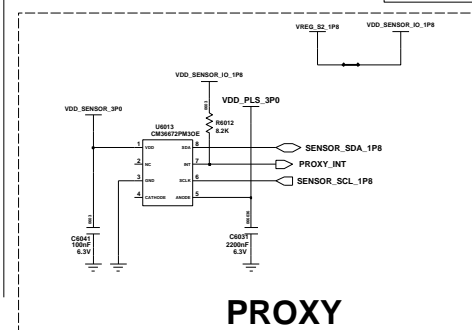
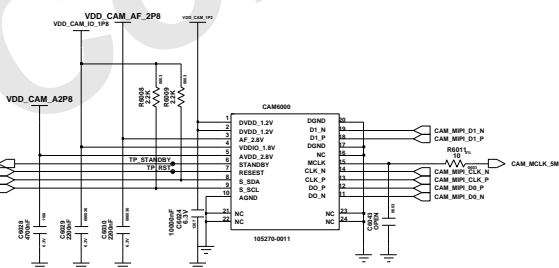
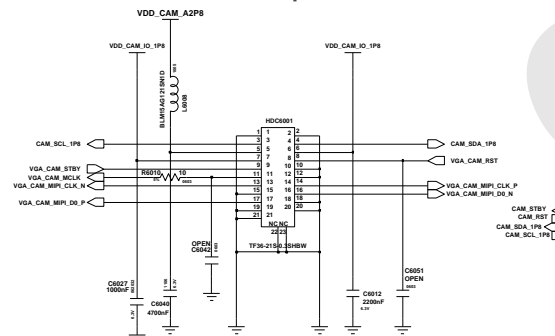
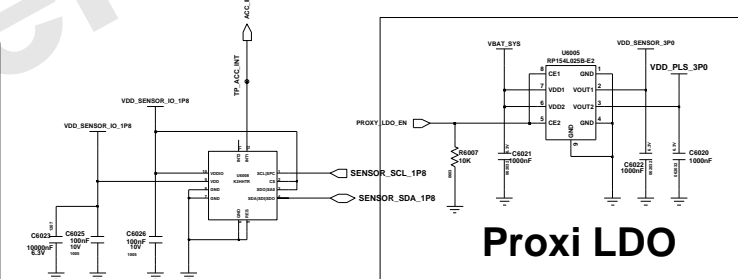


Engineer	AUDIO / MOTOR / KEY
Designer	
YSD DATE	TITLE
YSD DATE	SM-G7105_REV0.3
YSD DATE	Drawing Number
YSD DATE	Size

## TOUCH KEY& DCDC



# Accelero SENSOR



## VGA CAMERA

## 5M CAMERA

## PROXY

Engineer r/34	<h1>LCD &amp; CAM</h1>	TITLE
Drawn by r/34		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		