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ROSELA LABS

# SM-N750_REV0.4

**2013.12.16**
**REFERENCE**


| | | |
|---|---|---|
| sheet01 : | GSM / WCDMA | **100** |
| sheet02 : | GPS, BT/WIFI, NFC, SENSORS | **200** |
| sheet03 : | CP | **300** |
| sheet04 : | AP(Adonis Mini EVT1) | **400** |
| sheet05 : | AP PMIC(S.LSI), IF PMIC | **500** |
| sheet06 : | AUDIO | **600** |
| sheet07 : | CAM, LCD, SUB PCB | **700** |
| sheet08 : | WACOM | **800** |


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| | |
|---|---|
| Engineer | SAMSUNG ELECTRONICS |
| Drawn by: | |
| R&D CHK: | TITLE: N900 SIZE: A1 |
| DOC CTRL CHK: | |
| MFG ENGR CHK: | |
| QA CHK: | Drawing Number / SHEET 00/07 |

Changed by: | Date Changed: | Title Changed

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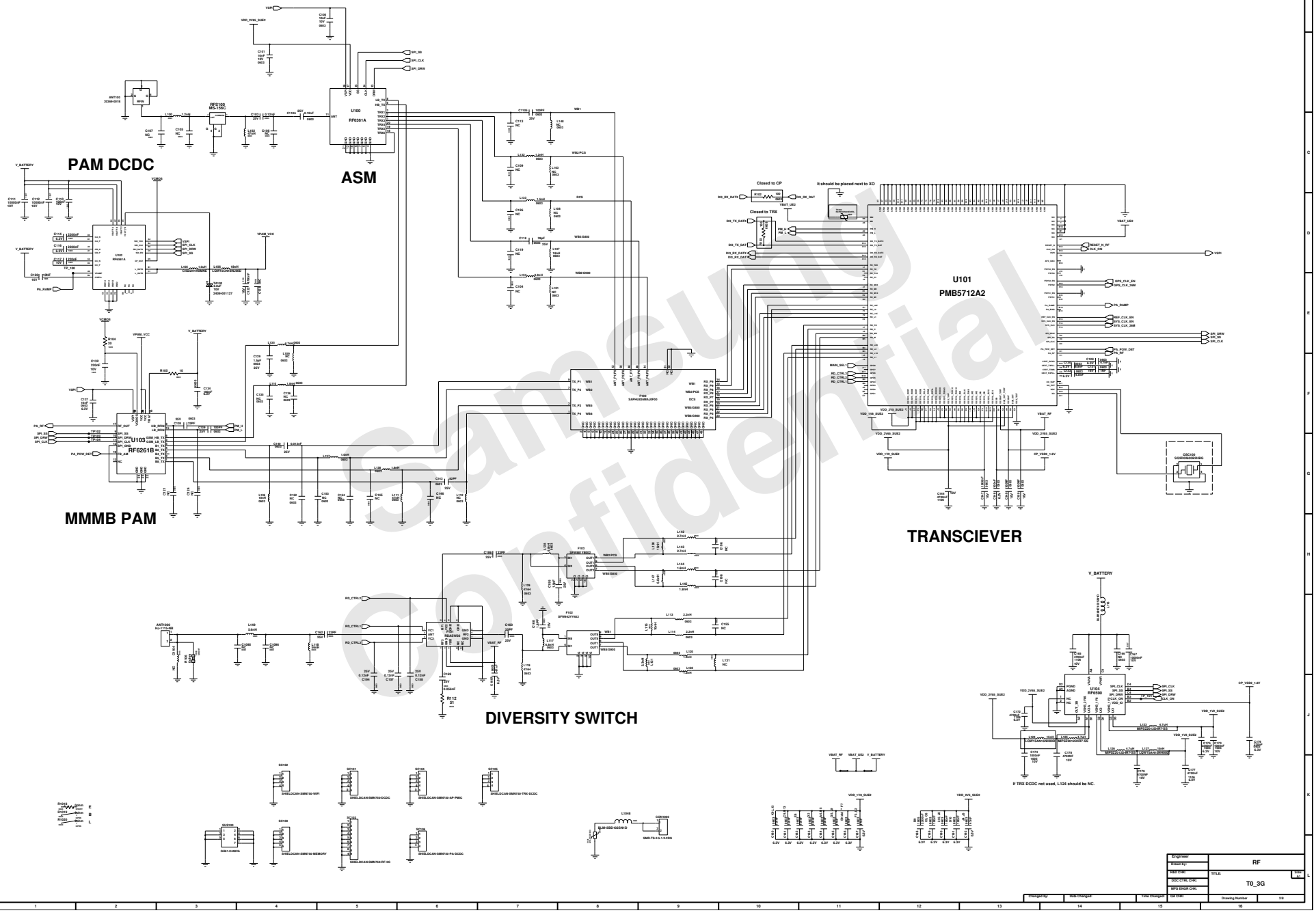
# RF Part

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| | |
|---|---|
| Engineer | RF |
| Issued By: | |
| R&D CHK: | TITLE: |
| DOC CTRL CHK: | T0_3G |
| BPG ENGR CHK: | |
| QA CHK: | Drawing Number: 3/8 |

| Changed By: | Date Changed: | Time Changed: |
|---|---|---|

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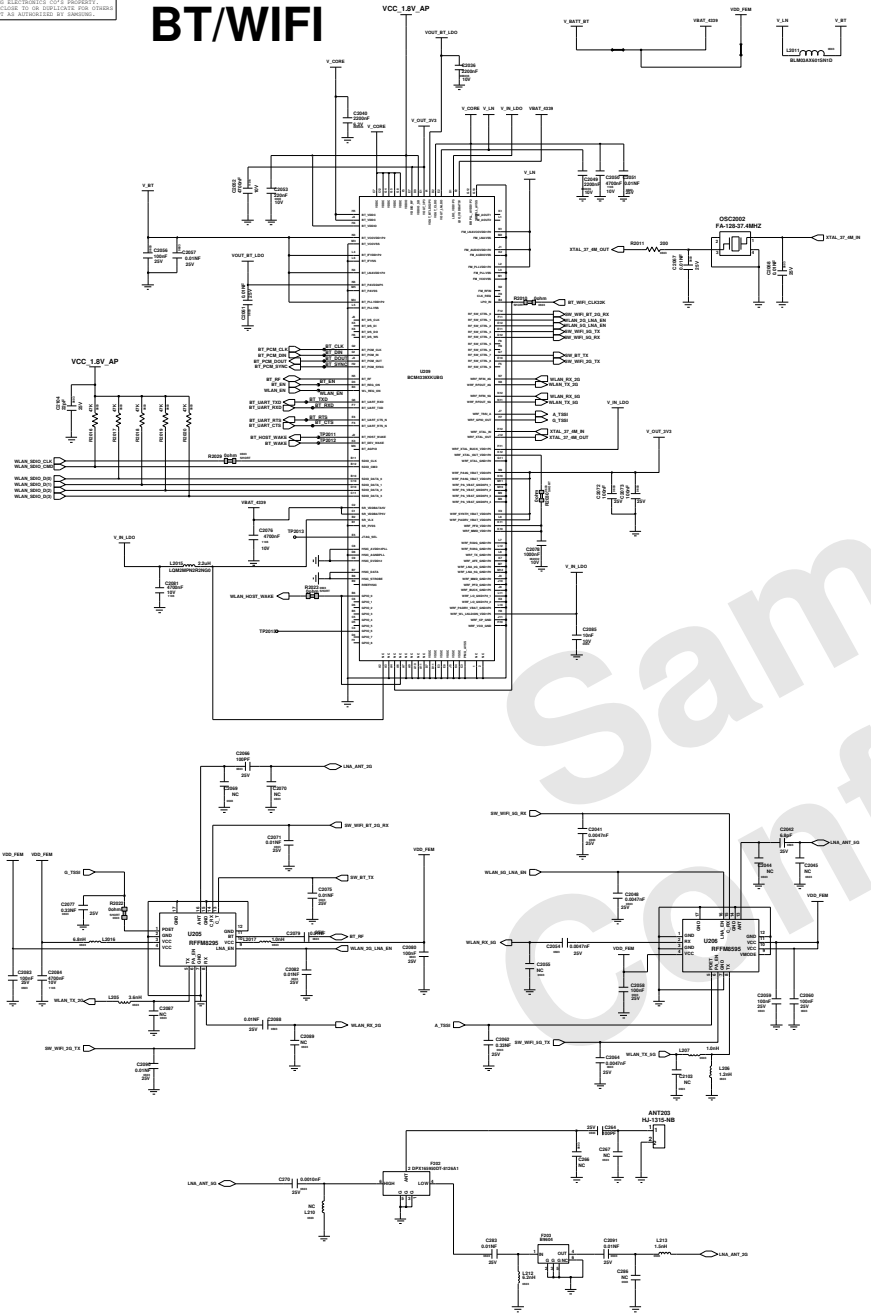
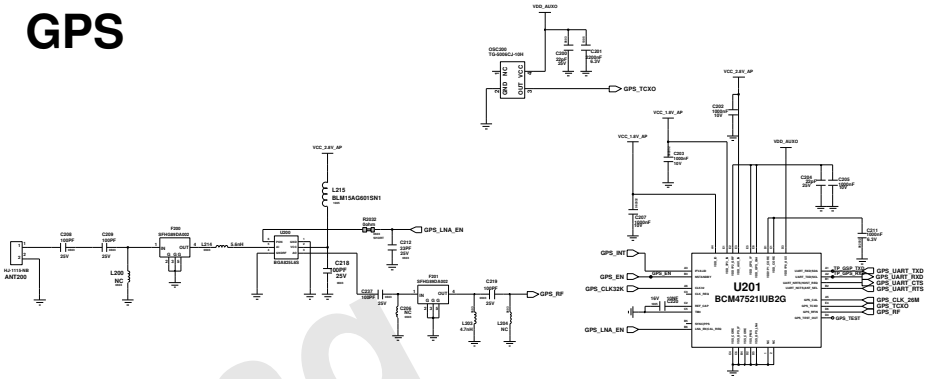
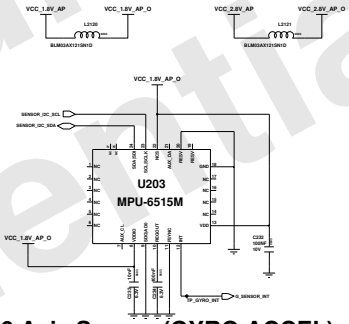
# BT/WIFI



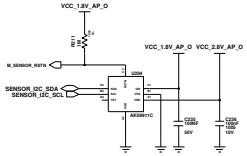

# GPS




# SENSORS

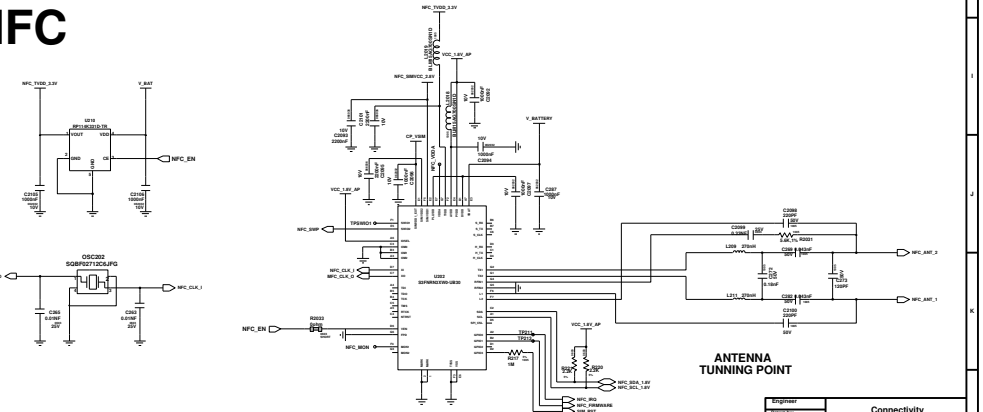



6 Axis Sensor (GYRO,ACCEL)




Magnetic Sensor

# NFC




| | |
|---|---|
| Engineer | |
| Research By | Connectivity |
| R&D CHK: | TITLE: SM-N900 |
| DOC CONTROL CHK: | SIZE: A1 |
| RFG ENGR CHK: | |
| QA CHK: | Drawing Number: 3/9 |

| Changed By | Date Changed | Time Changed |
|---|---|---|

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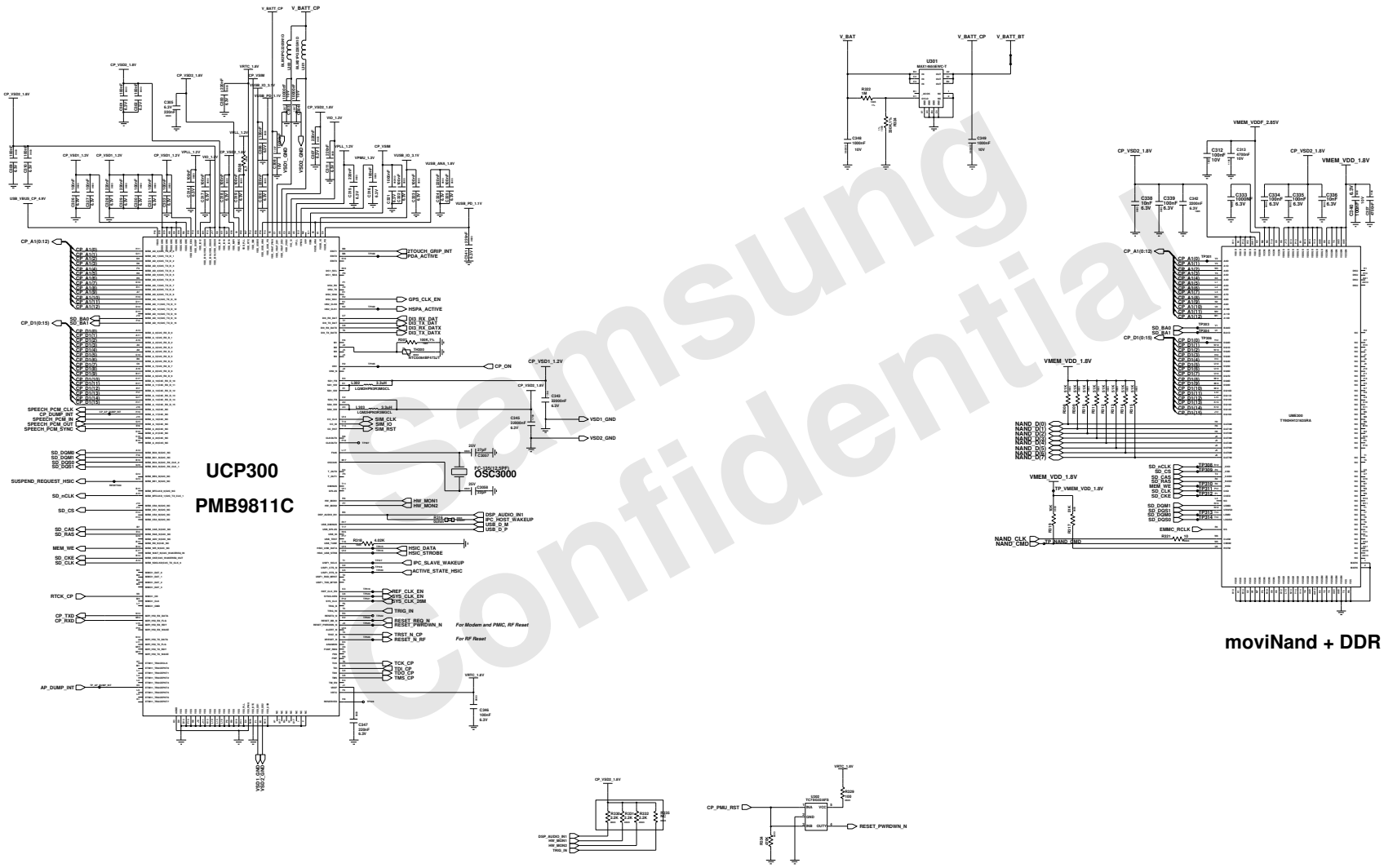
# CP PART

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| | |
|---|---|
| Engineer | CP + Memory |
| Issued By: | |
| R&D CHK: | TITLE: T0_3G |
| DOC CTRL CHK: | SIZE: A1 |
| MFG ENGR CHK: | |
| QA CHK: | Drawing Number: File |

| Changed By: | Date Changed: | Time Changed: |
|---|---|---|

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ANY USE OR DISCLOSURE OF SAMSUNG

# EXYNOS5260, MOVINAND

| | |
|---|---|
| 0100 | HL_LTE_EVT1_REV0.0 |
| 0001 | |
| 0010 | |
| 0011 | |
| | |
| | |

1010

**HW Version Check**
H/W Revision Setting

**AP CLOCK**

CCI HIgh : CCI BUS Dis
CCI Low : CCI BUS En

OM[4:1] 1000
eMMC4.5 Booting

**iROM BOOT**
eMMC4.5 / X-TAL

Core Mode (XOM[0])
High : Eagle Booting
Low : KFC_Booting(Default)

| | |
|---|---|
| OM[4:1] 0000 | 1st eMMC(eMMC memory power : always on), 2nd USB |
| OM[4:1] 0010 | 1st SD/MMC, 2nd USB |
| OM[4:1] 0100 | 1st eMMC, 2nd USB |
| OM[4:1] 1000 | 1st eMMC(eMMC memory power : always on), 2nd SD |
| OM[4:1] 1100 | 1st eMMC, 2nd SD/MMC |

**eFUSE Circuit**

**AP/CP JTAG**

**PCB JTAG**

GT-I9500_REV0.5

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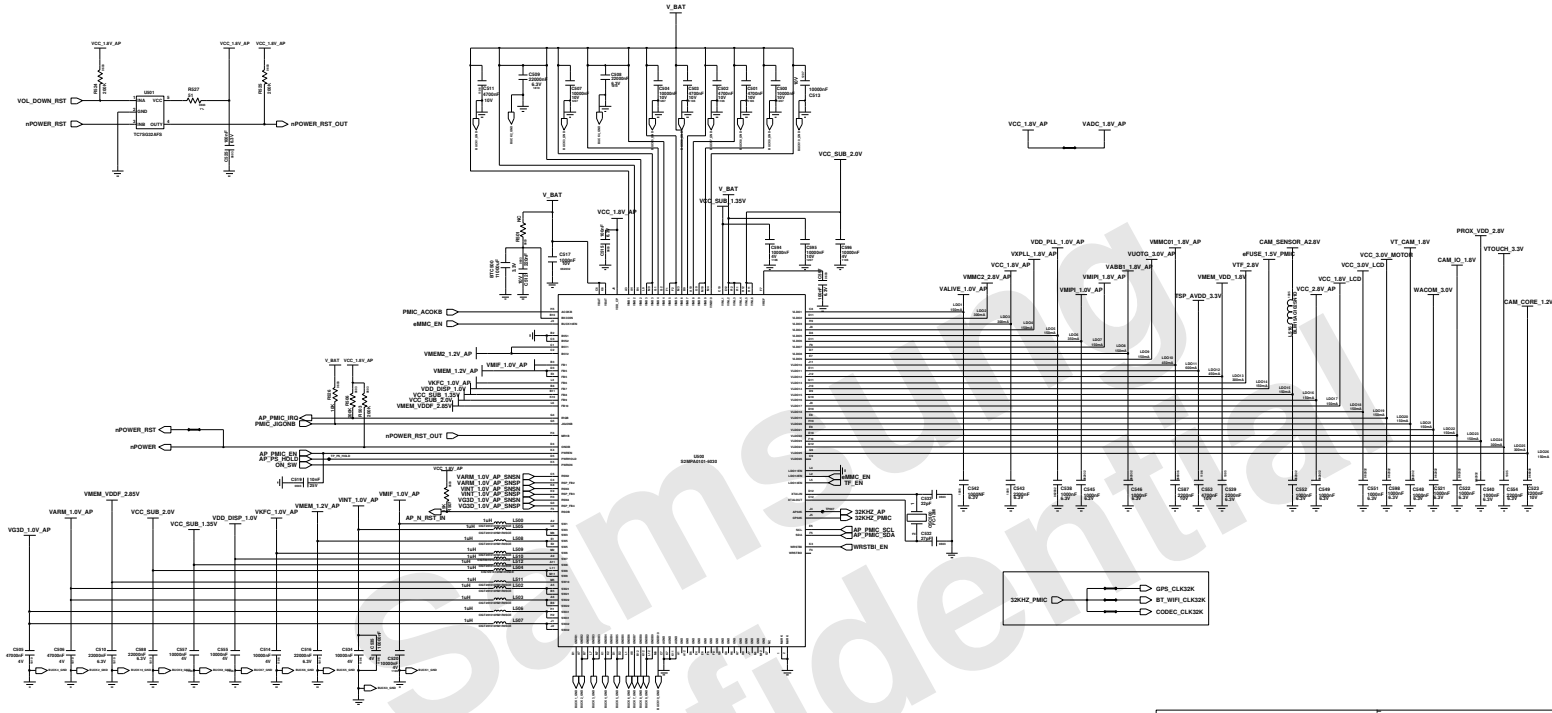
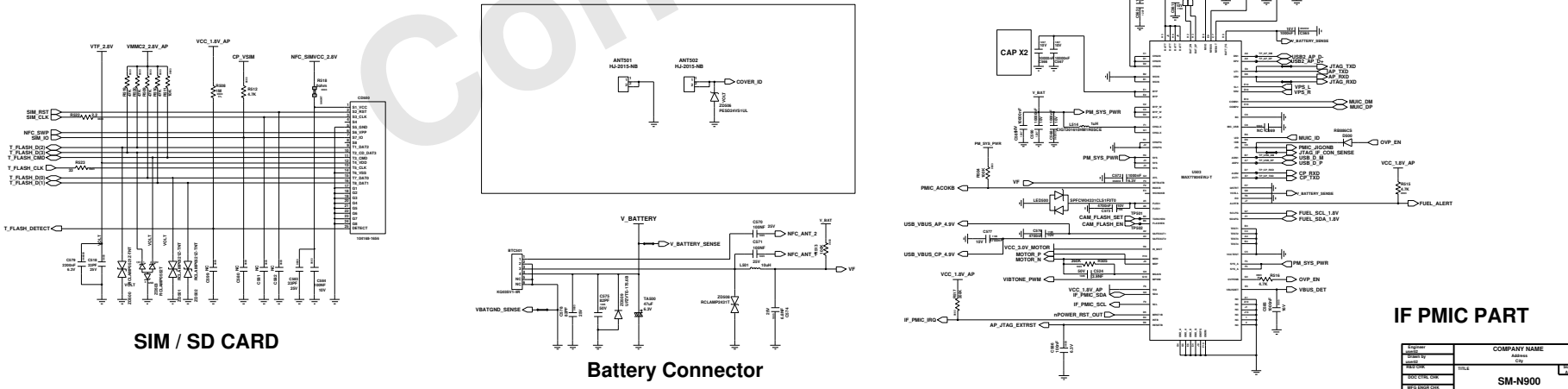
# POWER PART





## SIM / SD CARD

## Battery Connector

## IF PMIC PART

| | |
|---|---|
| COMPANY NAME | |
| Address | |
| City | |
| TITLE | SM-N900 |
| SIZE | A1 |

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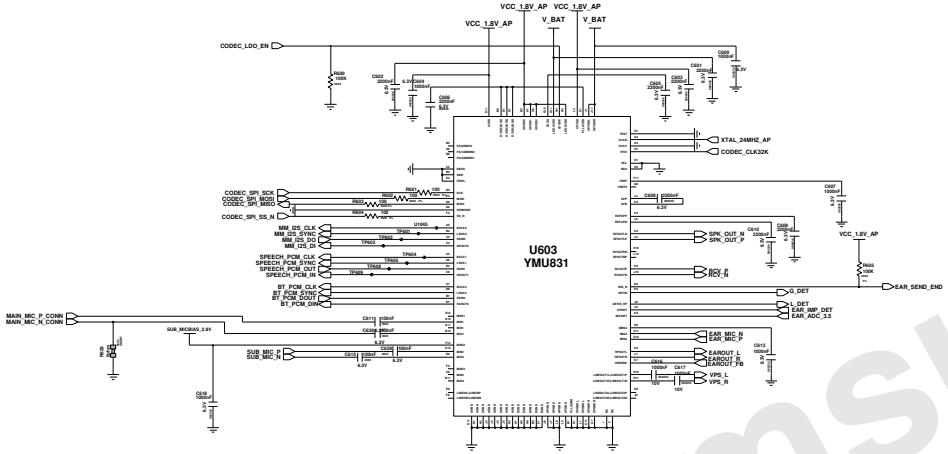
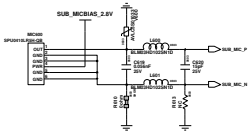
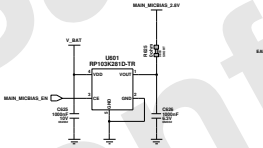
# Audio PART

| MAIN | CHOI MIN JUN |
|---|---|
| SUB | SONG MIN WOO |

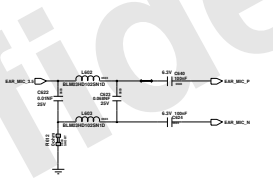


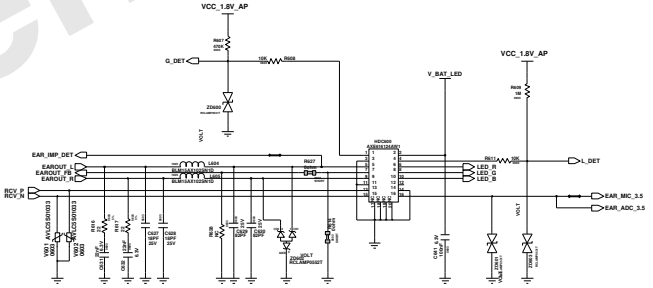

**SUB MIC**




**MIC Bias LDOs**

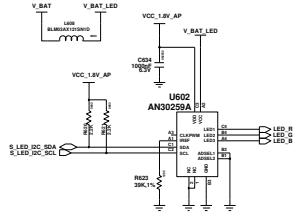



**Ear MIC**



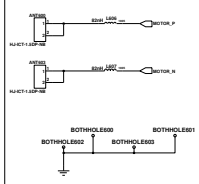

**E/J+RCV+SVC LED**

| MAIN | SON HEE |
|---|---|
| SUB | AN SUNG BIN |

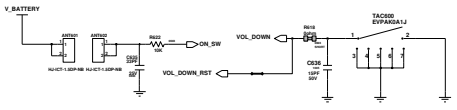



**SVC LED DRIVER**

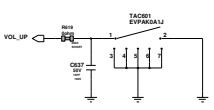
| MAIN | KIM KYU WON |
|---|---|
| SUB | JANG KYUNG OK |




**POWER**




**VOL_DOWN**




**VOL_UP**

| | |
|---|---|
| Engineer | AUDIO |
| Design By | |
| R&D CHK | TITLE: SM-N900 |
| DOC CTRL CHK | |
| BPG ENGR CHK | |
| QA CHK | Drawing Number |

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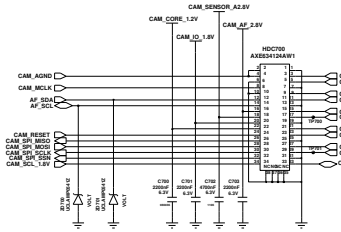
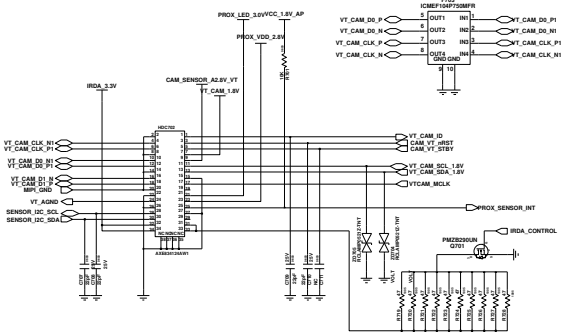
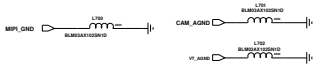
# CAMERA PART



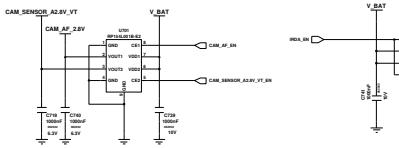
8M_Camera Connector (MIPI)



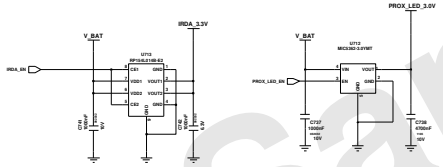
VT_CAMERA (2M)



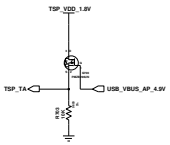
AGND



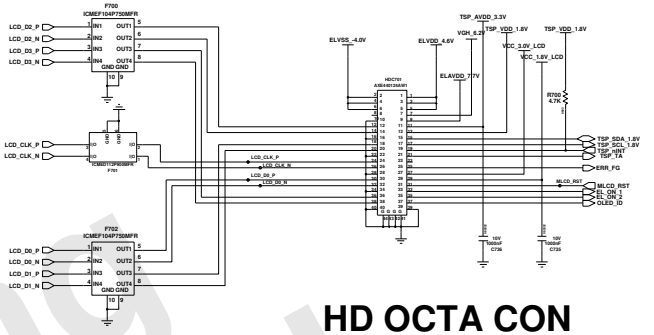
VT CAM LDO



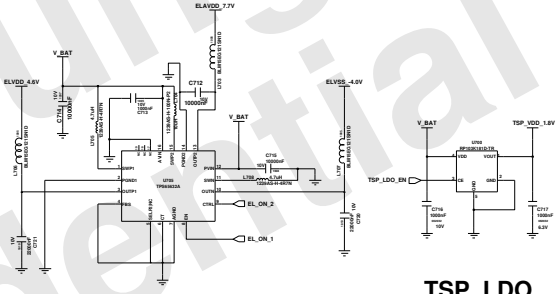
# DISPLAY PART



Charging_DET



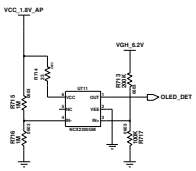
HD OCTA CON



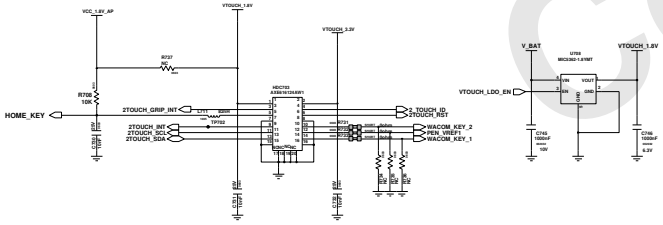
HD OCTA DC-DC



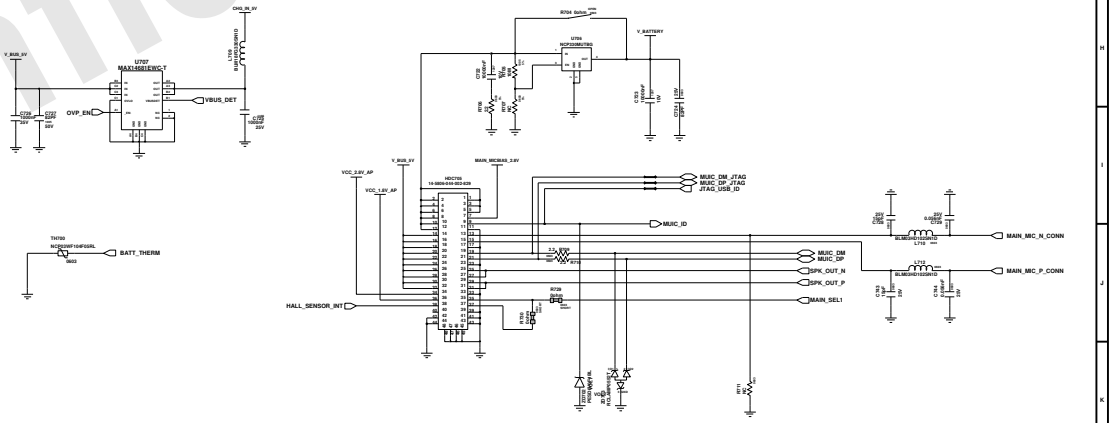
TSP_LDO



ESD DET



2Touch_Key



SUB FPCB IF CONN.
(11PIN USB / Main ANT / SPK )

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| | |
|---|---|
| Engineer: | CAM / DISPLAY |
| Drawn by: | |
| Design by: | |
| Appd: | |
| RDD CHK: | |
| DOC CTRL CHK: | |
| BPG ENGR CHK: | |
| QA CHK: | |
| TITLE: | SM-N900 |
| SIZE: | A1 |
| REV: | |
| Drawing Number: | |
| Last Changed by: Jack | Date Changed: Monday, November 23, 2009 |
| Time Changed: 8:49:14 am | 1/16 |

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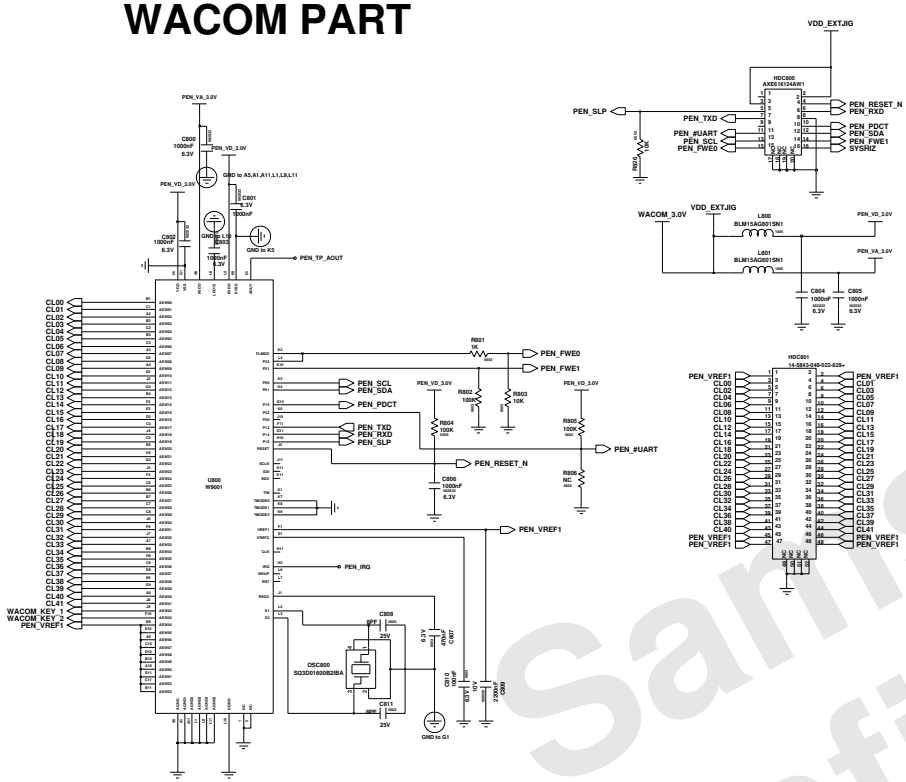
# WACOM PART



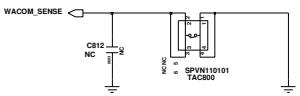




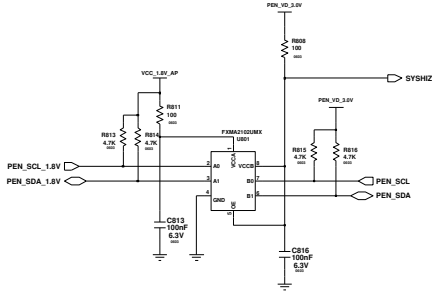


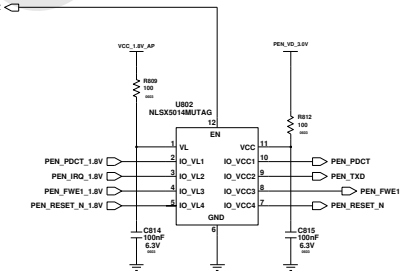




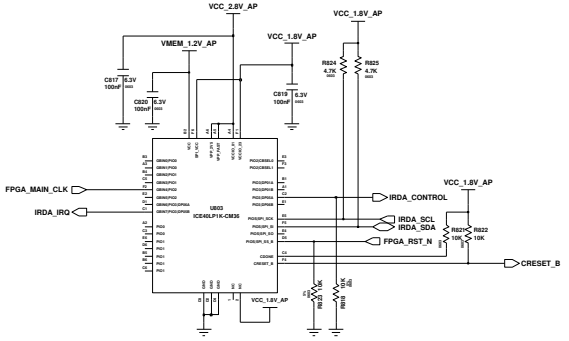



# WACOM PEN DETECT




# IR LED




| | | | |
|---|---|---|---|
| Engineer | | WACOM / MHL | |
| Drawn by: | | | |
| R&D CHK: | | TITLE: | SIZE: A1 |
| DOC CTRL CHK: | | SM-N900 | |
| EPC ENGR CHK: | | | |
| Changes by: | Date Changed: | Time Changed: QA CHK: | Drawing Number |

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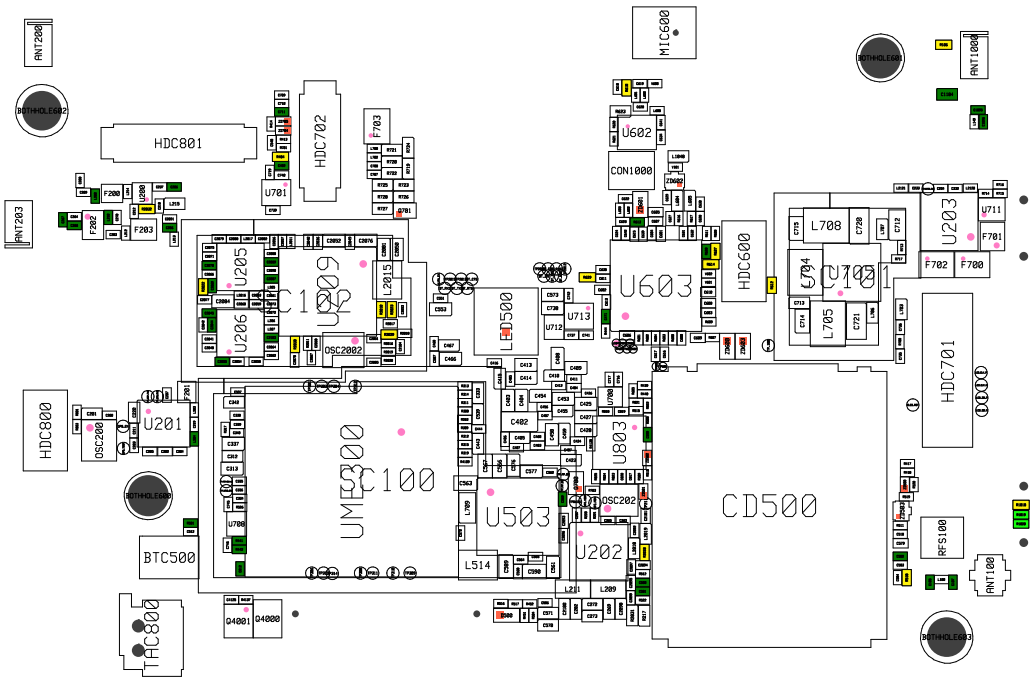
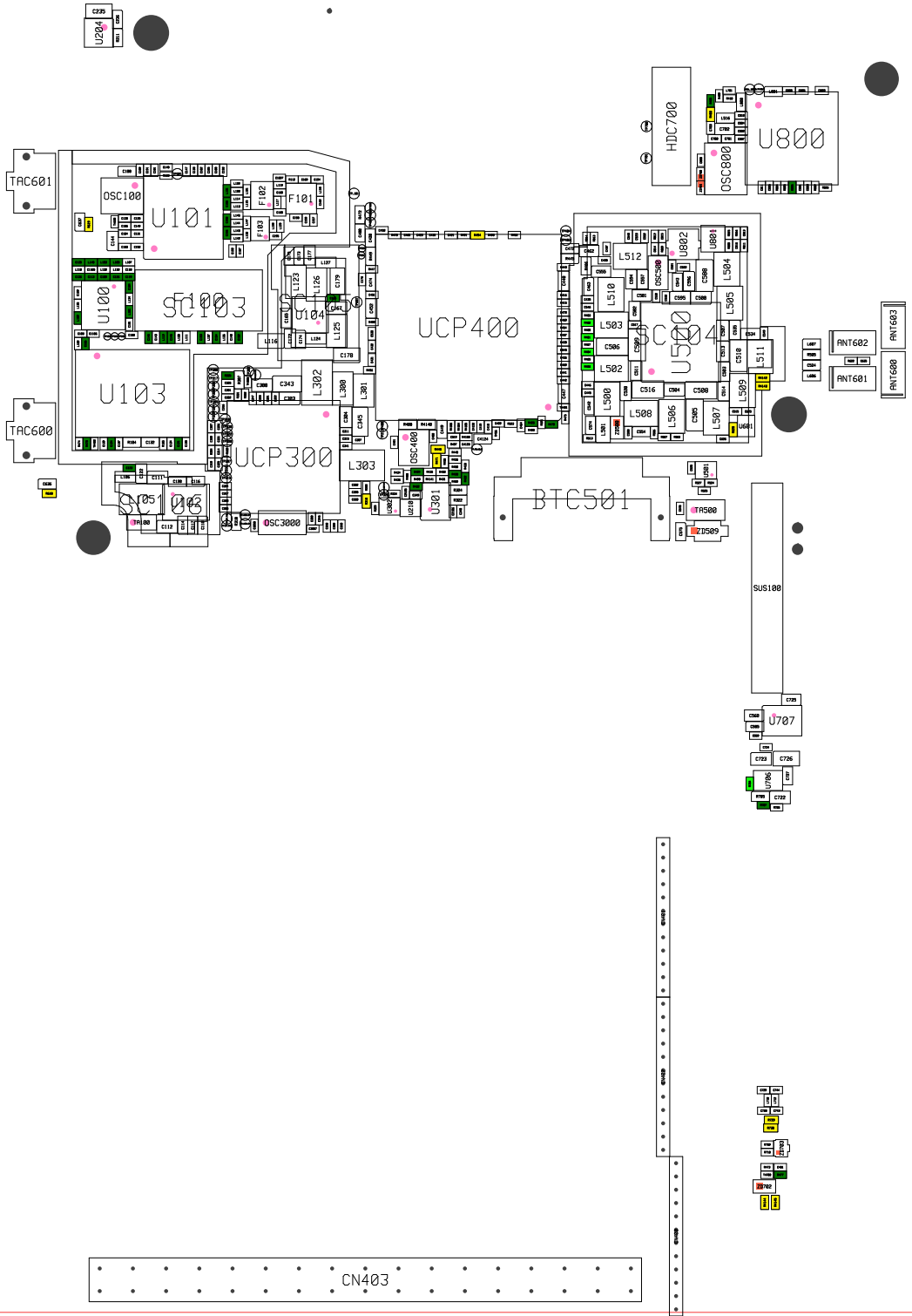
Model :
Revision :
Manufacture Count :
Charge :
Contact :
Date :
Routing :
Resin :



Model :
Revision :
Manufacture Count :
Charge :
Contact :
Date :
Routing :
Resin :