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ROSELA LABS

SM_A810S_REV0.4

2016.08.03

sheet01 : GSM / WCDMA / LTE / CDMA RF
sheet02 : TRANSCEIVER(SHNNON933) / CP(SHANNON333) / CP PMIC
sheet03 : BT/WIFI / GPS / TDMB / NFC
sheet04 : AP (EXYNOS7420 - ISTOR)
sheet05 : AP (EXYNOS7420 - ISTOR)
sheet06 : PMIC / IF PMIC / BATTERY CON
sheet07 : AUDIO/ IF SUB CON
sheet08 : SIM / SD / BTP / SENSOR / OCTA / CAM / UPPER SUB

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Engineer:
Drawn by:
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:
QA CHK:
TITLE: A8_ISTOR
Size: A1
Changed by: ssmm
Date Changed:
Time Changed:
REV:
Drawing Number:
Page: 5

user87|swcad87|10.252.50.171|20160808-153237

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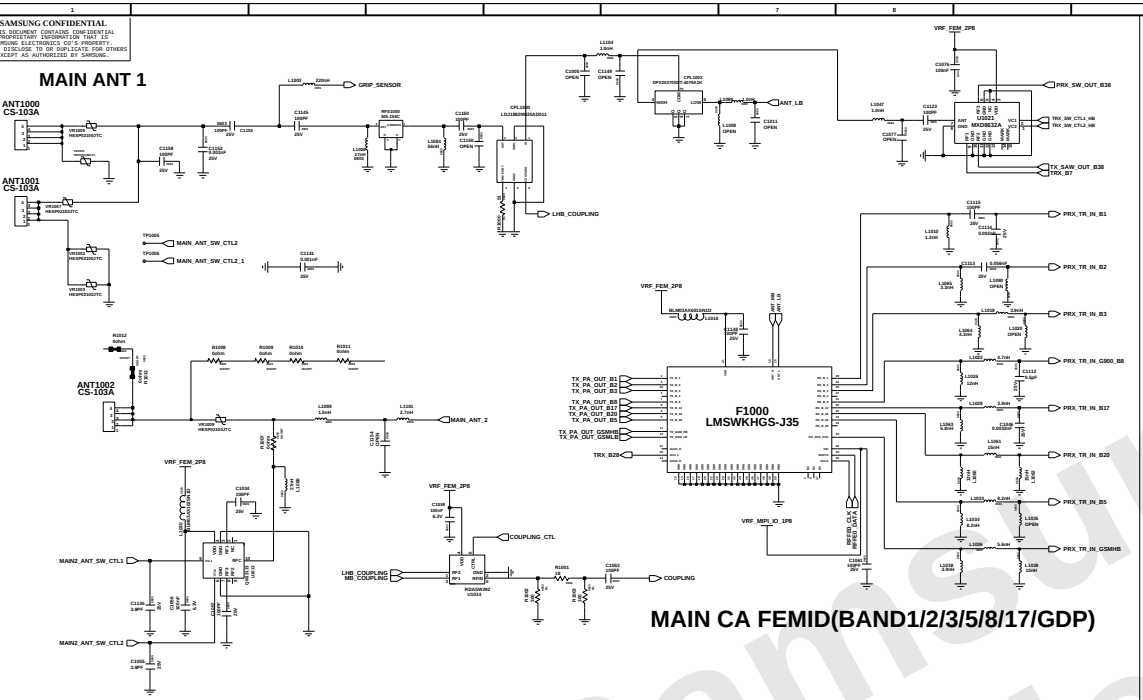
MAIN ANT 1



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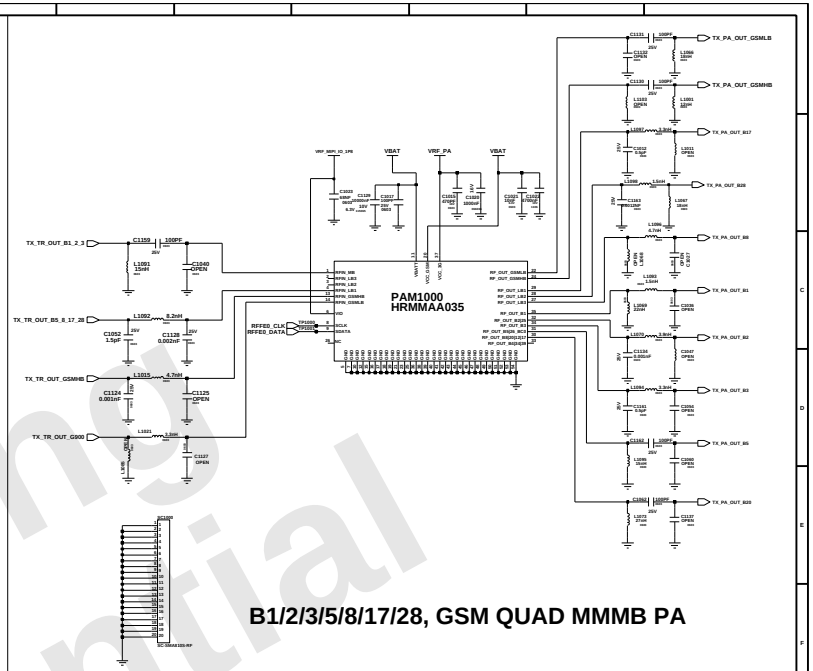

B1/2/3/5/8/17/28, GSM QUAD MMMB PA

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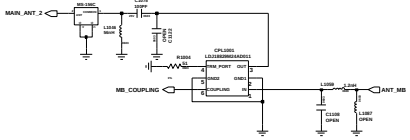

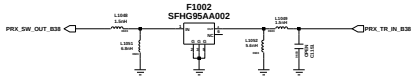

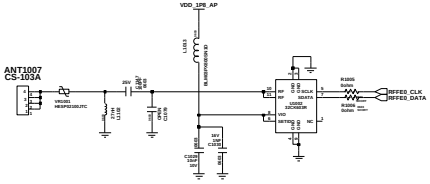

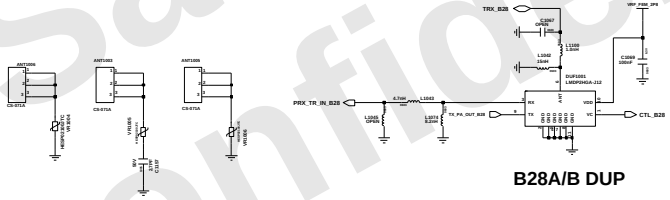

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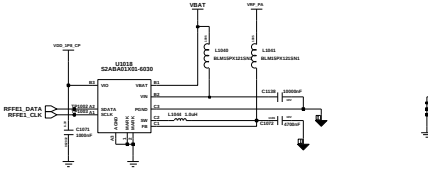

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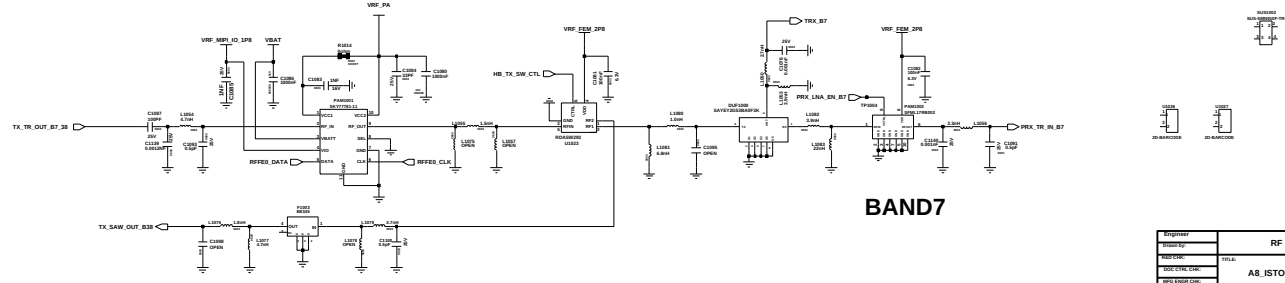

BAND7

Engineer	RF MAIN
Manager Sign	
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RPG ENGR CHK:	
QA CHK:	Drawing Number: 1/11

Changed By	Date Changed	Time Changed

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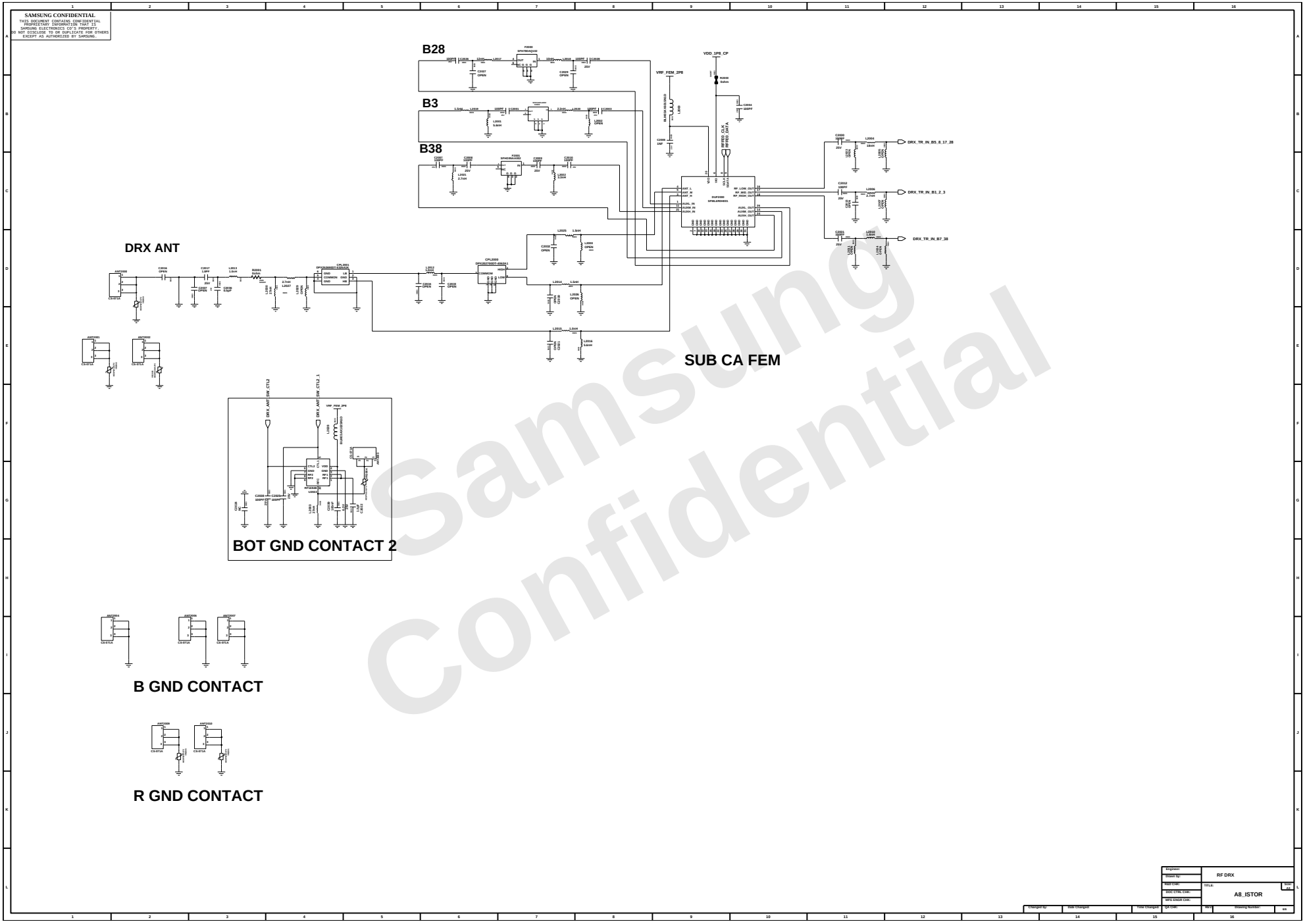

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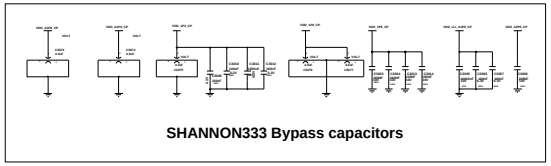
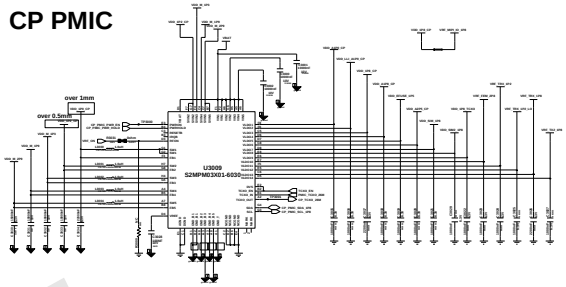
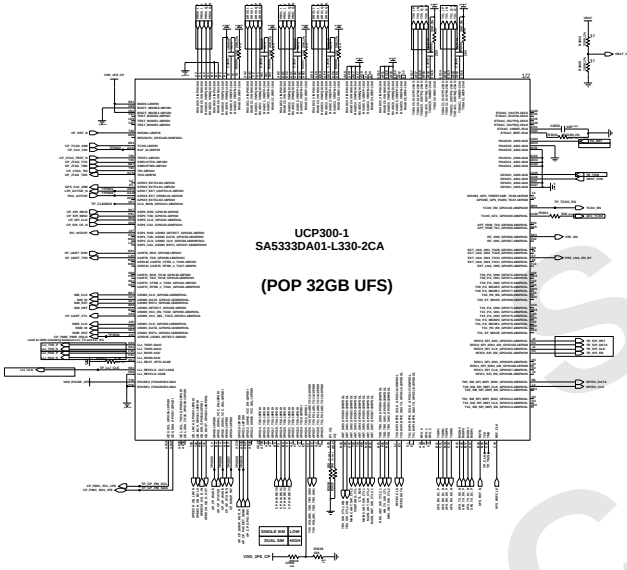
CP PART (S333)



SHANNON333 Bypass capacitors

CP PMIC





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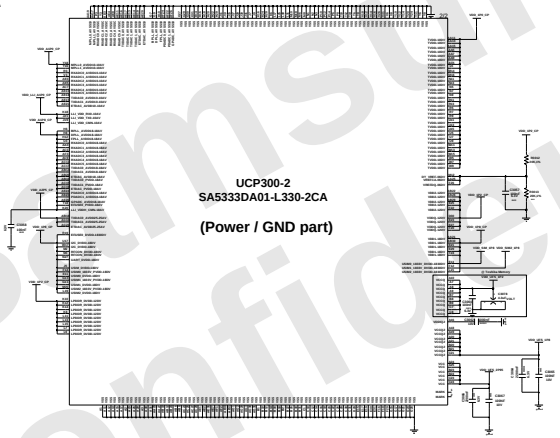
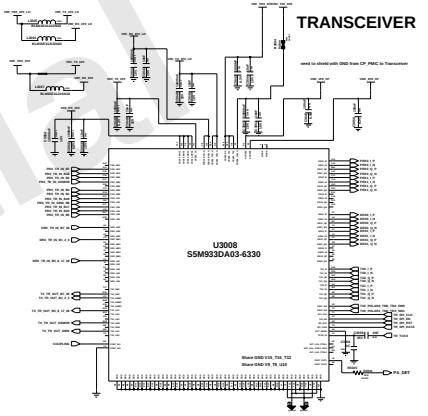
(POP 32GB UFS)

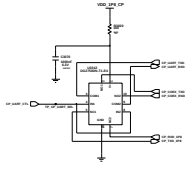


UCP300-2
SA5333DA01-L330-2CA

(Power / GND part)

TRANSCEIVER





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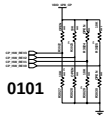

0101

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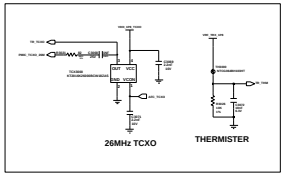
REV0.2	0010	
REV0.2A	0011	
REV0.3	0100	
REV0.3A	0101	

CP JTAG



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CP / CP PMIC	
A8_ISTOR	

user87|swcad87|10.252.50.171|20160808-153241

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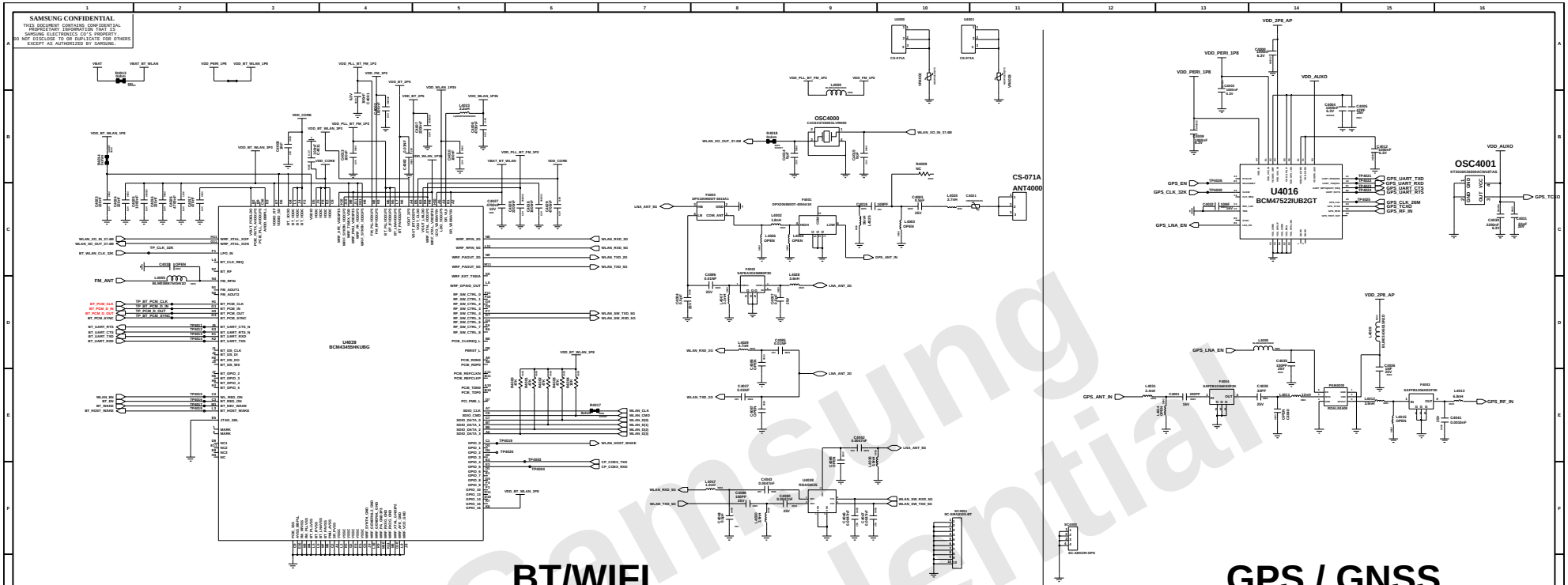

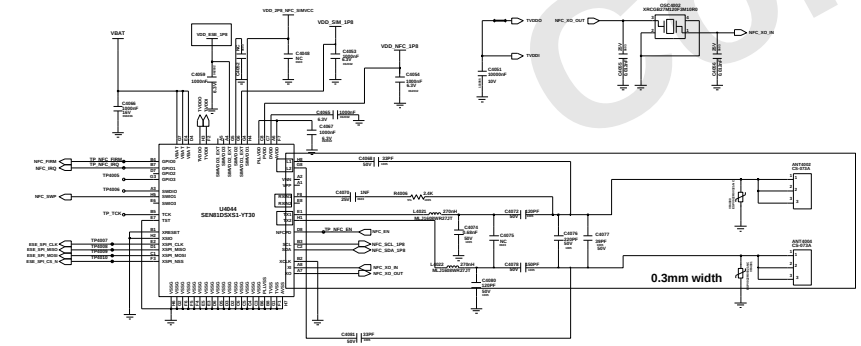

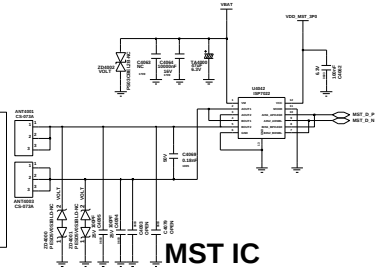

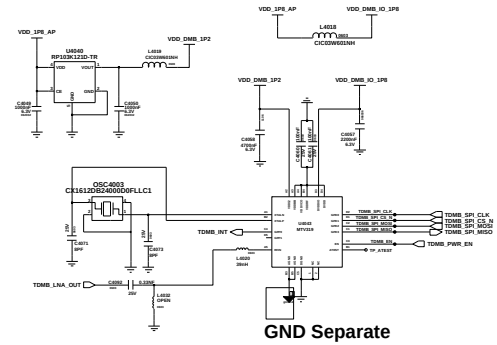

Engineer		CONNEOCTIVITY / TDMB	
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Changed By:	Date Changed:	Time Changed:	QA CHK:
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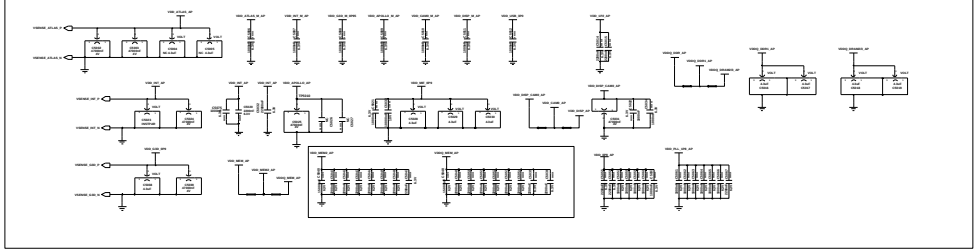
4. EXYNOS7420(ISTOR), eMMC

AP DECAP.



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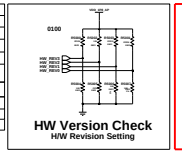
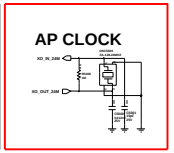
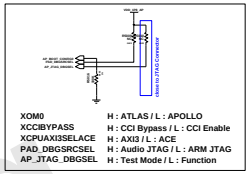


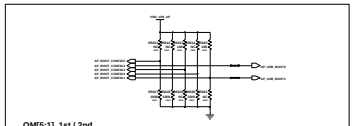
HW Version Check
H/W Revision Setting

AP CLOCK





XOM0	H : ATLAS / L : APOLLO
XCCIBYPASS	H : CCI Bypass / L : CCI Enable
XCPUAXI3SELACE	H : AXI3 / L : ACE
PAD_DBGSRCSEL	H : Audio JTAG / L : ARM JTAG
AP_JTAG_DBGSEL	H : Test Mode / L : Function



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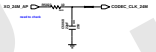
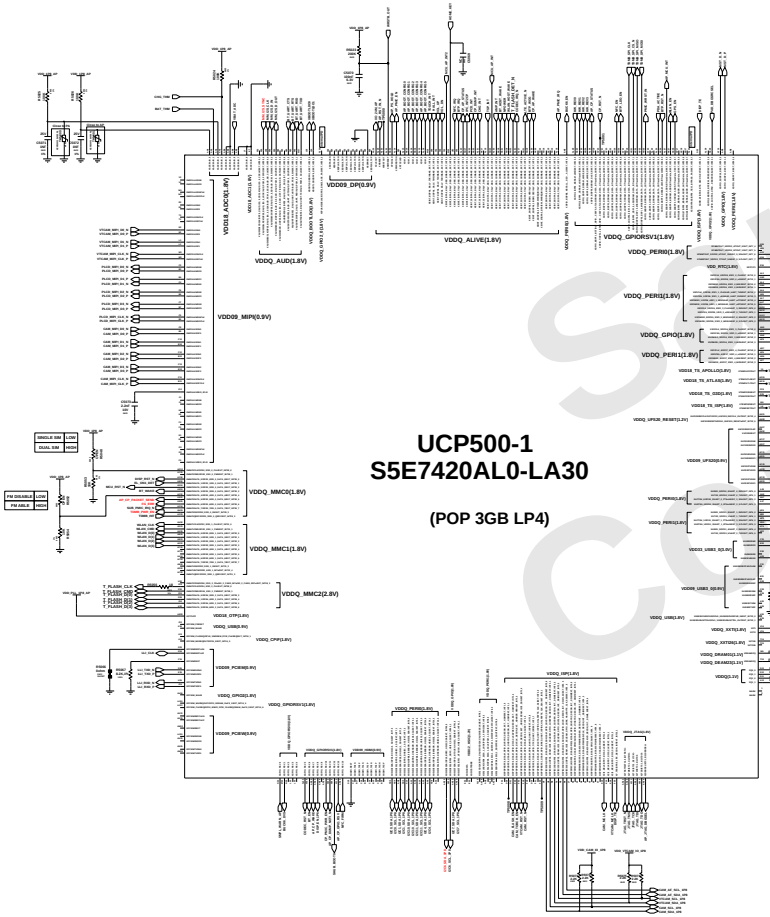
OM[5:1] 00010	SD_MMC (MSH CH2) / USB
OM[5:1] 00100	eMMC (MSH CH0) / USB
OM[5:1] 00101	UFS / USB
OM[5:1] 01000	eMMC (MSH CH0) with SWRST / SD_MMC (MSH CH2)
OM[5:1] 01100	eMMC (MSH CH0) / SD_MMC (MSH CH2) need to check
OM[5:1] 01101	UFS / SD_MMC (MSH CH2)

iROM BOOT

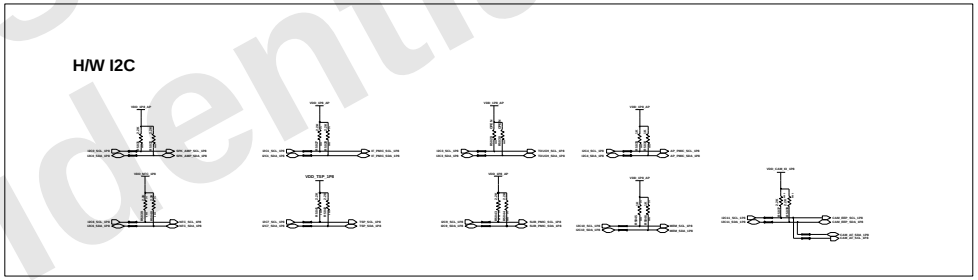




UCP500-1
S5E7420AL0-LA30

(POP 3GB LP4)

H/W I2C



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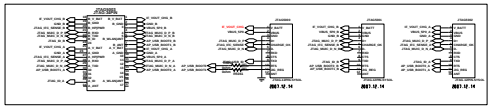

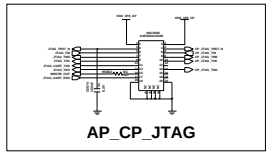

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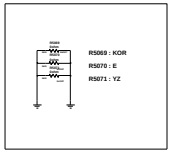

RS069	KOR
RS070	E
RS071	YZ

Engineer	
Assistant	AP, SIM&SD SOCKET
Technician	
Project Name	SM-G920F
Documentation	

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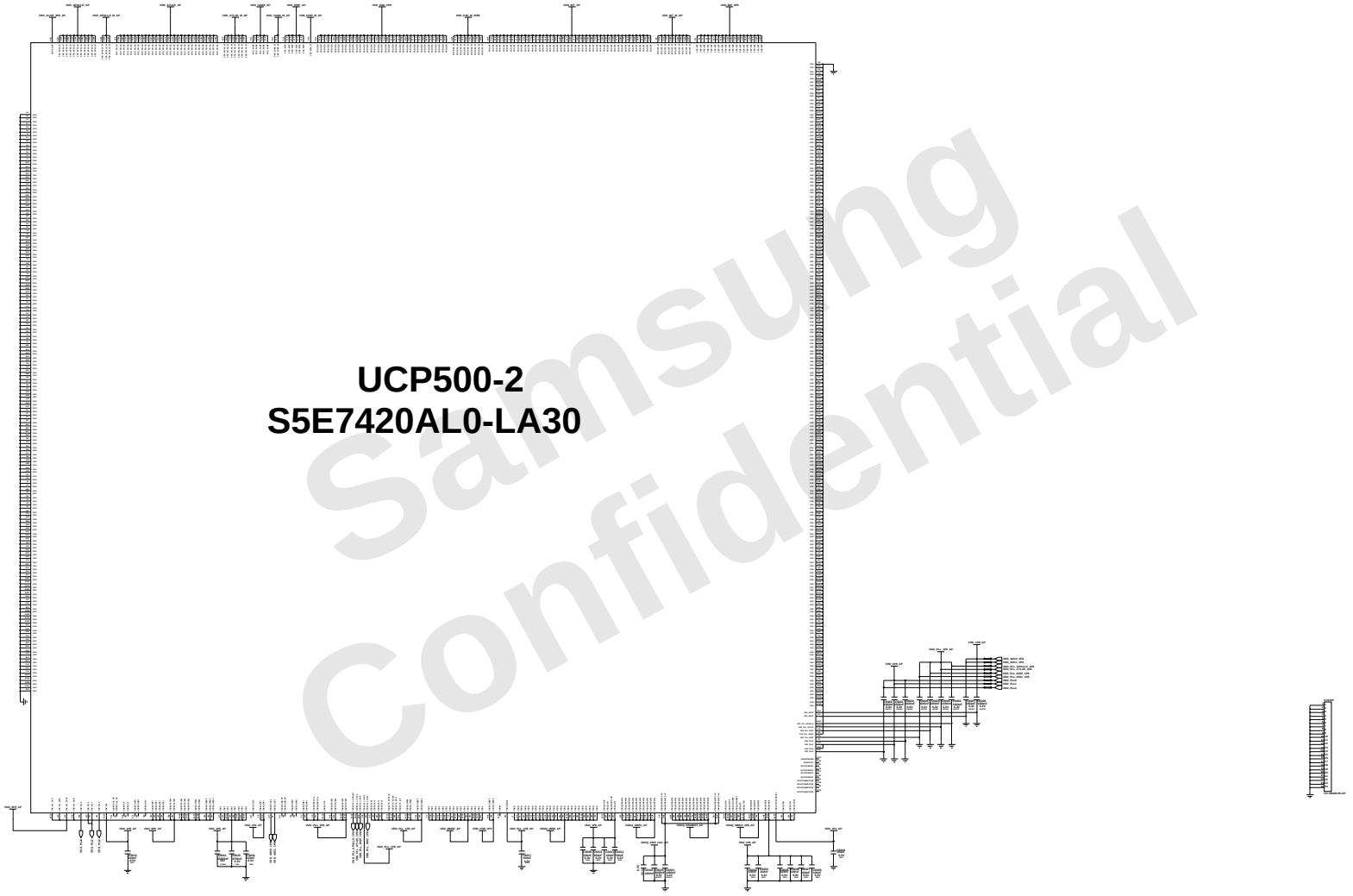
UCP500-2
S5E7420AL0-LA30



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AP

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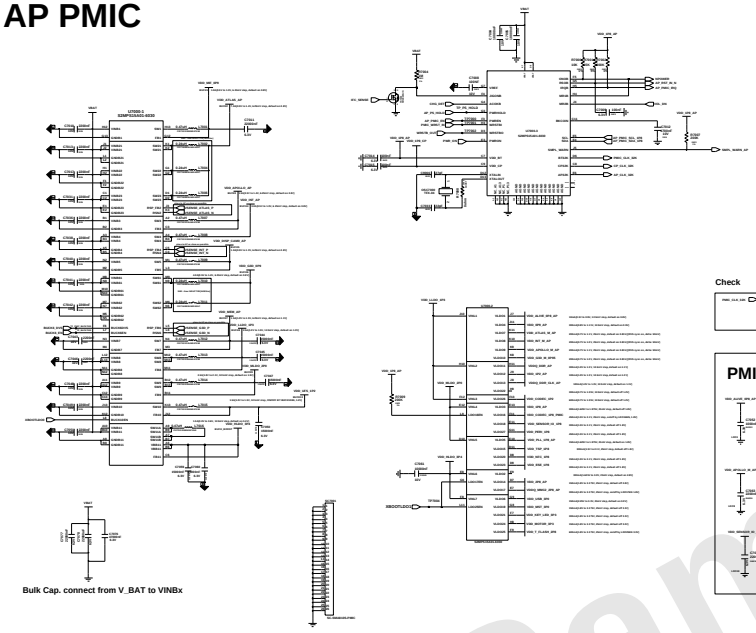
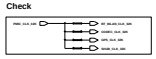
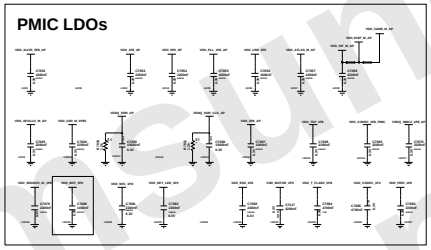
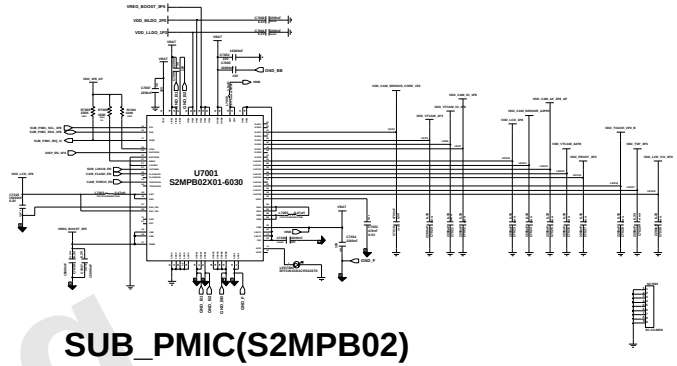
AP PMIC



Bulk Cap. connect from V_BAT to VIN8x







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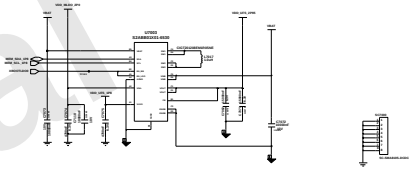

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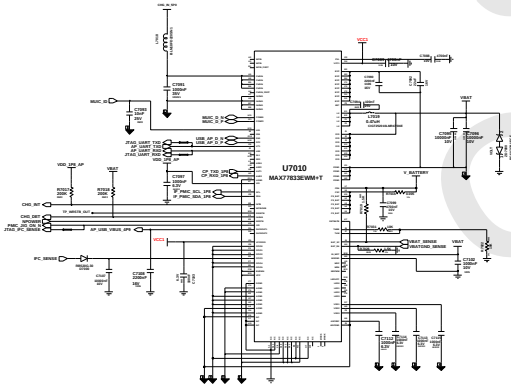

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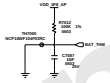

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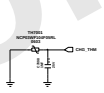
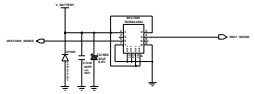


CHG THEMISTOR



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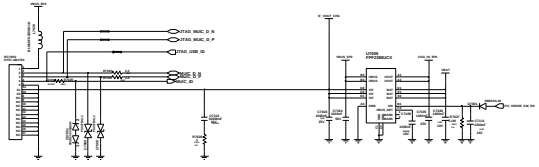

IF Connector
(PCB CUT)

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Project Name	AP/IF/SUB PMIC
Date	
Drawn by	
Checked by	
Approved by	
Date Checked	
Date Approved	
Drawing Number	A8_ISTOR

user87|swcad87|10.252.50.171|20160808-153248

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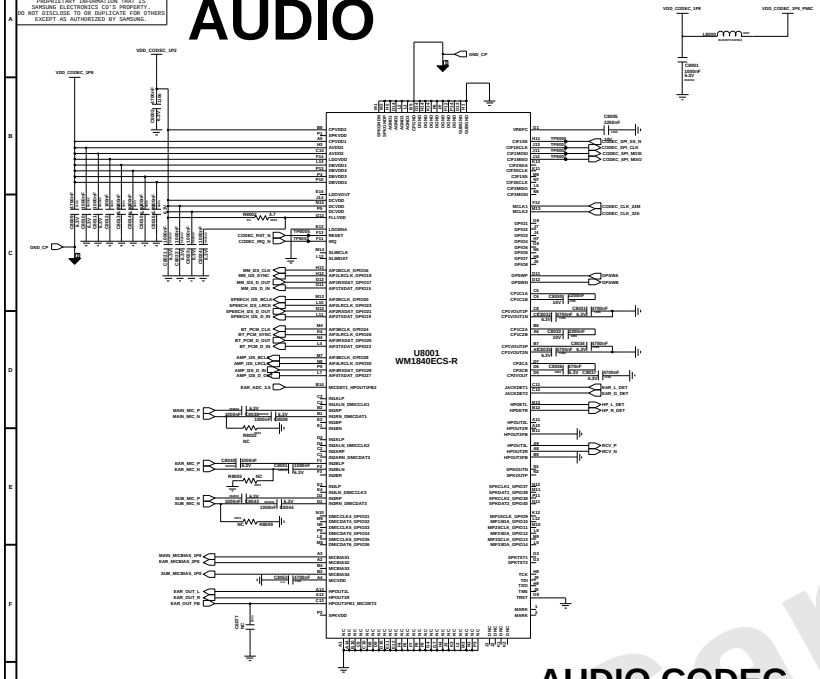
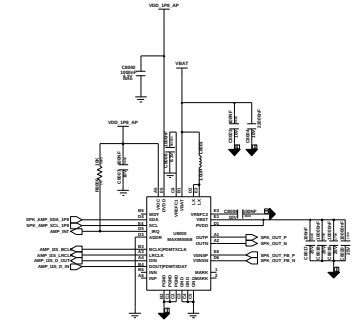
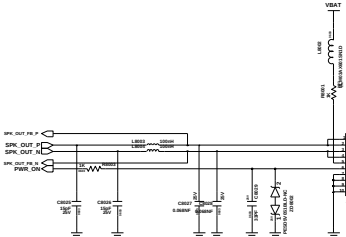
AUDIO



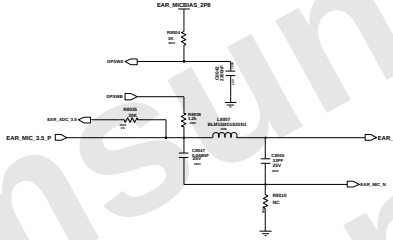
AUDIO CODEC



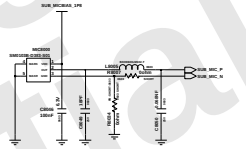
AUDIO AMP



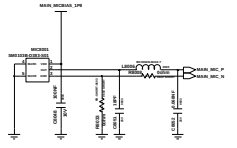
SPK/POWER KEY CON



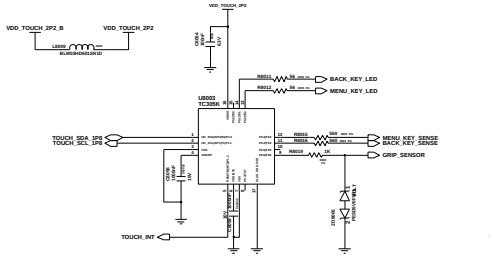
Ear MIC



SUB MIC

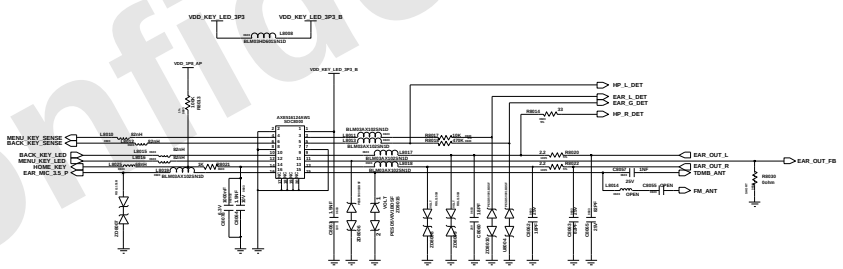


MAIN MIC

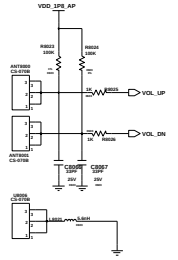


TOUCH KEY & LED

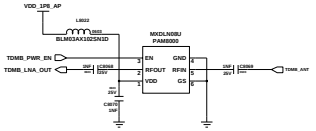
Shielding Can



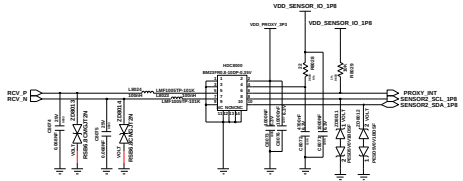
EAR/TOUCH CONNECTOR



VOLUME KEY



TDMB LNA



RCV/PROXY CONNECTOR

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DOC CTRL CHK:		A8_ISTOR	
MFG ENGR CHK:			
QA CHK:		REV:	Drawing Number:

Changed by:	Date Changed:	Time Changed:	

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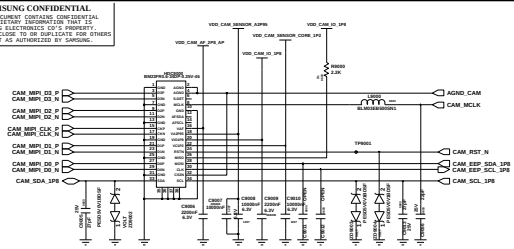
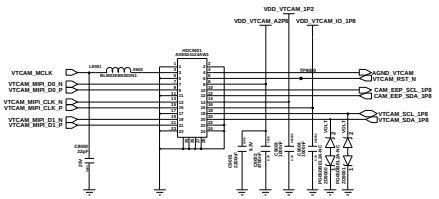
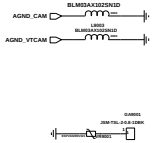
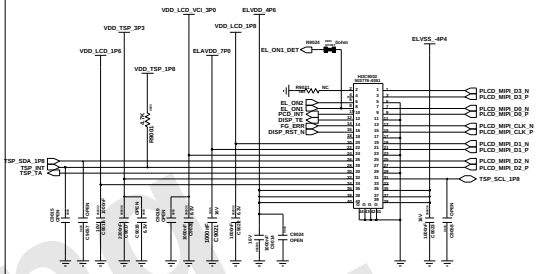


16M_Camera Connector (MIPI)

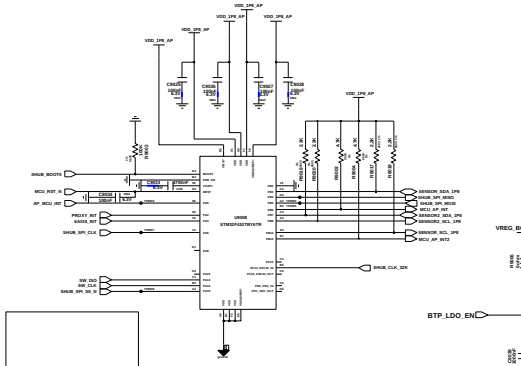


5M Camera CON

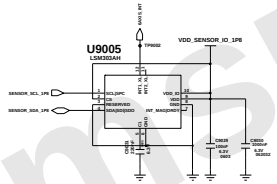




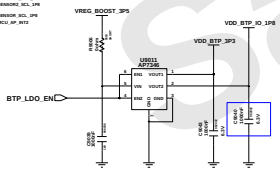
5.7" FHD OCTA CON



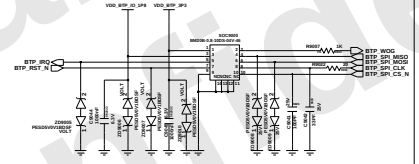
Sensor Hub



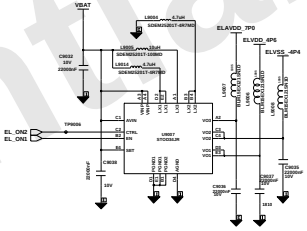
6 Axis Sensor(Magnet & ACC)



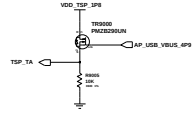
BTP OCP and PWR

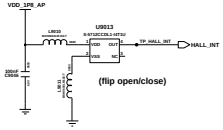


BTP IF Conn

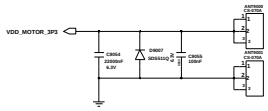


OCTA DCDC CONVERTER

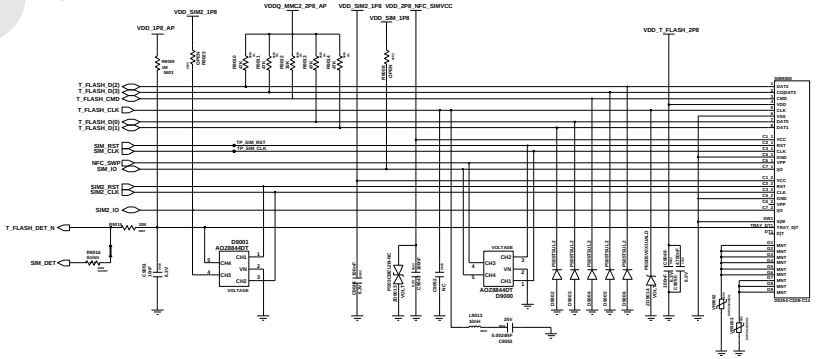




Hall IC



MOTOR
(DC)



3IN2 (SIM1:SIM2/SD) SOCKET

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Sheet No:			
RDD CHK:		TITLE: A8_ISTOR	SIZE: A3
DOC CTRL CHK:			
MFG ENGR CHK:			
QA CHK:		REV:	Drawing Number:

Changed By:	Date Changed:	Time Changed:

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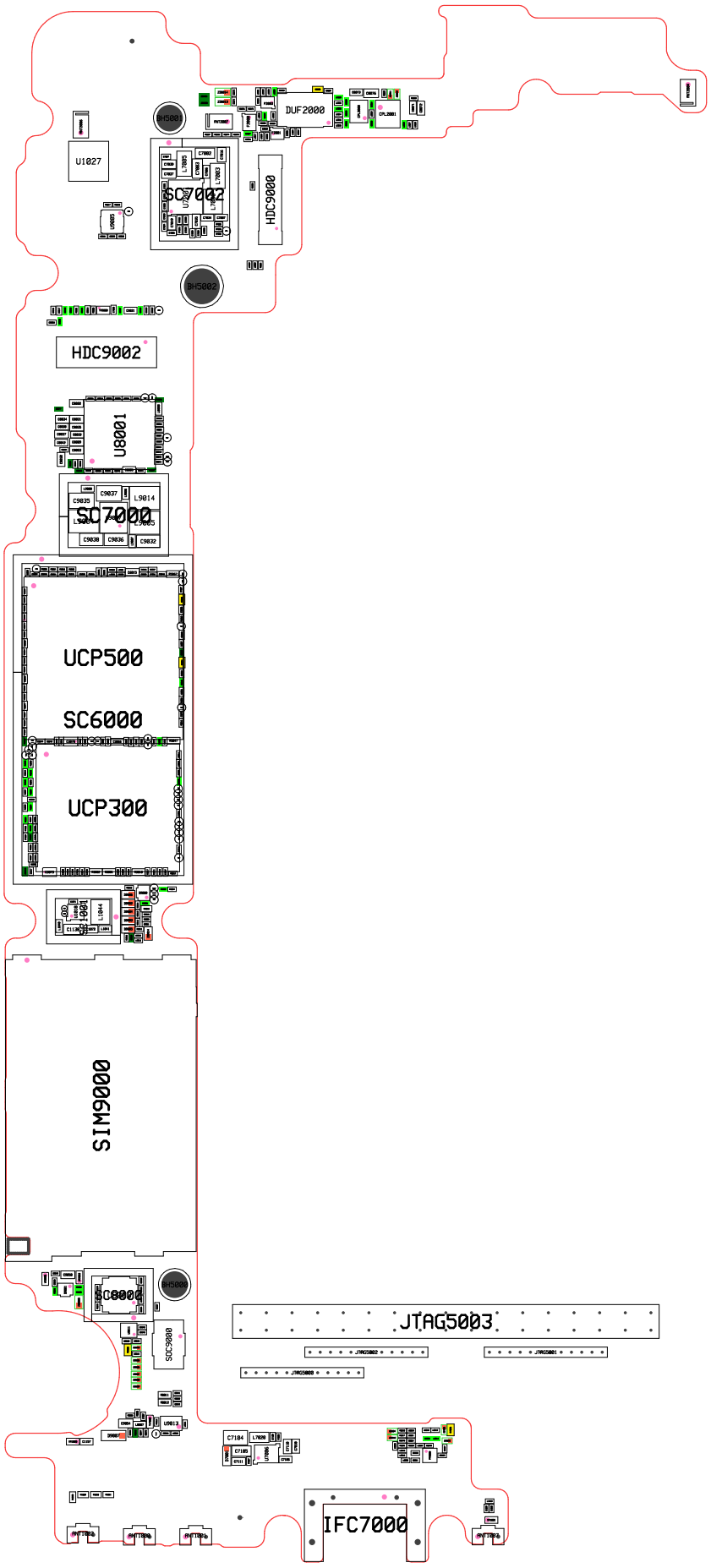
Model : SM-A810S
Revision : REV04
Manufacture Count : 300EA
Charge : 조봉기
Contact : 010-9346-7066
Date : 2016-08-10
Routing : ㅇ
Resin : ㅇ

VX_Setup(2016-01-20)_HHP



AON1605(TR7001) : IT'S Wrong Polarity possibility. Check the Pin1

Model : SM-A810S
Revision : REV04
Manufacture Count : 300EA
Charge : 조봉기
Contact : 010-9346-7066
Date : 2016-08-10
Routing : ㅇ
Resin : ㅇ

VX_Setup(2016-01-20)_HHP

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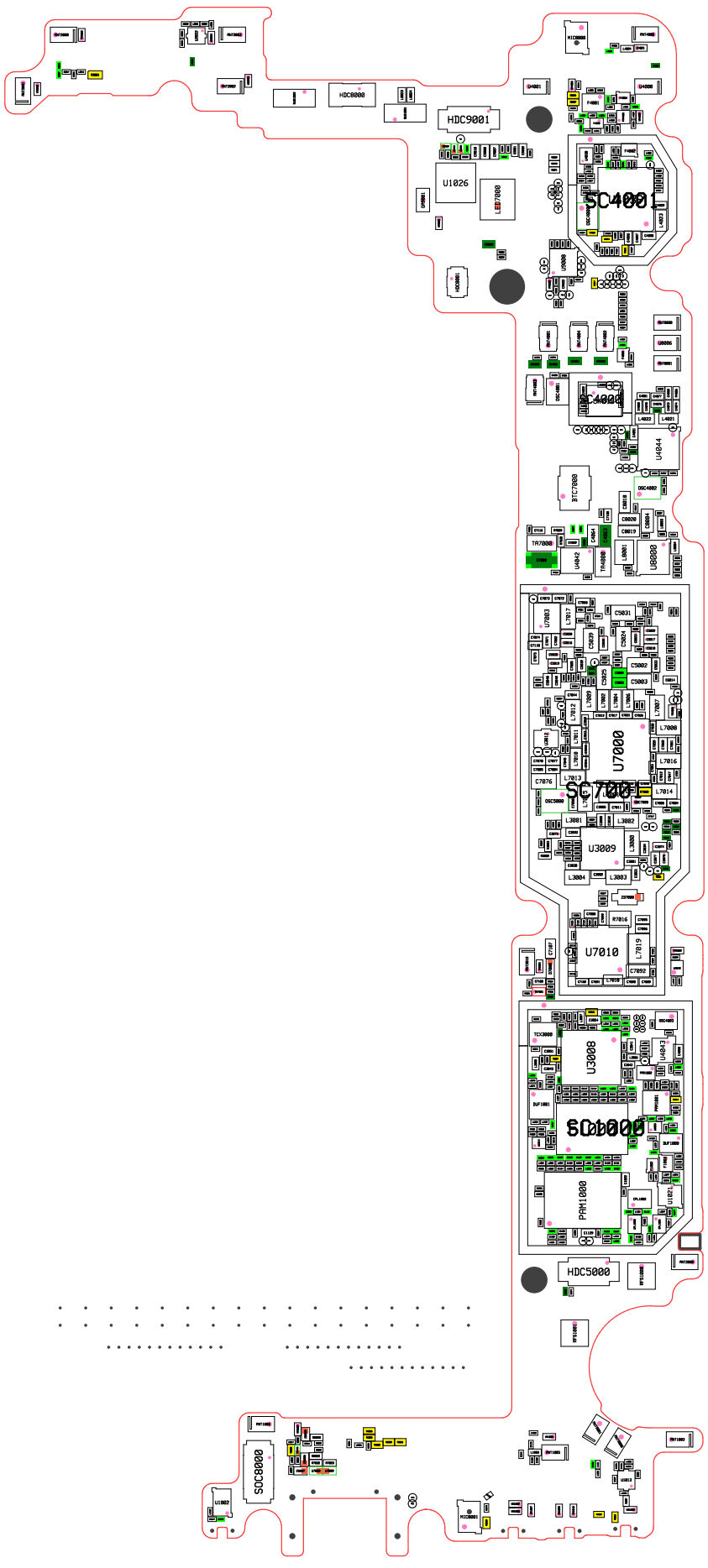

AON1605(TR7001) : IT'S Wrong Polarity possibility. Check the Pin1