

SAMSUNG SM-G965F Schematic Diagram



Schematic *Diagram*



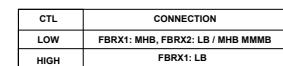
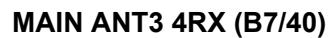
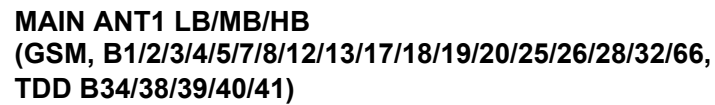
SM-G965F_REV0.9

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- sheet01 : RF1 - MAIN_ANT
- sheet02 : RF2 - SUB ANT
- sheet03 : AP(EXYNOS9810) CP , UFS MEMORY
- sheet04 : CONNECTIVITY , SENSOR
- sheet05 : LHOTSE & ETC
- sheet06 : LHOTSE ,Mobile1Tech.com
- sheet07 : AP PMIC, IF PMIC
- sheet08 : AUDIO CODEC, SUB PCB
- sheet09 : VISUAL PART 1/2
- sheet10 : CON10001
- sheet11 : RF3 - MHB MMMB FOR SCC & AS (EU NC)
- sheet12 : UPDATED

Engineer:	
Drawn By:	
RED CHK:	TITLE: <div>DOC A1</div>
DOC CTRL CHK:	
RF3 ENGR CHK:	
SA CHK:	REV: <div>1</div>

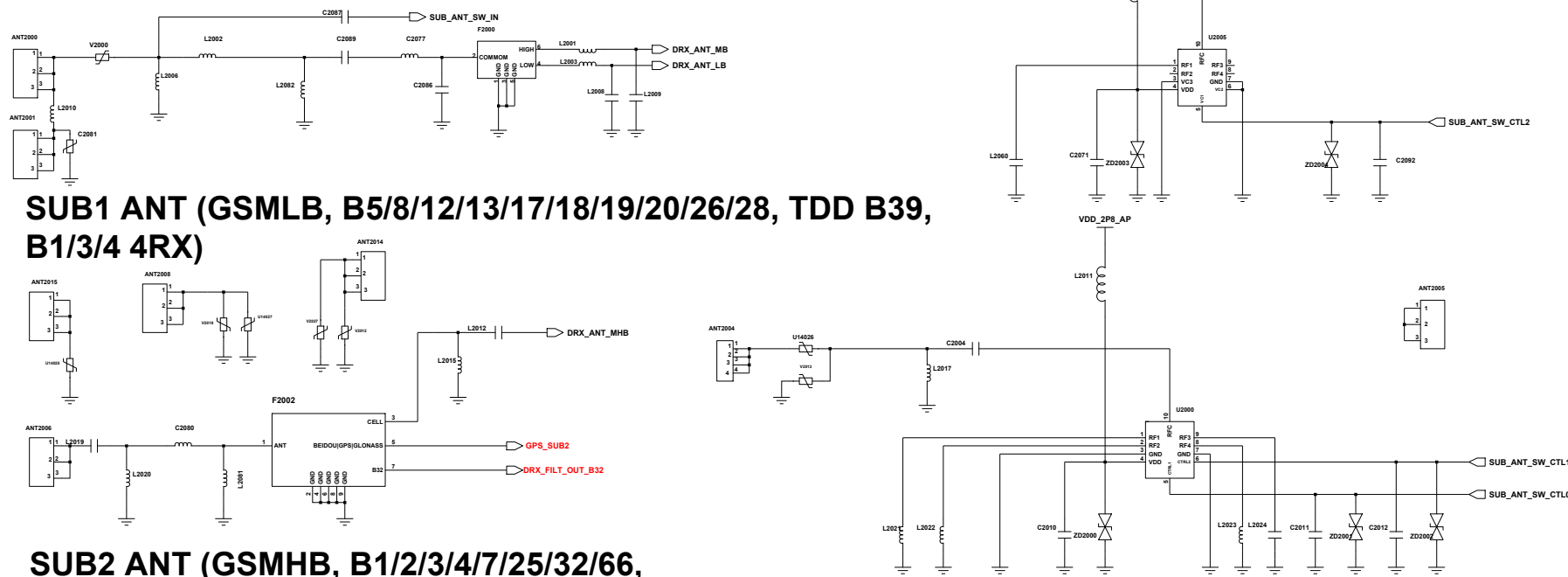
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user112	20171212	112039		1		1



**S735 ET MODULATOR
(KOR : MB/HB PAMID)**

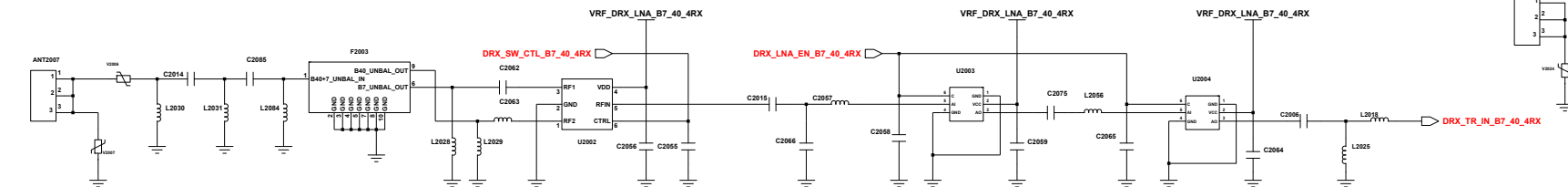
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DOC CTRL CHK:		
MFG ENGR CHK:		Size: 16 1 12 A

RF2 - SUB ANT



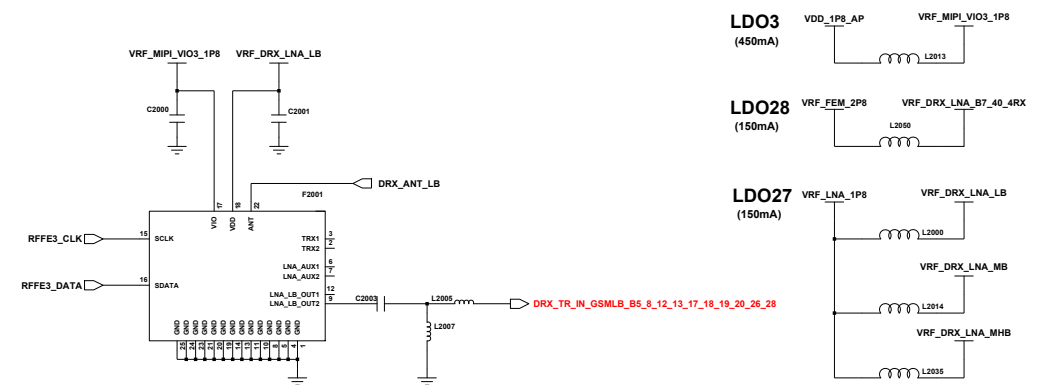
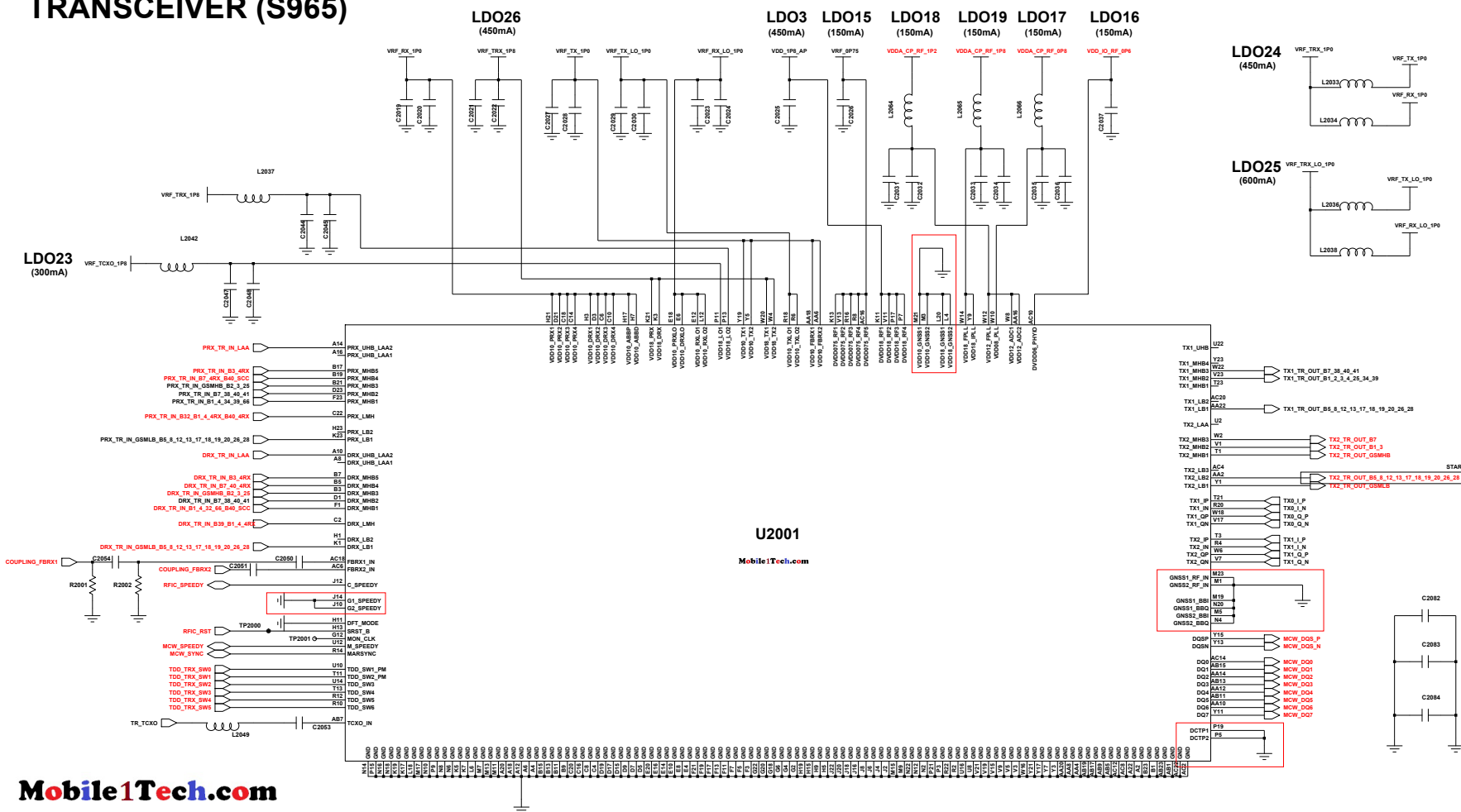
SUB1 ANT (GSMLB, B5/8/12/13/17/18/19/20/26/28, TDD B39, B1/3/4 4RX)

SUB2 ANT (GSMHB, B1/2/3/4/7/25/32/66, TDD B38/40/41)

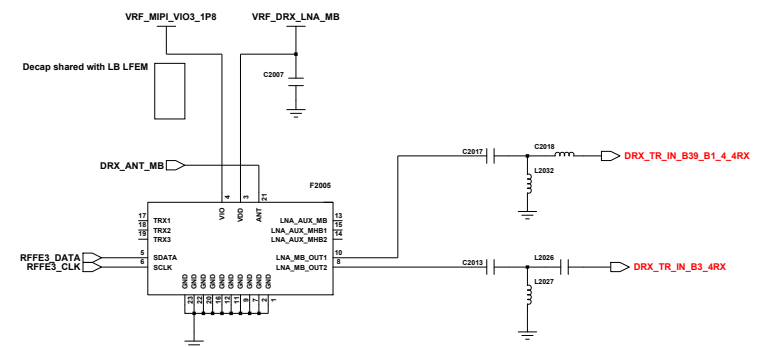


SUB3 ANT (B7/40 4RX)

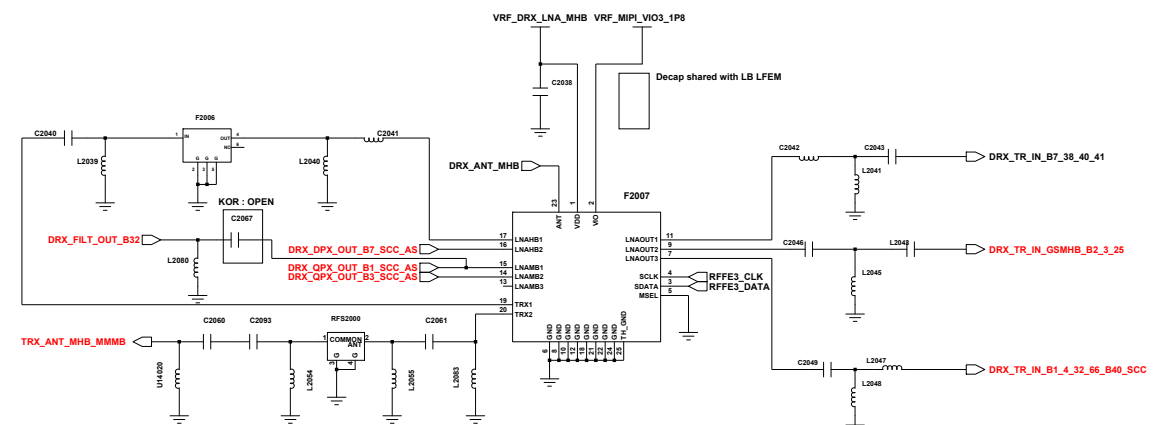
TRANSCIEVER (S965)



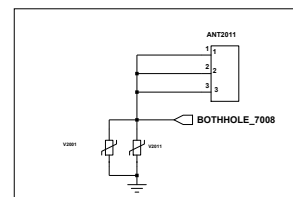
LB LFEM (GSMLB, B5/8/12/13/17/18/19/20/26/28)



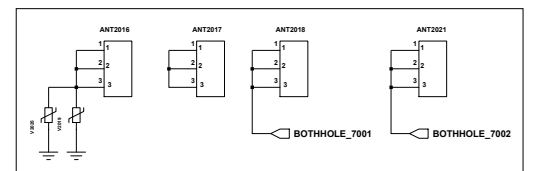
MB LFEM (TDD B39, B1/3/4 4RX)



OMH LFEM (GSMHB, B1/2/3/4/7/25/32/66, TDD B38/40/41)



Connectivity



GND_CONTACT



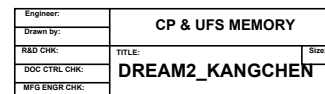
64G 

128G 

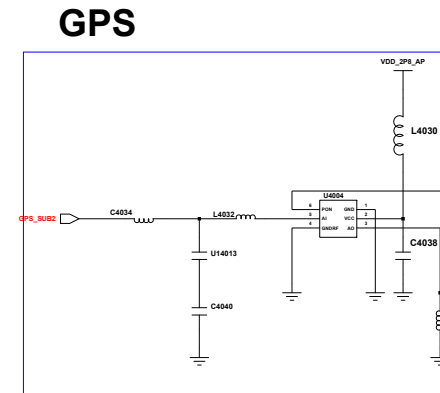
256G 



T-FLASH / SIM / PROXY / IRIS LED / RGB

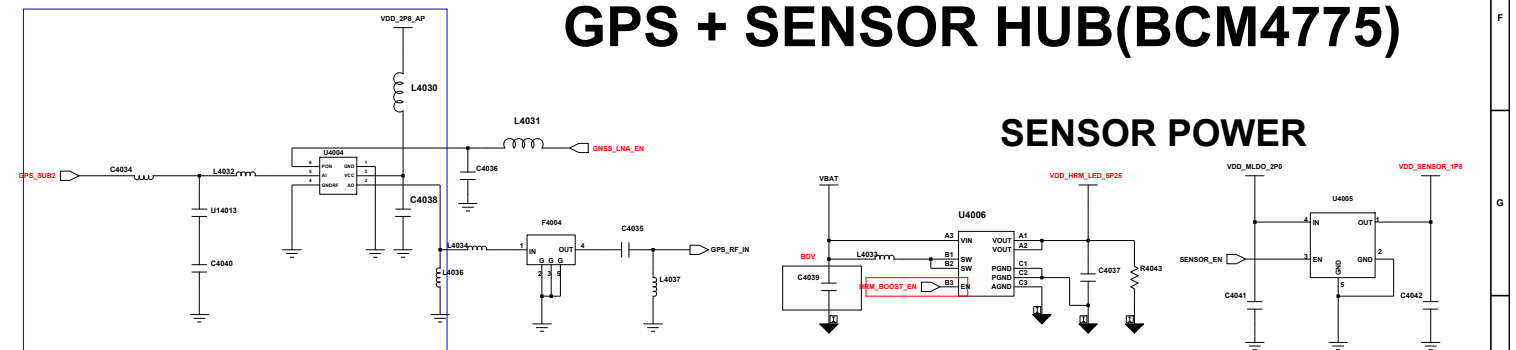


SENSORS

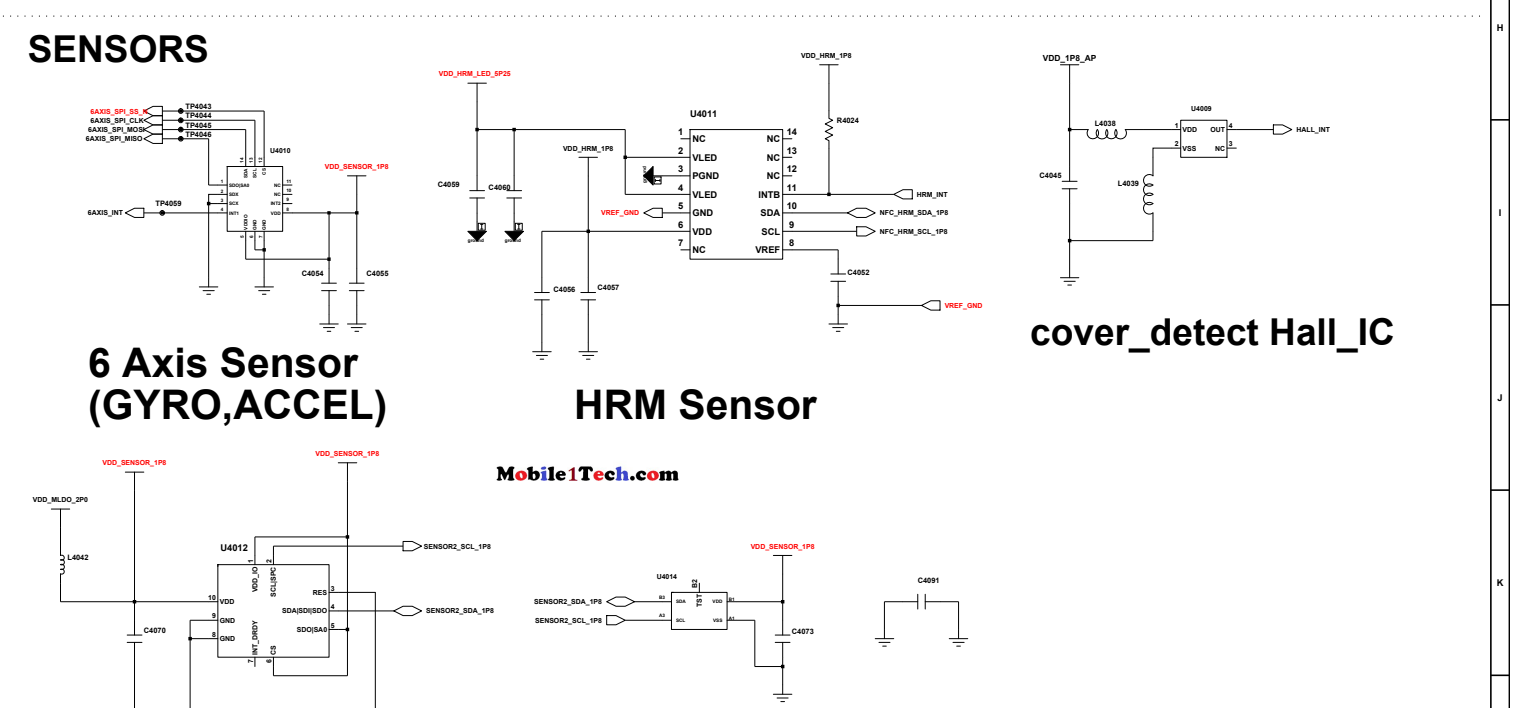


GPS + SENSOR HUB(BCM4775)

SENSOR POWER



NFC



6 Axis Sensor (GYRO,ACCEL)

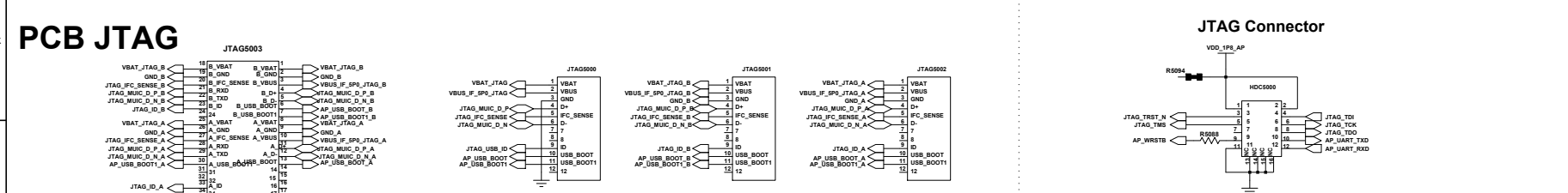
HRM Sensor

Barometer Sensor

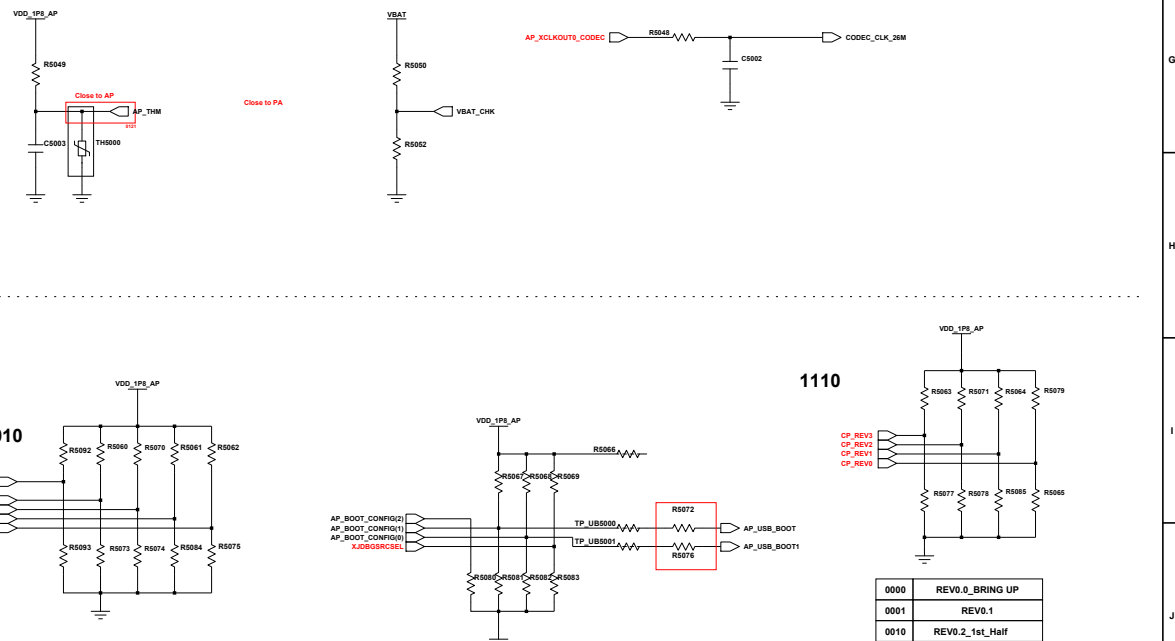
MAGNETIC SENSOR

UCP500-1

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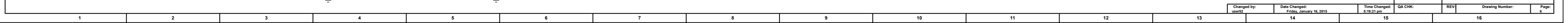
ETC

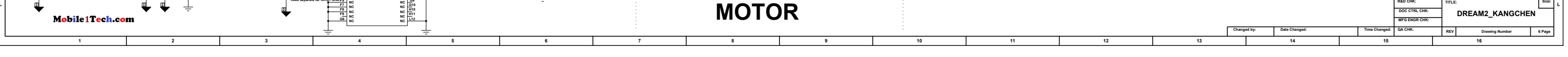
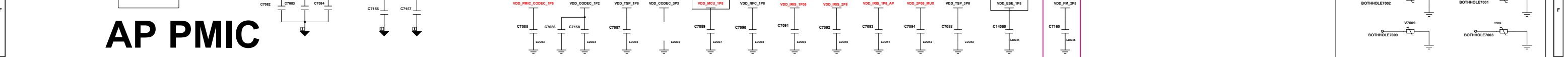


0000	REV0.0_BRING UP
0001	REV0.1
0010	REV0.2_1st_Half
0011	G965F REV0.0
0100	G965F REV0.1
0101	G965F REV0.1+VT1+MD05
0110	G965F REV0.2
0111	G965F REV0.3
1000	G965F REV0.4
1001	G965F REV0.5
1010	G965F REV0.6
1011	G965F REV0.7
1100	G965F REV0.8
1101	Network Lock Algorithm
1110	G965F REV0.9

H/W Revision Setting

CP H/W Revision Setting			
Engineer:	KANGCHEN & ETC		
Drawn by:	TITLE:		Size:
R&D CHK:	DREAM2_KANGCHEN		
DOC CTRLR CHK:			
MFG ENGR CHK:			
QA CHK:	REV	Drawing Number	6 Page



[illegible]

The schematic diagram illustrates the USB AP module's internal circuitry. Key components include:

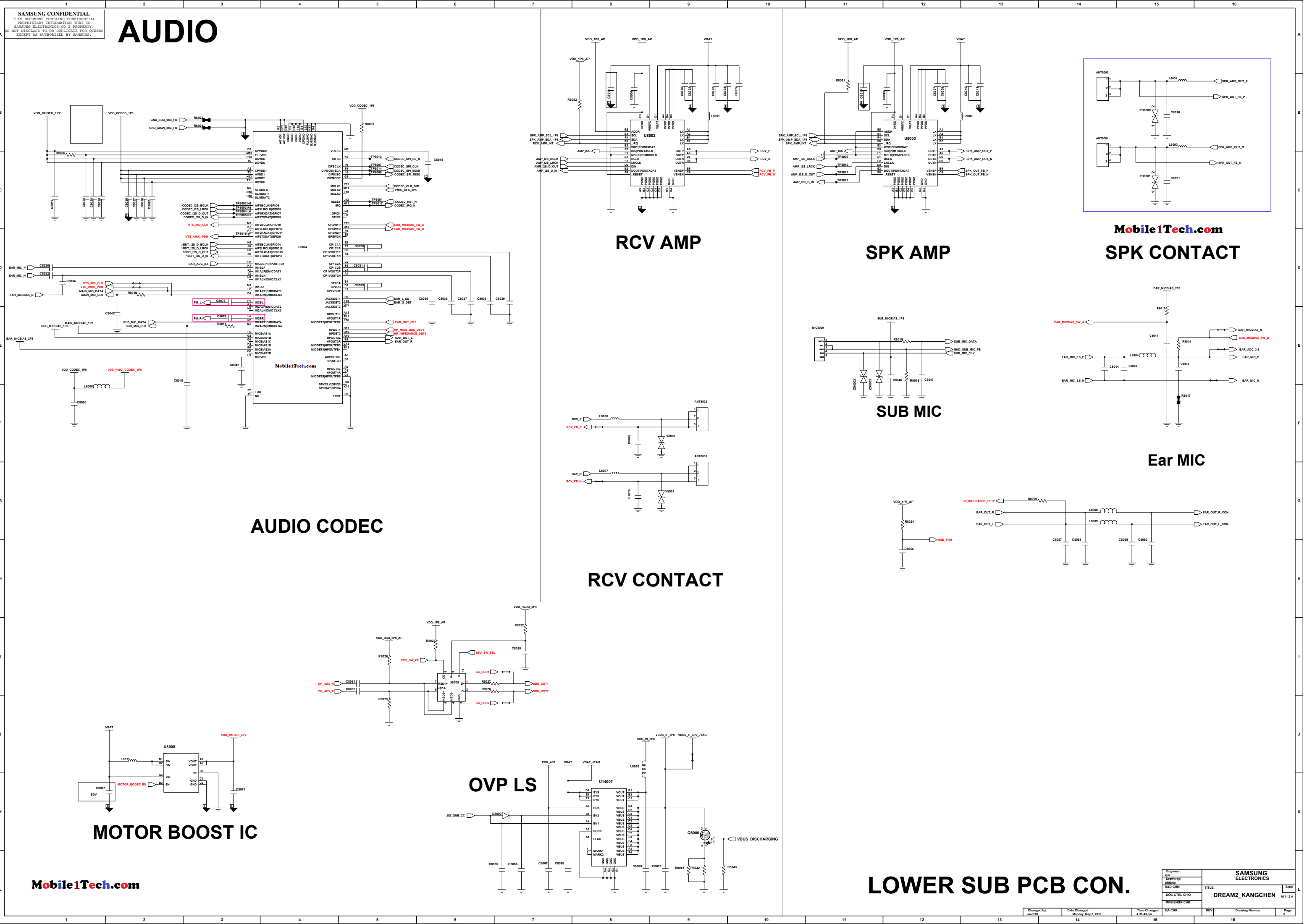
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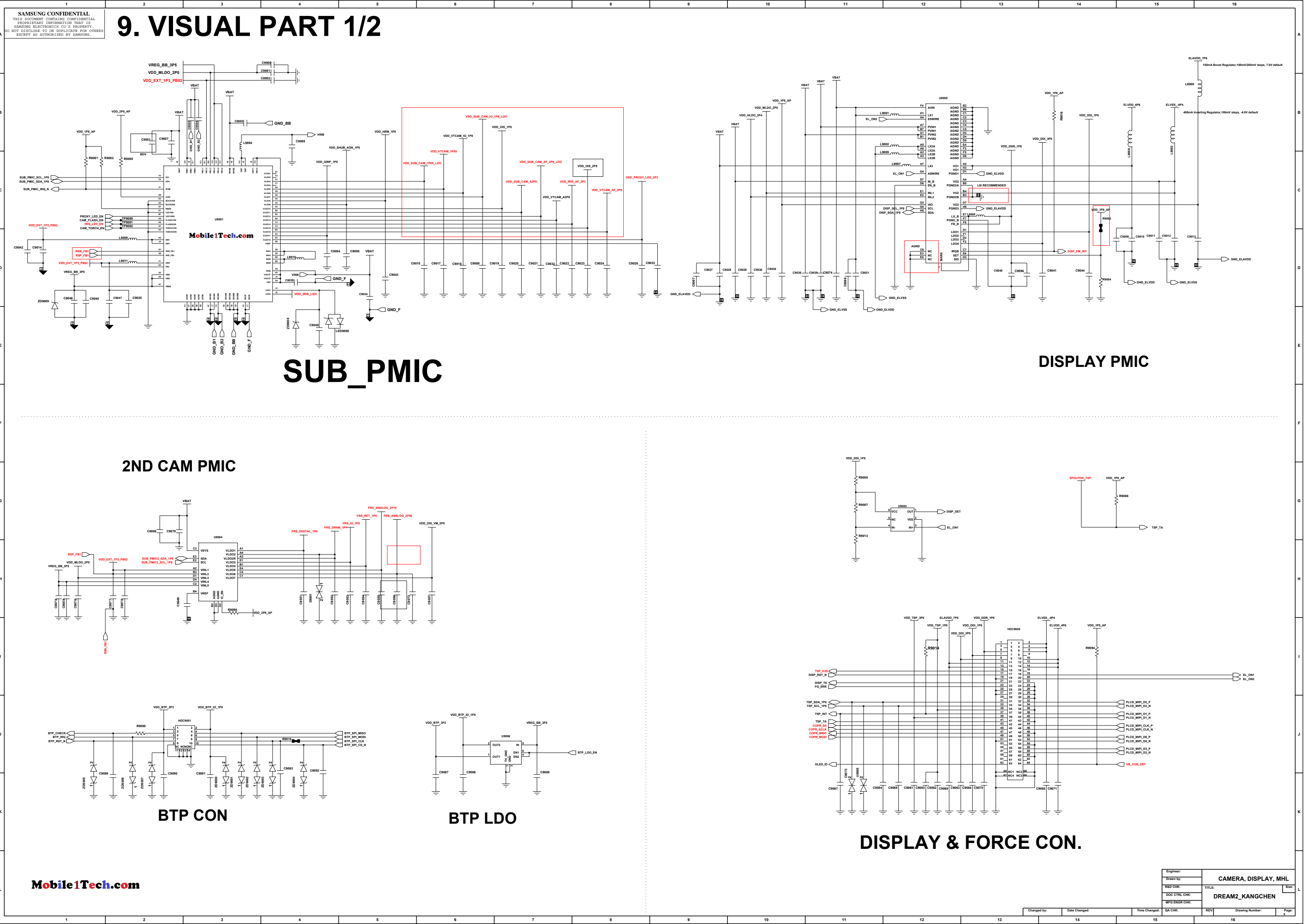
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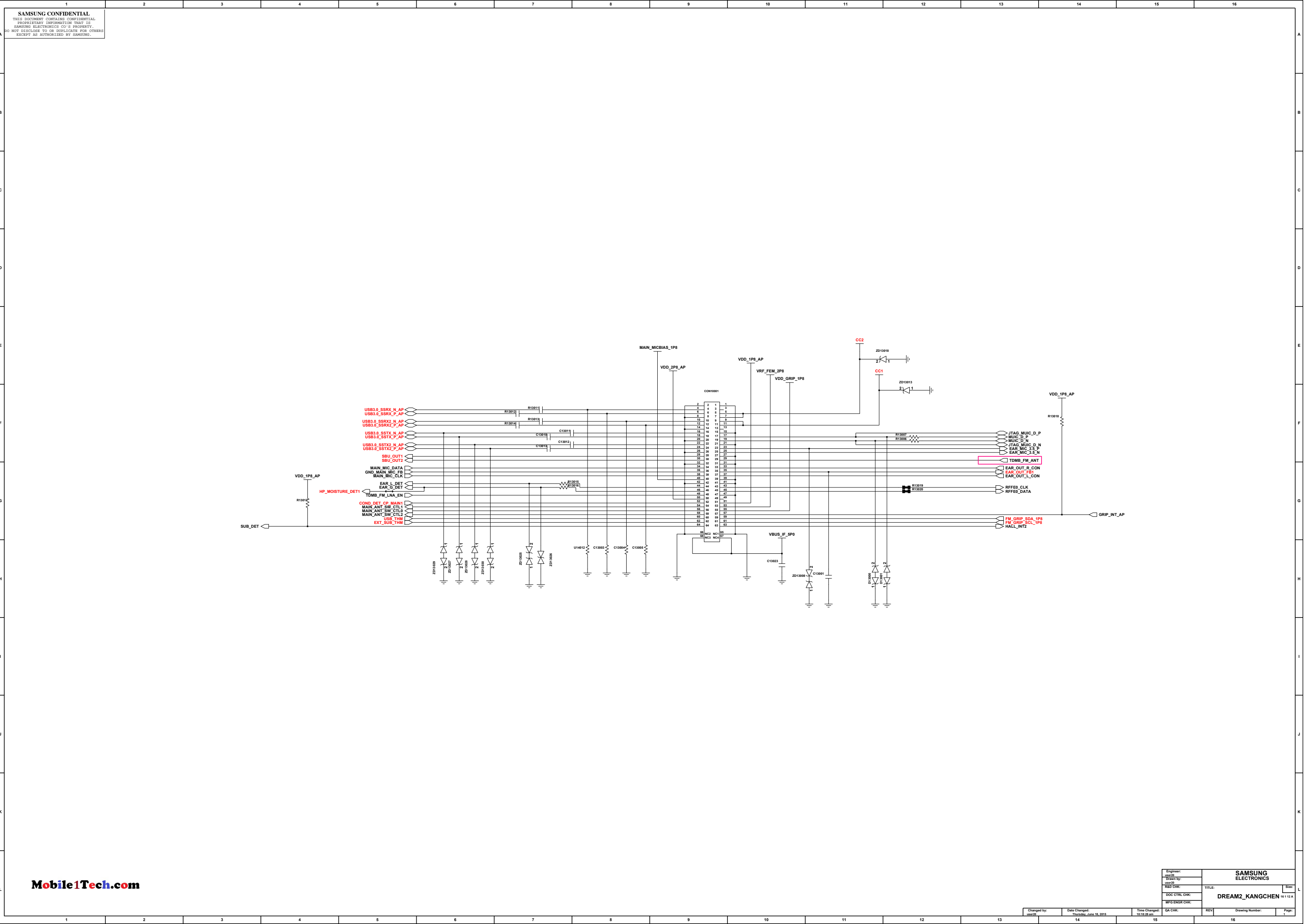
WIRELESS CHARGER IC

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Design by:	TITLE:	Size: L
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DOC CTRL CHK:	DREAM2_KANGCHEN	
MFG ENGR CHK:		

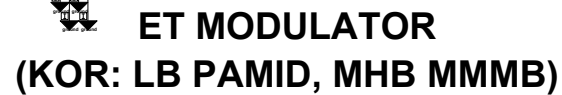
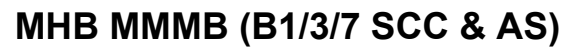
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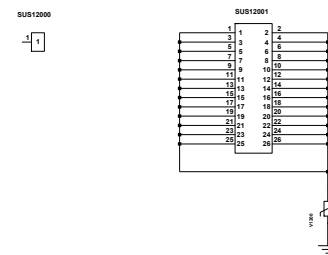


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EUR  R12001



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EUR  R12001

