EE1001 Adder tutorial (Week 10)

KF Tsang

Tutorial (Week 10)

- \blacksquare Compute 6 + 7 using the logic circuits.
- (1) How many adders are needed?
- (2) Which adder should be adopted? Half or full?
- (3) Work out the input and output of each adder?
- (4) Draw the logic circuits.