Totally Integrated	
Automation Portal	

winda / PLC_1 [CPU 1513-1 PN] / Program blocks

winda_graph [FB1]

winda_graph P	roperties						
General							
Name	winda_graph	Number	1	Type	FB	Language	GRAPH
Numbering	Automatic	Network lan-	LAD	Block version	V6.0		
		guage					
Information							
Title		Author		Comment		Family	
Version	0.1	User-defined ID					

vinda_graph lame	Data type	Default value	Retain	Accessible	Writ-	Visible in	Setnoint	Supervi-	Comment
	рата туре	Default Value	Retain	from HMI/OPC UA/Web API	able	HMI engi- neering	Setpoint	sion	Comment
▼ Input									
OFF_SQ	Bool	false	Non-retain	False	False		False		Turn sequence off
INIT_SQ	Bool	false	Non-retain	False	False		False		Set sequence to initial sta
ACK_EF S_PREV	Bool	false false	Non-retain Non-retain	False False	False False		False False		Acknowledge all errors and faults Output previous step in p
S_NEXT	Bool	false	Non-retain	False	False		False		rameter S_NO Indicate next step in para
									eter S_NO
SW_AUTO	Bool	false	Non-retain	False	False		False		Automatic mode
SW_TAP	Bool	false	Non-retain	False	False		False		Semiautomatic/switch witransition
SW_TOP	Bool	false	Non-retain	False	False	False	False		Semiautomatic/ignore tra sition
SW_MAN	Bool	false	Non-retain	False	False	False	False		Manual mode
S_SEL	Int	0	Non-retain	False	False	False	False		Select step to be output t S_NO
S_ON	Bool	false	Non-retain	False	False		False		Activate step indicated in S_NO
S_OFF	Bool	false	Non-retain	False		False	False		Deactivate step indicated S_NO
T_PUSH	Bool	false	Non-retain	False		False	False		Enable transition to switch in semi automatic mode
aktualne_pietro	Int	0	Non-retain	True	True		False		
wybrane_pietro	Int	0	Non-retain	True	True	True	False		
✓ Output		-			<u></u>				
S_NO	Int	0	Non-retain	False	False		False		Step number
S_MORE	Bool	false	Non-retain	False	False		False		More steps are available can be shown in S_NO
S_ACTIVE ERR_FLT	Bool	false false	Non-retain Non-retain	False False	False False		False False		Step indicated in S_NO is tive Interlock or supervision
ERN_FLI	Bool	laise	Non-retain	laise	i aise	i dise	laise		group error
AUTO_ON	Bool	false	Non-retain	False	False		False		Automatic mode is active
TAP_ON	Bool	false	Non-retain	False	False		False		Semiautomatic mode/ste with transition enabled
TOP_ON	Bool	false	Non-retain	False	False		False		Semiautomatic mode/igr transition enabled
MAN_ON	Bool	false	Non-retain	False	False	False	False		Manual mode is active
▼ InOut									
jedz_dol	Bool	false	Non-retain	True	True		False		
jedz_gora	Bool	false	Non-retain	True	True	True	False		
▼ Static									
▼ RT_DATA	G7_RTData- Plus_V6		Non-retain	False	False		True		Internal data area
VERSION	String[10]	'V6.0'	Non-retain	False	False		False		Block version
S_DISPLAY	Int	0	Non-retain	False	False		False		Internal display of outpu parameter S_NO
S_SEL_OLD S_DISPIDX	Int USInt	0 255	Non-retain Non-retain	False False	False False		False False		Previous value in S_SEL Index of the step in S_NO
S_DISPIDX T_DISPIDX	USInt	255	Non-retain	False	False		False		Index of the step in S_NC
■ MOP_EDGE	G7_MOP-	233	Non-retain	False			True		played in T_NO Mode in last cycle
	Plus_V6								_
AUTO	Bool	false	Non-retain	False	False		False		Status: automatic mode
MAN	Bool	false	Non-retain	False	False		False		Status: manual mode
TAP	Bool	false	Non-retain	False	False		False		Status: semi automatic/switch with transition
ТОР	Bool	false	Non-retain	False	False	raise	False		Status: semi automatic/ignore transition
ACK_S	Bool	false	Non-retain	False	False		False		Request: acknowledge st at parameter S_NO
REG_S	Bool	false	Non-retain	False	False	Ealco	False		Request: register step in

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ıme		Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	Writ- Visible i able HMI eng from neering HMI/ OPC UA/ Web API		Supervi- sion	Comment
7	Γ_PREV	Bool	false	Non-retain	False	False False	False		Request: output previous
7	Γ_NEXT	Bool	false	Non-retain	False	False False	False		valid transition in T_NO Request: output next valid transition in T_NO
	-OCK	Bool	false	Non-retain	False	False False	False		Status: interlocks activated
S	SUP	Bool	false	Non-retain	False	False False	False		Status: supervisions activa- ted
F	ACKREQ	Bool	false	Non-retain	False	False False	False		Status: acknowledgment required
	SSKIP	Bool	false	Non-retain	False	False False	False		Status: "Skip steps" enabled
	OFF	Bool	false	Non-retain	False	False False	False		Request: deactivate all step
	NIT	Bool	false	Non-retain	False	False False	False		Request: set sequence to in tial state
	HALT FMS_HALT	Bool Bool	false false	Non-retain Non-retain	False False	False False False False	False False		Status: sequence halted Status: all internal timers
									held
(OPS_ZERO	Bool	false	Non-retain	False	False False	False		Status: set all operands pro cessed with N, L, D instruc- tions to 0
9	SACT_DISP	Bool	false	Non-retain	False	False False	False		Status: display active steps
9	SEF_DISP	Bool	false	Non-retain	False	False False	False		only Status: display only steps with errors and disrupted
	SALL_DISP	Bool	false	Non-retain	False	False False	False		steps Status: display all steps
	S_PREV	Bool	false	Non-retain	False	False False	False		Request: output previous step to S_NO
9	S_NEXT	Bool	false	Non-retain	False	False False	False		Request: Output next step s S_NO parameter
9	S_SELOK	Bool	false	Non-retain	False	False False	False		Request: output step num- ber from S_SEL to S_NO
S	S_ON	Bool	false	Non-retain	False	False False	False		Request: activate step indi- cated in S_NO
9	S_OFF	Bool	false	Non-retain	False	False False	False		Request: deactivate step at parameter S_NO
٦	Γ_PUSH	Bool	false	Non-retain	False	False False	False		Request: transition switching enabled
F	REG	Bool	false	Non-retain	False	False False	False		Request: register all inter- lock and supervision errors
F	ACK	Bool	false	Non-retain	False	False False	False		Request: acknowledge all ir terlock and supervision er-
l	L_PERM	Bool	false	Non-retain	False	False False	False		rors Status: permanent processing of all interlocks
1	T_PERM	Bool	false	Non-retain	False	False False	False		Status: permanent process ing of all transitions
I	LP_MAN	Bool	false	Non-retain	False	False False	False		Status: permanent process ing of all interlocks in manual mode
L	MODE	Bool	false	Non-retain	False	False False	False		Status: learning mode is acitve
F	RESET_CRIT	Bool	false	Non-retain	False	False False	False		Request: reset all initial val ues recorded for interlocks and transitions
▼ MO	P	G7_MOP- Plus_V6		Non-retain	False	False False	True		Mode
F	AUTO	Bool	true	Non-retain	False	False False	False		Status: automatic mode
N	MAN	Bool	false	Non-retain	False	False False	False		Status: manual mode
ד	ГАР	Bool	false	Non-retain	False	False False	False		Status: semi automatic/switch with transition
7	ГОР	Bool	false	Non-retain	False	False False	False		Status: semi automatic/ig- nore transition
P	ACK_S	Bool	false	Non-retain	False	False False	False		Request: acknowledge step at parameter S_NO
F	REG_S	Bool	false	Non-retain	False	False False	False		Request: register step indicated in S_NO
7	T_PREV	Bool	false	Non-retain	False	False False	False		Request: output previous valid transition in T_NO
7	Γ_NEXT	Bool	false	Non-retain	False	False False	False		Request: output next valid transition in T_NO
L	-OCK	Bool	true	Non-retain	False	False False	False		Status: interlocks activated
S	SUP	Bool	true	Non-retain	False	False False	False		Status: supervisions activa- ted
F	ACKREQ	Bool	true	Non-retain	False	False False	False		Status: acknowledgment required
	SSKIP	Bool	false	Non-retain	False	False False	False		Status: "Skip steps" enabled
	OFF	Bool Bool	false	Non-retain Non-retain	False False	False False False False	False False		Request: deactivate all step Request: set sequence to in
	NIT HALT	Bool	false	Non-retain Non-retain	False	False False	False		tial state Status: sequence halted
r	TMS_HALT	Bool	false	Non-retain	False	False False	False		Status: all internal timers

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ne	Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	able	Visible in HMI engi- neering	Setpoint	Supervi- sion	Comment
OPS_ZERO	Bool	false	Non-retain	False	False	False	False		Status: set all operands p cessed with N, L, D instrutions to 0
SACT_DISP	Bool	true	Non-retain	False	False	False	False		Status: display active step only
SEF_DISP	Bool	false	Non-retain	False	False	False	False		Status: display only steps with errors and disrupted steps
SALL_DISP	Bool	false	Non-retain	False		False	False		Status: display all steps
S_PREV	Bool	false	Non-retain	False	False	False	False		Request: output previous step to S_NO
S_NEXT	Bool	false	Non-retain	False	False	False	False		Request: Output next ste S_NO parameter
S_SELOK	Bool	false	Non-retain	False	False	False	False		Request: output step number from S_SEL to S_NO
S_ON	Bool	false	Non-retain	False	False	False	False		Request: activate step inc
S_OFF	Bool	false	Non-retain	False	False	False	False		Request: deactivate step a
T_PUSH	Bool	false	Non-retain	False	False	False	False		Request: transition switch
REG	Bool	false	Non-retain	False	False	False	False		Request: register all inter- lock and supervision error
ACK	Bool	false	Non-retain	False	False	False	False		Request: acknowledge all terlock and supervision errors
IL_PERM	Bool	false	Non-retain	False	False	False	False		Status: permanent proces
T_PERM	Bool	false	Non-retain	False	False	False	False		Status: permanent proces
ILP_MAN	Bool	false	Non-retain	False	False	False	False		Status: permanent procesing of all interlocks in ma
LMODE	Bool	false	Non-retain	False	False	False	False		Status: learning mode is acitve
RESET_CRIT	Bool	false	Non-retain	False	False	False	False		Request: reset all initial volues recorded for interlock and transitions
TIME_DELTA	Time	T#0ms	Non-retain	False	False	False	False		Cycle time
▼ SQ_FLAGS	G7_SQFlags- Plus_V6		Non-retain	False	False	False	True		Sequence bit memory
ERR_FLT	Bool	false	Non-retain	False	False	False	False		Interlock and supervision group error
ERROR	Bool	false	Non-retain	False		False	False		Interlock group error
FAULT	Bool	false	Non-retain	False		False	False		Supervision group error
RT_FAIL NO_SNO	Bool	false false	Non-retain Non-retain	False False		False False	False False		Runtime error Requested step number r found
NF_OFL	Bool	false	Non-retain	False	False	False	False		Overflow: too many ON o OFF requests
SA_OFL	Bool	false	Non-retain	False	False	False	False		Overflow: too many steps active
TV_OFL	Bool	false	Non-retain	False	False	False	False		Overflow: too many valid transitions
NO_SWI	Bool	false	Non-retain	False	False	False	False		Do not switch in this cycle
CYC_OP	Bool	false	Non-retain	False	False	False	False		Cyclic execution of the se
AS_MSG	Bool	true	Non-retain	False	False	False	False		quence after initialization Alarms during runtime er bled or disabled by instru
SQ_BUSY	Bool	false	Non-retain	False	False	False	False		tion Internal edge memory bi
SA_BUSY	Bool	false	Non-retain	False	False	False	False		for sequence processing Internal edge memory bit
PRE_CNT	USInt	1	Non-retain	False	False	False	False		for sequence processing Number of permanent in structions preceding the
POST_CNT	USInt	1	Non-retain	False	False	False	False		quencer Number of permanent in structions after the seque
SQ_CNT	USInt	1	Non-retain	False		False	False		er Number of branch paths
S_CNT	USInt	5	Non-retain	False		False	False		Number of steps
LOCK_CNT	USInt	0	Non-retain	False		False	False		Number of interlocks
SUP_CNT	USInt	0	Non-retain	False		False	False		Number of supervisions
T_CNT SQ_PART_CNT	USInt USInt	6	Non-retain Non-retain	False False		False False	False False		Number of transitions Number of branches
SQ_PARI_CNI MAX_TVAL	USInt	3	Non-retain Non-retain	False		False	False		Max. number of simultan
.vi/ (/\ V/ \L			14011 Tetalli	i uise	, 4136	. 4130	. 4150		ously valid transitions
MAX_SACT	USInt	1	Non-retain	False	Ealco	False	False		Max. number of simultan

Non-retain

Non-retain

Non-retain

MAX_SACT

AS_MSG

▼ EXEC_BITS

USInt

Byte

Array[0..249] of Bool

16#65

Max. number of simultaneously active steps
Alarm flags

System-internal

False False

False False

False False

False

False

False

False

False

False

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	Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	Writ- Visible in able HMI engi from neering HMI/ OPC UA/ Web API		Supervi- sion	Comment				
EXEC_BITS[0]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[1]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[2]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[3]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[4]	Bool	false false	Non-retain	False	False False	False False		System-internal				
EXEC_BITS[5] EXEC_BITS[6]	Bool Bool	false	Non-retain Non-retain	False False	False False	False		System-internal System-internal				
EXEC_BITS[7]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[8]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[9]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[10]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[11]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[12]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[13]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[14]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[15]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[16]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[17]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[18]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[19]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[20]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[21]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[22]	Bool	false false	Non-retain Non-retain	False	False False False False	False		System-internal				
EXEC_BITS[23]	Bool	false	Non-retain	False False	False False	False False		System-internal System-internal				
EXEC_BITS[24] EXEC_BITS[25]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[26]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[27]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[28]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[29]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[30]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[31]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[32]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[33]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[34]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[35]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[36]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[37]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[38]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[39]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[40]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[41]	Bool Bool	false false	Non-retain Non-retain	False False	False False False False	False False		System-internal System-internal				
EXEC_BITS[42]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[43] EXEC_BITS[44]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[44]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[46]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[47]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[48]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[49]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[50]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[51]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[52]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[53]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[54]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[55]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[56]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[57]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[58]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[59]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[60]	Bool	false	Non-retain	False	False False False False	False		System-internal				
EXEC_BITS[61]	Bool	false false	Non-retain Non-retain	False False	False False	False False		System-internal System-internal				
EXEC_BITS[62] EXEC_BITS[63]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[64]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[65]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[66]	Bool	false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[67]	Bool	false	Non-retain	False	False False	False		System-internal				
		false	Non-retain	False	False False	False		System-internal				
EXEC_BITS[68]	Bool	Tuise	i ton retain					2,310				

Non-retain

Non-retain

Non-retain

False

False

False

False False

False False

False False

False

False

False

System-internal

System-internal

System-internal

EXEC_BITS[70]

EXEC_BITS[71]

EXEC_BITS[72]

Bool

Bool

Bool

false

false

false

ne		Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	able	Visible in HMI engi- neering	Setpoint	Supervi- sion	Comment
EXEC I	BITS[73]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[74]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[75]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[76]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[77]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[78]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[79]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[80]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[81]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[82]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[83]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[84]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[85]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[86]	Bool	false	Non-retain	False	False		False		System-internal
		Bool	false	Non-retain	False	False		False		System-internal
	BITS[87]	Bool	false	Non-retain	False	False		False		•
	BITS[88]					False		False		System-internal
	BITS[89]	Bool	false	Non-retain	False					System-internal
	BITS[90]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[91]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[92]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[93]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[94]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[95]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[96]	Bool	false	Non-retain	False	False		False		System-internal
EXEC_	BITS[97]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[98]	Bool	false	Non-retain	False	False		False		System-internal
EXEC_	BITS[99]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_I	BITS[100]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_I	BITS[101]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_	BITS[102]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_I	BITS[103]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_I	BITS[104]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_I	BITS[105]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_F	BITS[106]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[107]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[108]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[109]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[110]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[111]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[112]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[113]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[114]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[114] BITS[115]	Bool	false	Non-retain	False		False	False		System-internal
		Bool	false	Non-retain	False	False		False		System-internal
	BITS[116]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[117]		false							-
	BITS[118]	Bool		Non-retain	False	False		False		System-internal
	BITS[119]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[120]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[121]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[122]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[123]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[124]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[125]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[126]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[127]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[128]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[129]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[130]	Bool	false	Non-retain	False	False		False		System-internal
EXEC_I	BITS[131]	Bool	false	Non-retain	False		False	False		System-internal
EXEC_I	BITS[132]	Bool	false	Non-retain	False	False	False	False		System-internal
EXEC_I	BITS[133]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[134]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[135]	Bool	false	Non-retain	False	False	False	False		System-internal
	BITS[136]	Bool	false	Non-retain	False	False		False		System-internal
	BITS[137]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[137]	Bool	false	Non-retain	False			False		System-internal
	BITS[130] BITS[139]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[140]	Bool	false	Non-retain	False	False		False		System-internal
		Bool	false		False		False	False		•
	BITS[141]			Non-retain						System-internal
	BITS[142]	Bool	false	Non-retain	False		False	False		System-internal
	BITS[143]	Bool	false	Non-retain	False			False		System-internal
	BITS[144]	Bool	false	Non-retain	False	False		False		System-internal
EXEC_I	BITS[145]	Bool	false	Non-retain	False	False	False	False		System-internal

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		Data type	Default value	Retain	from HMI/OPC UA/Web API	Writ- Visible in able HMI engi- from neering HMI/ OPC UA/ Web API	Setpoint	Supervi- sion	Comment
EXEC_I	BITS[146]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[147]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[148]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[149]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[150]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[151]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[152]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[153]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[154]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[155]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[156]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[157]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[158]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[159]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[160]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[160] BITS[161]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[161] BITS[162]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[162]	Bool	false	Non-retain	False	False False	False		System-internal
		Bool	false	Non-retain Non-retain	False	False False	False		System-internal
	BITS[164]								-
	BITS[165]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[166]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[167]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[168]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[169]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[170]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[171]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[172]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[173]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[174]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[175]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[176]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[177]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[178]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[179]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[180]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[181]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_I	BITS[182]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_I	BITS[183]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[184]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[185]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[186]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[187]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[188]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[189]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[190]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[190]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[191] BITS[192]	Bool	false	Non-retain	False	False False	False		System-internal
			false	Non-retain	False	False False	False		System-internal
	BITS[193]	Bool							<u> </u>
	BITS[194]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[195]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[196]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[197]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[198]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[199]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[200]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[201]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[202]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[203]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[204]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[205]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[206]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[207]	Bool	false	Non-retain	False	False False	False		System-internal
EXEC_	BITS[208]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[209]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[210]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[211]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[212]	Bool	false	Non-retain	False	False False	False		System-internal
	BITS[212]	Bool	false	Non-retain	False		False		System-internal
	BITS[214]	Bool	false	Non-retain	False		False		System-internal
		Bool	false	Non-retain	False		False		System-internal
	BITS[215]	Bool	false	Non-retain Non-retain		False False	False		System-internal
	BITS[216]				False				
	BITS[217]	Bool	false	Non-retain	False		False		System-internal
FXFC_	BITS[218]	Bool	false	Non-retain	False	False False	False		System-internal

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me	Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API		MI engi-	Setpoint	Supervi- sion	Comment
EXEC_BITS[219]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[220]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[221]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[222]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[223]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[224]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[225]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[226]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[227]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[228]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[229]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[230]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[231]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[232]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[233]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[234]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[234]	Bool	false	Non-retain	False	False Fa		False		System-internal
			Non-retain		False Fa				•
EXEC_BITS[236]	Bool	false false	Non-retain Non-retain	False False	False Fa		False False		System-internal
EXEC_BITS[237]	Bool								System-internal
EXEC_BITS[238]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[239]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[240]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[241]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[242]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[243]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[244]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[245]	Bool	false	Non-retain	False	False Fa		False		System-internal
EXEC_BITS[246]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[247]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[248]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
EXEC_BITS[249]	Bool	false	Non-retain	False	False Fa	alse	False		System-internal
▼ OFFSETS	G7_Offsets-		Non-retain	False	False Fa	alse	True		Internal offsets
	Plus_V6								
SINI_OFFSET	UInt	0	Non-retain	False	False Fa	alse	False		Offset of internal array SIN
LSTT_OFFSET	UInt	2	Non-retain	False	False Fa		False		Offset of internal array LSTT[]
ATAJ_OFFSET	UInt	8	Non-retain	False	False Fa		False		Offset of internal array ATAJ[]
ATAB_OFFSET	UInt	14	Non-retain	False	False Fa		False		Offset of internal array ATAB[]
PSTT_OFFSET	UInt	26	Non-retain Non-retain	False False	False Fa		False False		Offset of internal array PSTT[] Offset of internal array
NSTT_OFFSET ASSJ_OFFSET	UInt	32	Non-retain	False	False Fa		False		NSTT[] Offset of internal array
ASSB_OFFSET	UInt	37	Non-retain	False	False Fa		False		ASSJ[] Offset of internal array
PTTS_OFFSET	UInt	42	Non-retain	False	False Fa		False		ASSB[] Offset of internal array
NTTS_OFFSET	UInt	47	Non-retain	False	False Fa		False		PTTS[] Offset of internal array
SW_SQTS_OFFSET	UInt	52	Non-retain	False	False Fa		False		NTTS[] Offset of internal array
SWITCH_OFFSET	UInt	57	Non-retain	False	False Fa	_	False		SW_SQTS[] Offset of internal array
									SWITCH[]
TVX_OFFSET	UInt	59	Non-retain	False	False Fa	alse	False		Offset of internal array TV
TTX_OFFSET	UInt	63	Non-retain	False	False Fa	alse	False		Offset of internal array TT
TSX_OFFSET	UInt	67	Non-retain	False	False Fa	alse	False		Offset of internal array TS
S00X_OFFSET	UInt	69	Non-retain	False	False Fa	alse	False		Offset of internal array SOOX[]
SOFFX_OFFSET	UInt	71	Non-retain	False	False Fa		False		Offset of internal array SOFFX[]
SONX_OFFSET	UInt	73	Non-retain	False	False Fa		False		Offset of internal array SONX[]
SAX_OFFSET	UInt	75	Non-retain	False	False Fa		False		Offset of internal array SAX[]
SERRX_OFFSET	UInt	77	Non-retain	False	False Fa		False		Offset of internal array SERRX[]
SMX_OFFSET	UInt	83	Non-retain	False	False Fa		False		Offset of internal array SMX[]
SOX_OFFSET	UInt	89	Non-retain	False	False Fa		False		Offset of internal array SC
S1X_OFFSET	UInt	95	Non-retain	False	False Fa		False		Offset of internal array S1
THRESHOLD_SUP	USInt	0	Non-retain	False	False Fa		False		Threshold for step activat
THRESHOLD_WARN	USInt	0	Non-retain	False	False Fa		False		Threshold for step activat time (warning only)
▼ GC_FLAGS	G7_GCFlags-		Non-retain	False	False Fa	1	True		Compiler flags

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COND_ED SSKIP_ON ACK_REQ ILP_MAN SWM_LOCKED SET_ENO IL_CAT IL_SUBCAT_1	USInt Bool Bool Bool Bool	16#E3 false true	Non-retain Non-retain Non-retain	E.L.	API		
SSKIP_ON ACK_REQ ILP_MAN SWM_LOCKED SET_ENO IL_CAT	Bool Bool Bool Bool	false true	Non-retain	False	False False	False	Language in networks
ACK_REQ ILP_MAN SWM_LOCKED SET_ENO IL_CAT	Bool Bool Bool	true		False	False False	False	Skip steps
ILP_MAN SWM_LOCKED SET_ENO IL_CAT	Bool Bool		Non retain	False	False False	False	Acknowledgement requ
SWM_LOCKED SET_ENO IL_CAT	Bool Bool	false		laise	l disc l disc	laise	for reaction errors
SET_ENO IL_CAT	Bool		Non-retain	False	False False	False	Permanent processing of interlocks in manual mo
IL_CAT		false	Non-retain	False	False False	False	Lock operating mode setion
	LICIO	false	Non-retain	False	False False	False	Set ENO automatically
II SURCAT 1	USInt	1	Non-retain	False	False False	False	Category for interlock
12_3000/11_1	USInt	0	Non-retain	False	False False	False	Subcategory 1 for inter
IL_SUBCAT_2	USInt	0	Non-retain	False	False False	False	Subcategory 2 for inter
REACT_CAT	USInt	1	Non-retain	False	False False	False	Category for reaction
REACT_SUBCAT_1	USInt	0	Non-retain	False	False False	False	Subcategory 1 for react
REACT_SUBCAT_2	USInt	0	Non-retain	False	False False	False	Subcategory 2 for react
WARN_CAT	USInt	2	Non-retain	False	False False	False	Category for warnings
	USInt	0	Non-retain	False	False False	False	Subcategory 1 for warr
WARN_SUBCAT_1		_					
WARN_SUBCAT_2	USInt	0	Non-retain	False	False False	False	Subcategory 2 for warn
CRIT_ON	Bool	false	Non-retain	False	False False	False	Criteria analysis activat
▼ Trans1	G7_Transition- Plus_V6	falsa	Non-retain	False	False False	True	Transition structure
TV	Bool	false	Non-retain	False	False False	False	Transition is valid
TT	Bool	false	Non-retain	False	False False	False	Transition is satisfied
TS	Bool	false	Non-retain	False	False False	False	Transition switches
TNO	Int	1	Non-retain	False	False False	False	Indicates the user-defir
CRIT	DWord	16#0	Non-retain	False	False False	False	transition number Status of the maximum LAD/FBD elements of the transition in the curren
CRIT_OLD	DWord	16#0	Non-retain	False	False False	False	cessing cycle Status of the maximum LAD/FBD elements of th transition in the previous
CRIT_FLT	DWord	16#0	Non-retain	False	False False	False	processing cycle Copy of CRIT if an error curs
▼ Trans2	G7_Transition- Plus_V6		Non-retain	False	False False	True	Transition structure
TV	Bool	false	Non-retain	False	False False	False	Transition is valid
TT	Bool	false	Non-retain	False	False False	False	Transition is satisfied
TS	Bool	false	Non-retain	False	False False	False	Transition switches
TNO	Int	2	Non-retain	False	False False	False	Indicates the user-defir
CRIT	DWord	16#0	Non-retain	False	False False	False	Status of the maximum LAD/FBD elements of th transition in the curren cessing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False False	False	Status of the maximum LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False False	False	Copy of CRIT if an error
▼ Trans3	G7_Transition- Plus_V6		Non-retain	False	False False	True	Transition structure
TV	Bool	false	Non-retain	False	False False	False	Transition is valid
TT	Bool	false	Non-retain	False	False False	False	Transition is satisfied
TS	Bool	false	Non-retain	False	False False	False	Transition switches
TNO	Int	3	Non-retain	False	False False	False	Indicates the user-defir
CRIT	DWord	16#0	Non-retain	False	False False	False	Status of the maximum LAD/FBD elements of the transition in the currencessing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False False	False	Status of the maximum LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False False	False	Copy of CRIT if an error curs
▼ Trans4	G7_Transition- Plus_V6		Non-retain	False	False False	True	Transition structure
TV	Bool	false	Non-retain	False	False False	False	Transition is valid
TT	Bool	false	Non-retain	False	False False	False	Transition is satisfied
TS	Bool	false	Non-retain	False	False False	False	Transition switches
TNO	Int	4	Non-retain	False	False False	False	Indicates the user-defir transition number
CRIT	DWord	16#0	Non-retain	False	False False	False	Status of the maximum LAD/FBD elements of the transition in the current cessing cycle

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nme	Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	able	Visible in HMI engi- neering	Setpoint	Supervi- sion	Comment
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 33 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error oc
▼ Trans6	G7_Transition- Plus_V6		Non-retain	False	False	False	True		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False	False	False		Transition is satisfied
TS	Bool	false	Non-retain	False	False		False		Transition switches
TNO	Int	6	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 3 LAD/FBD elements of the transition in the current p cessing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error occurs
▼ Trans8	G7_Transition- Plus_V6		Non-retain	False	False	False	True		Transition structure
TV	Bool	false	Non-retain	False	False	False	False		Transition is valid
TT	Bool	false	Non-retain	False	False		False		Transition is satisfied
TS	Bool	false	Non-retain	False	False	False	False		Transition switches
TNO	Int	8	Non-retain	False	False	False	False		Indicates the user-defined transition number
CRIT	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 3 LAD/FBD elements of the transition in the current p cessing cycle
CRIT_OLD	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 3 LAD/FBD elements of the transition in the previous processing cycle
CRIT_FLT	DWord	16#0	Non-retain	False	False	False	False		Copy of CRIT if an error oc
▼ START	G7_StepPlus_V6	5	Non-retain	False	False	False	True		Step structure
S1	Bool	false	Non-retain	False	False	False	False		Step is activated
L1	Bool	false	Non-retain	False	False	False	False		interlock leaving state
V1	Bool	false	Non-retain	False	False		False		Supervision entering stat
R1	Bool	false	Non-retain	False	False		False		Reserved
A1	Bool	false	Non-retain	False	False		False		Error is acknowledged
S0	Bool	false	Non-retain	False	False		False		Step is deactivated
LO	Bool	false	Non-retain	False	False False		False False		Interlock entering state
V0 X	Bool Bool	false false	Non-retain Non-retain	False False	False		False		Supervision leaving state Step is active
LA	Bool	false	Non-retain	False	False		False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False		False		Supervision active
RA	Bool	false	Non-retain	False	False		False		Reserved
AA	Bool	false	Non-retain	False	False		False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the progr med interlock
VS	Bool	false	Non-retain	False	False	False	False		Direct result of the progr med supervision
SNO	Int	1	Non-retain	False	False		False		User step number
Т	Time	T#0ms	Non-retain	False	False	-	False		Total step activation time
U	Time	T#0ms	Non-retain	False	False	False	False		Step activation time with disturbance
T_MAX	Time	T#10S	Non-retain	False	False		False		Maximal step activation t
T_WARN	Time	T#7S	Non-retain	False	False		False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 3 LAD/FBD elements in the terlock in the current pro cessing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False		False		Copy of CRIT_LOC when t interlock leaves the state
SM	Bool	false	Non-retain	False	False		False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False		False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False		False		System-internal
▼ JAZDA_GORA	G7_StepPlus_V6		Non-retain	False	False		True		Step structure
	Bool	false	Non-retain	False	False		False		Step is activated
S1		l							
S1 L1	Bool	false	Non-retain	False	False		False		interlock leaving state
S1		false false false	Non-retain Non-retain Non-retain	False False False	False False False	False	False False		interlock leaving state Supervision entering stat Reserved

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ame	Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	Writ- Visible in able HMI engi- from neering HMI/ OPC UA/ Web API		Supervi- sion	Comment
A1	Bool	false	Non-retain	False	False False	False		Error is acknowledged
S0	Bool	false	Non-retain	False	False False	False		Step is deactivated
LO	Bool	false	Non-retain	False	False False	False		Interlock entering state
V0	Bool	false	Non-retain	False	False False	False		Supervision leaving state
X		false	Non-retain	False	False False	False		Step is active
LA		false	Non-retain	False	False False	False		Interlock is not satisfied
VA		false	Non-retain	False	False False	False		Supervision active
RA		false	Non-retain	False	False False	False		Reserved
AA		false	Non-retain Non-retain	False	False False False False	False False		Reserved
SS LS	Bool Bool	false	Non-retain	False False	False False	False		System-internal Direct result of the progra
VS		false	Non-retain	False	False False	False		med interlock Direct result of the progra
								med supervision
SNO	Int	2	Non-retain	False	False False	False		User step number
T		T#0ms	Non-retain	False	False False	False		Total step activation time
U T_MAX	Time Time	T#0ms T#10S	Non-retain Non-retain	False False	False False	False False		Step activation time withous disturbance Maximal step activation ti
		T#7S	Non-retain	False	False False	False		Warning time
T_WARN CRIT_LOC		16#0	Non-retain	False	False False	False		Status of the maximum 33
CRIT_LOC	DWord	10#0	Non-retain	i dise	l dise i dise	laise		LAD/FBD elements in the iterlock in the current processing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False False	False		Copy of CRIT_LOC when the interlock leaves the state
SM		false	Non-retain	False	False False	False		System-internal
H_IL_ERR		16#0	Non-retain	False	False False	False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False False	False		System-internal
▼ STOP_WINDY	G7_StepPlus_V6		Non-retain	False	False False	True		Step structure
S1		false	Non-retain	False	False False	False		Step is activated
L1		false	Non-retain	False	False False	False		interlock leaving state
V1		false	Non-retain	False	False False	False		Supervision entering state
R1		false	Non-retain	False	False False	False		Reserved
A1		false false	Non-retain	False	False False False False	False False		Error is acknowledged
S0 L0		false	Non-retain Non-retain	False False	False False	False		Step is deactivated Interlock entering state
V0		false	Non-retain	False	False False	False		Supervision leaving state
X		false	Non-retain	False	False False	False		Step is active
LA		false	Non-retain	False	False False	False		Interlock is not satisfied
VA		false	Non-retain	False	False False	False		Supervision active
RA	Bool	false	Non-retain	False	False False	False		Reserved
AA	Bool	false	Non-retain	False	False False	False		Reserved
SS	Bool	false	Non-retain	False	False False	False		System-internal
LS		true	Non-retain	False	False False	False		Direct result of the programed interlock
VS	Bool	false	Non-retain	False	False False	False		Direct result of the programed supervision
SNO	Int	3	Non-retain	False	False False	False		User step number
T	Time	T#0ms	Non-retain	False	False False	False		Total step activation time
U		T#0ms	Non-retain	False	False False	False		Step activation time with disturbance
T_MAX		T#10S T#7S	Non-retain	False False	False False False False	False False		Maximal step activation t
T_WARN CRIT_LOC		16#0	Non-retain Non-retain	False	False False	False		Warning time Status of the maximum 3 LAD/FBD elements in the terlock in the current pro cessing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False False	False		Copy of CRIT_LOC when t interlock leaves the state
SM		false	Non-retain	False	False False	False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False False	False		System-internal
H_SV_FLT		16#04	Non-retain	False	False False	False		System-internal
▼ JAZDA_DOL	G7_StepPlus_V6		Non-retain	False	False False	True		Step structure
S1		false	Non-retain	False	False False	False		Step is activated
L1		false	Non-retain	False	False False	False		interlock leaving state
V1		false	Non-retain	False	False False	False		Supervision entering stat
R1		false	Non-retain	False	False False	False		Reserved
A1		false	Non-retain	False	False False	False		Error is acknowledged
S0		false	Non-retain	False	False False	False		Step is deactivated
LO		false	Non-retain	False	False False	False		Interlock entering state
V0		false false	Non-retain Non-retain	False False	False False False False	False False		Supervision leaving state Step is active
X		false	Non-retain Non-retain	False	False False	False		Interlock is not satisfied
Ι Δ		false	Non-retain	False	False False	False		Supervision active
LA VA	Bool	laise						
VA RA		false	Non-retain	False	False False	False		Reserved

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Auto	mati	on P	ortal

Name	Data type	Default value	Retain	Accessible from HMI/OPC UA/Web API	able	Visible in HMI engi- neering	Setpoint	Supervi- sion	Comment
AA	Bool	false	Non-retain	False	False	False	False		Reserved
SS	Bool	false	Non-retain	False	False	False	False		System-internal
LS	Bool	true	Non-retain	False	False	False	False		Direct result of the program- med interlock
VS	Bool	false	Non-retain	False	False		False		Direct result of the program- med supervision
SNO	Int	4	Non-retain	False	False		False		User step number
Т	Time	T#0ms	Non-retain	False	False		False		Total step activation time
U	Time	T#0ms	Non-retain	False	False		False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False	False	False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False	False	False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False		False		Status of the maximum 32 LAD/FBD elements in the in- terlock in the current pro- cessing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False		False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False		False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False		False		System-internal
H_SV_FLT	Byte	16#04	Non-retain	False	False		False		System-internal
▼ STOP_WINDY2	G7_StepPlus_	_V6 false	Non-retain Non-retain	False False	False False		True False		Step structure
S1		false	-		False		False		Step is activated
L1	Bool		Non-retain	False					interlock leaving state
V1	Bool	false	Non-retain	False	False		False		Supervision entering state
R1	Bool	false	Non-retain	False	False		False		Reserved
A1	Bool	false	Non-retain	False	False		False		Error is acknowledged
SO	Bool	false	Non-retain	False	False		False		Step is deactivated
LO	Bool	false	Non-retain	False	False		False		Interlock entering state
VO	Bool	false	Non-retain	False	False		False		Supervision leaving state
X	Bool	false	Non-retain	False	False		False		Step is active
LA	Bool	false	Non-retain	False	False		False		Interlock is not satisfied
VA	Bool	false	Non-retain	False	False		False		Supervision active
RA	Bool	false	Non-retain	False	False		False		Reserved
AA	Bool	false	Non-retain	False	False		False		Reserved
SS	Bool	false	Non-retain	False	False		False		System-internal
LS	Bool	true	Non-retain	False	False		False		Direct result of the program- med interlock
VS	Bool	false	Non-retain	False	False		False		Direct result of the program- med supervision
SNO	Int	5	Non-retain	False	False		False		User step number
T	Time	T#0ms	Non-retain	False	False		False		Total step activation time
U	Time	T#0ms	Non-retain	False	False		False		Step activation time without disturbance
T_MAX	Time	T#10S	Non-retain	False	False		False		Maximal step activation time
T_WARN	Time	T#7S	Non-retain	False	False		False		Warning time
CRIT_LOC	DWord	16#0	Non-retain	False	False	False	False		Status of the maximum 32 LAD/FBD elements in the in- terlock in the current pro- cessing cycle
CRIT_LOC_ERR	DWord	16#0	Non-retain	False	False		False		Copy of CRIT_LOC when the interlock leaves the state
SM	Bool	false	Non-retain	False	False		False		System-internal
H_IL_ERR	Byte	16#0	Non-retain	False	False	False	False		System-internal
H_SV_FLT Temp	Byte	16#04	Non-retain	False	False	False	False		System-internal

Alarms

Category for GRAPH Warning warnings

Enable alarms	True		
Category	Category enabler	C	Display class
Error		C)
Warning		C	
Info		C	
Category 4		C	
Category 5		C)
Category 6		C)
Category 7		C)
Category 8		C)
Category for inter- locks	Subcategory 1 for terlocks	n-	Subcategory 2 for interlocks
Category for supervi- sions	Subcategory 1 for pervisions	su-	Subcategory 2 for su- pervisions

Subcategory 2 for GRAPH warnings

Subcategory 1 for GRAPH warnings

		
Totally Integrated Automation Portal		
Sequences (1)		
1:		
	L.	
	S1 START	
	T1 Trans1 T2 Trans2	
	JAZDA_DOL JAZDA_GORA	
	H → T6 Trans6 T7 Trans3	
	S5 S3 STOP_WINDY2 STOP_WINDY	
	T8 Trans8 T4 Trans4 T51	