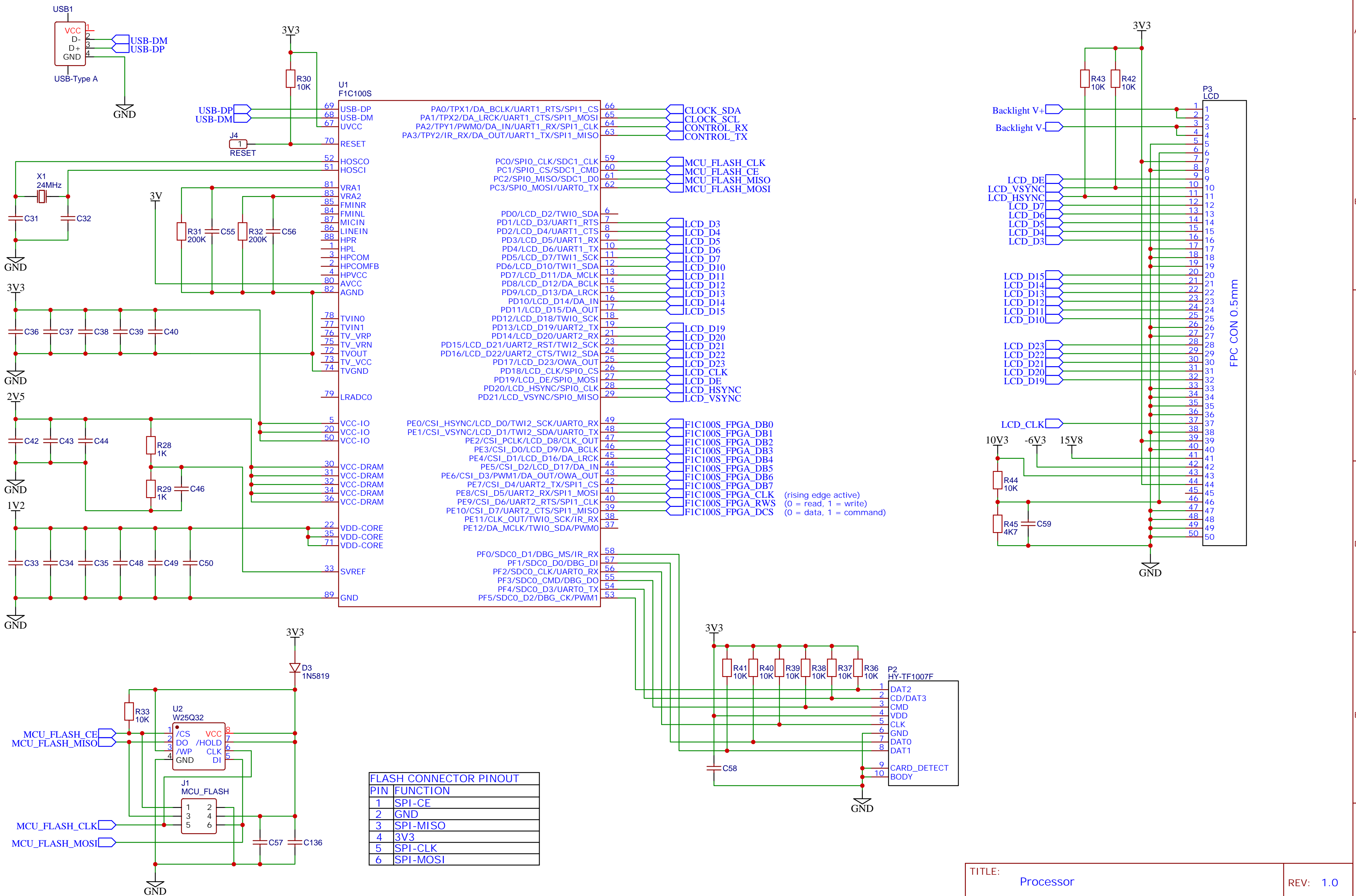
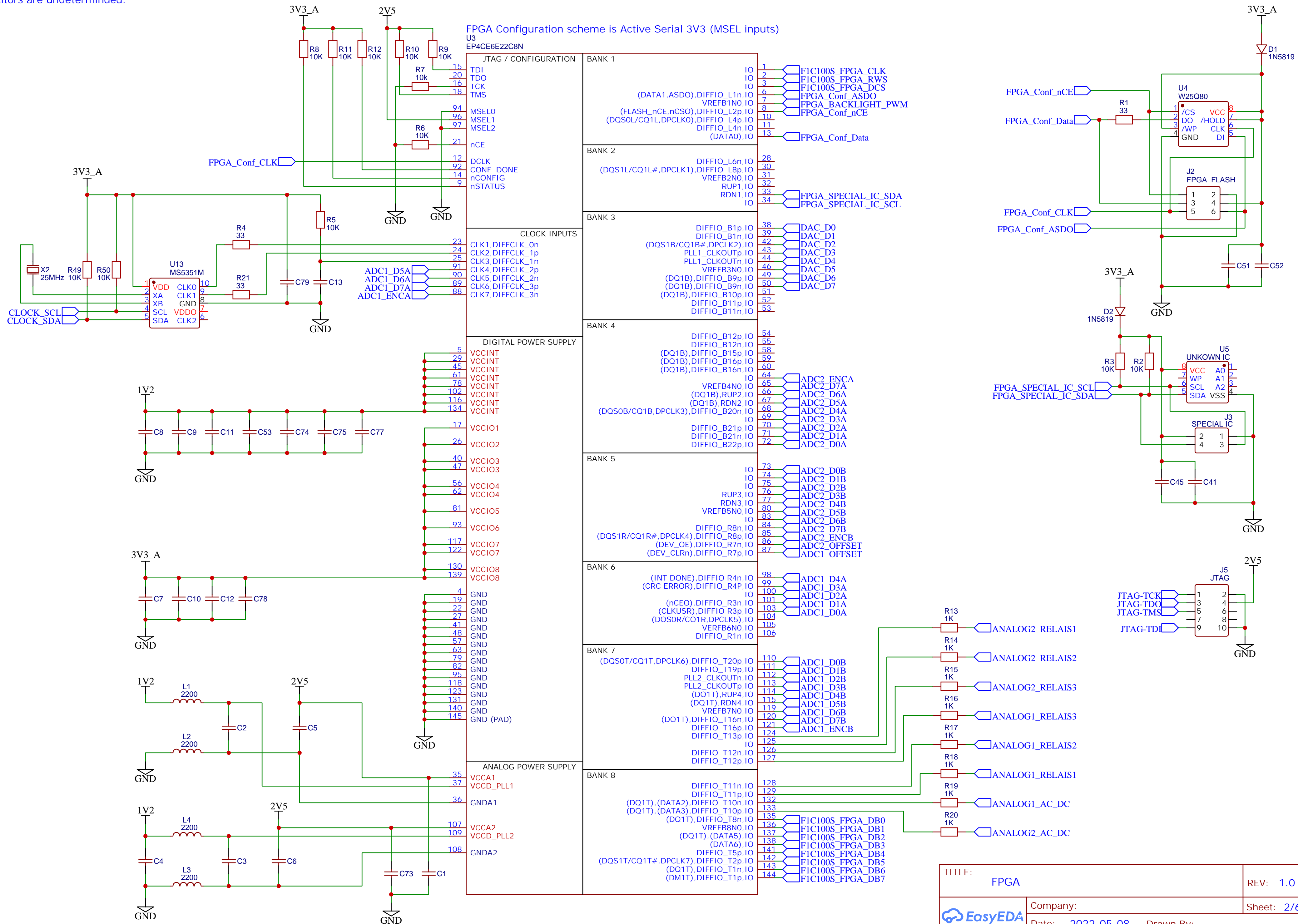


Capacitors are undetermined.



Capacitors are undetermined.

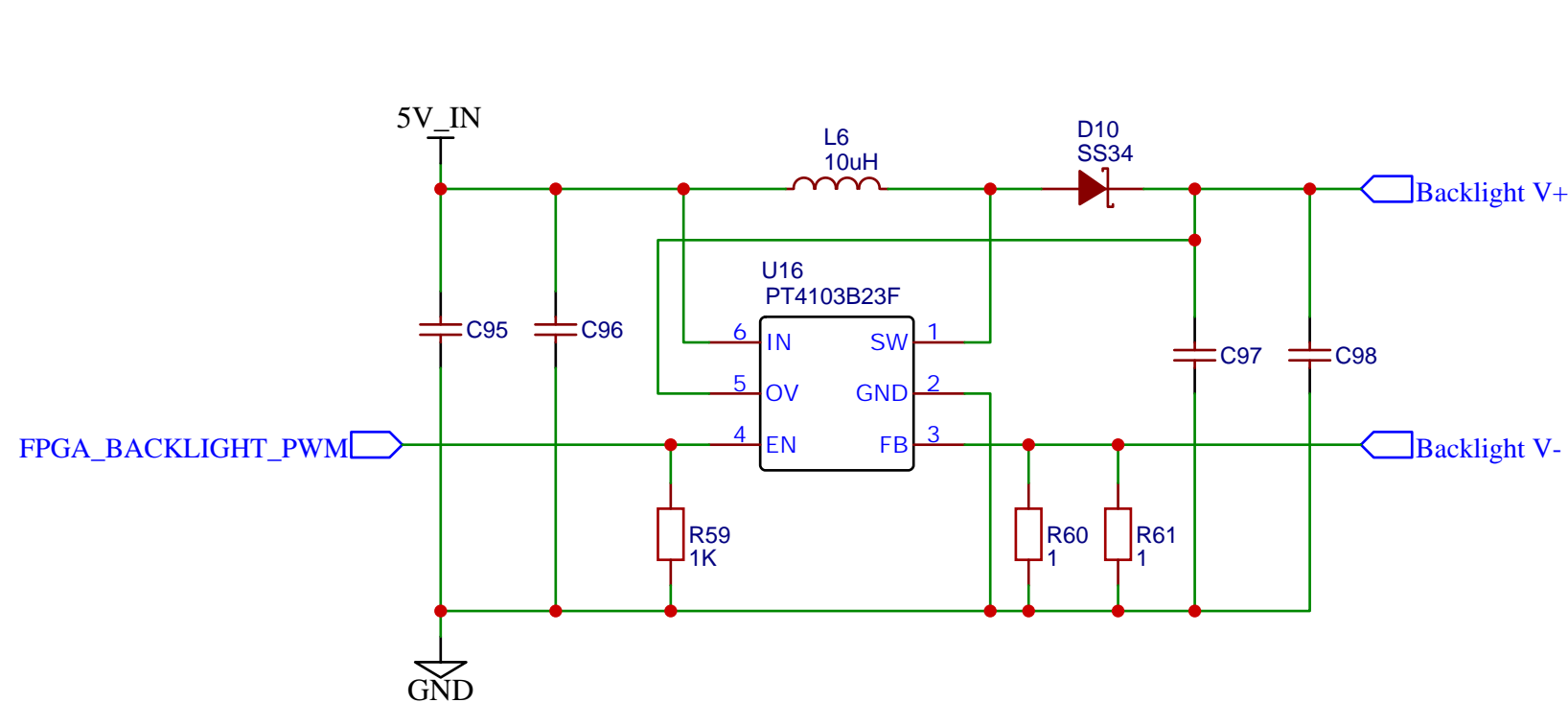
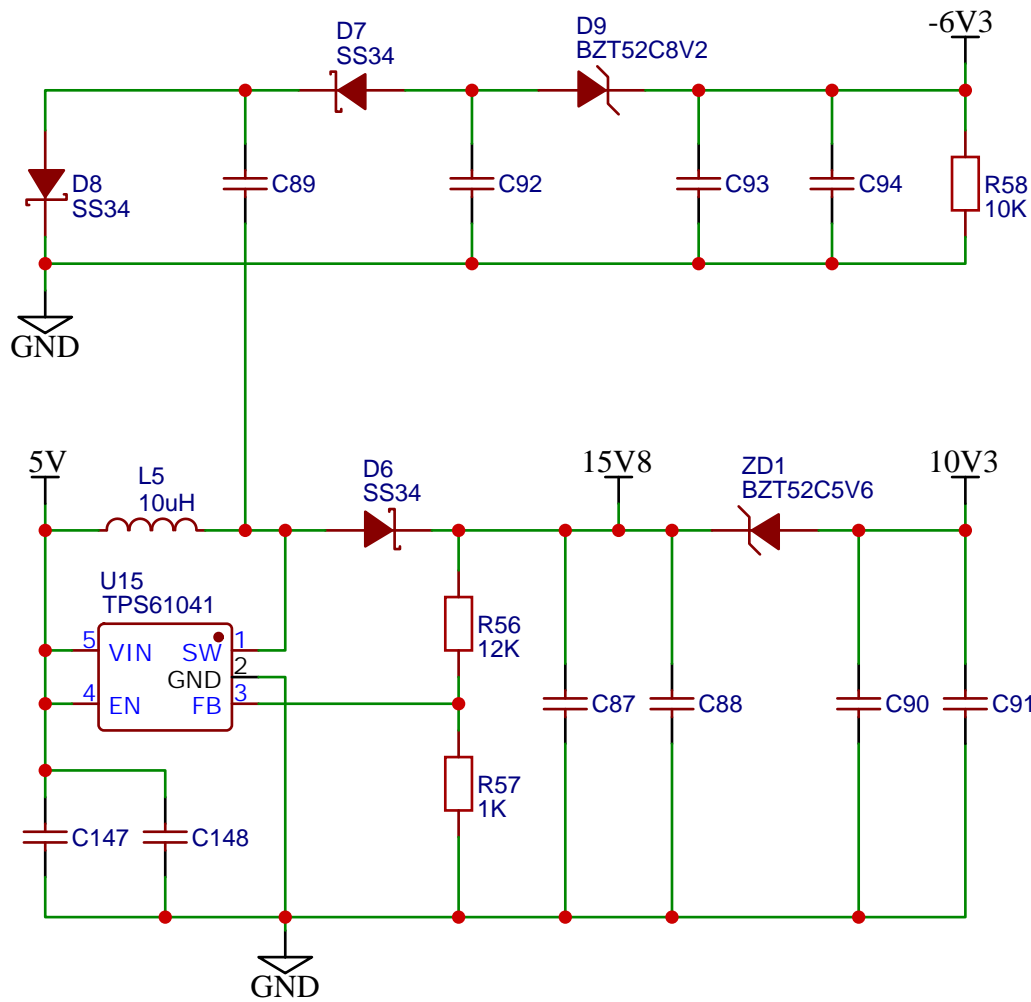
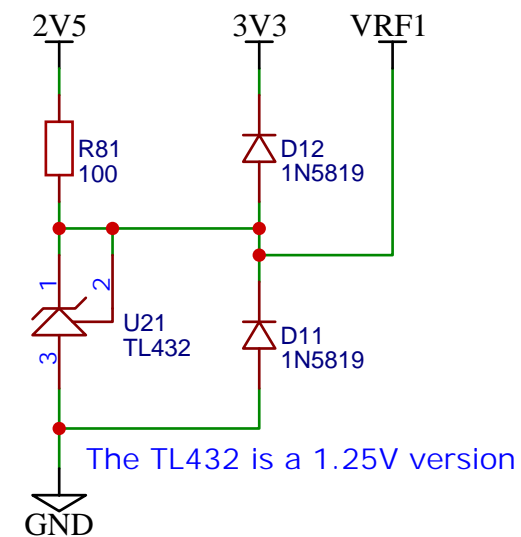
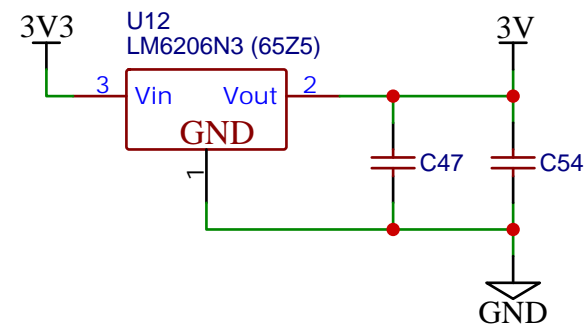
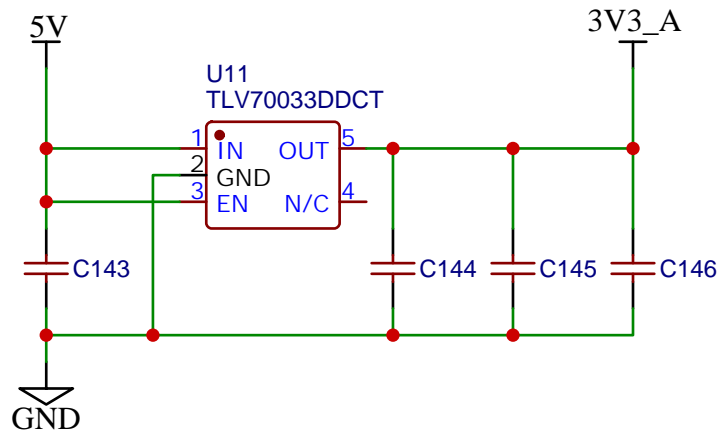
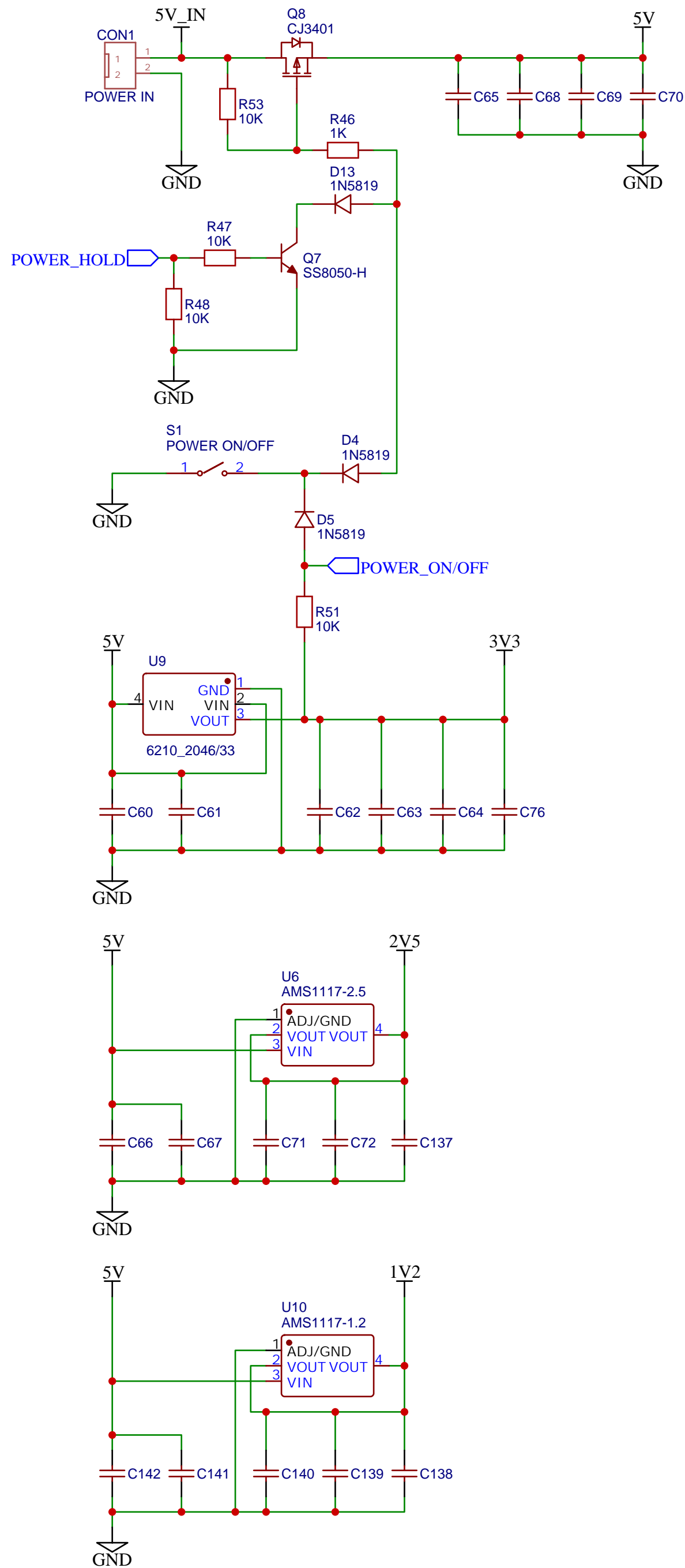


E



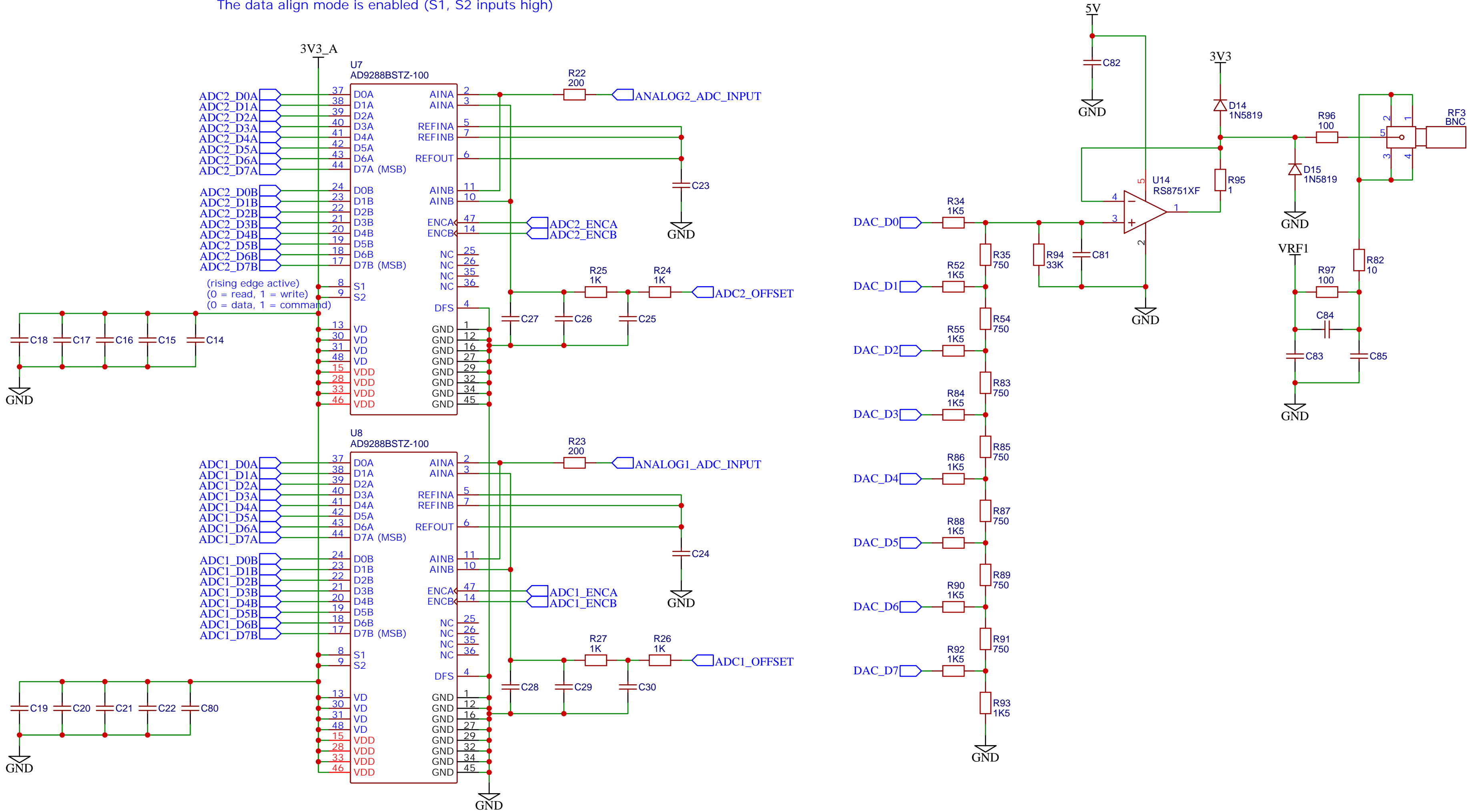
1	2	3	4	5	6	7	8	
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Capacitors are undetermined.



Capacitors are undetermined.

For the ADC's offset binary output is selected (DFS input low)
The data align mode is enabled (S1, S2 inputs high)



Capacitors are undetermined.

