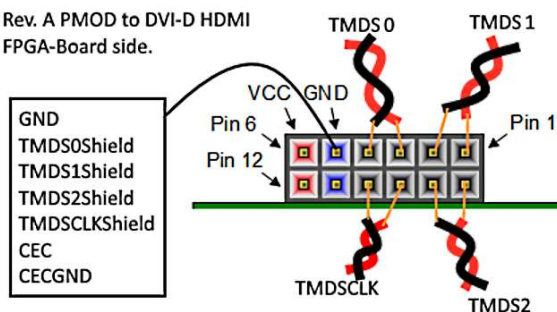


Rev. A PMOD to DVI-D HDMI  
FPGA-Board side.



As +5v is GND, monitor autodetects may not pick up signal. Manually select input.  
Tested at 1080p60 on Arty S7 Port JA.  
High Speed PMOD implies no series resistors & matched pair traces to FPGA host-side.

**Domipheus Labs**

Sheet: /  
File: pmodhdmis.sch

**Title: PMOD to DVI-D HDMI**

Size: User Date: 2018-06-09

KiCad E.D.A. kicad 4.0.7

**Rev: A**

Id: 1/1