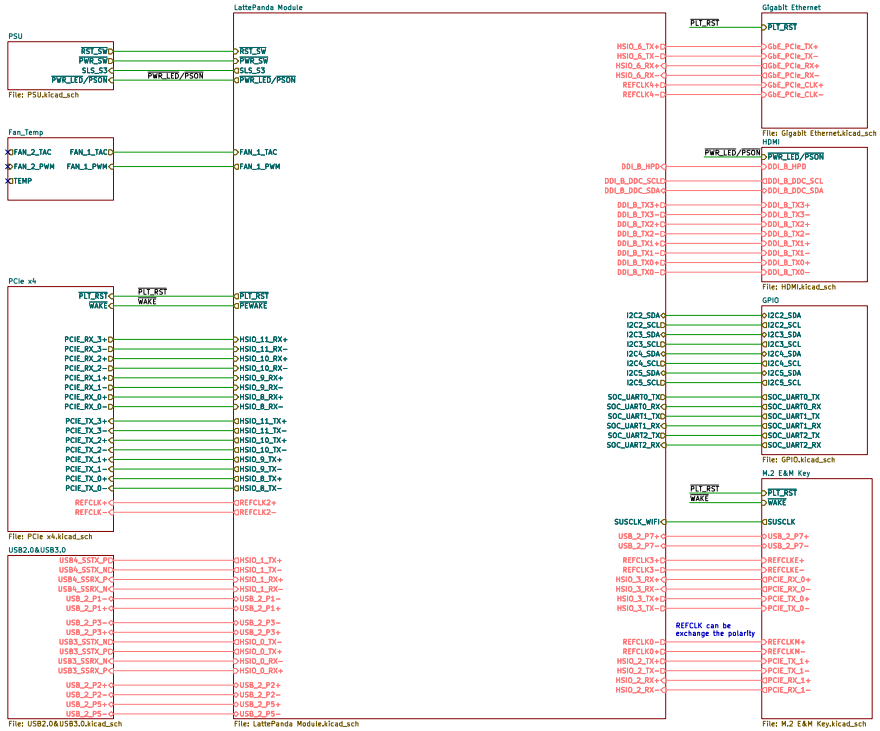
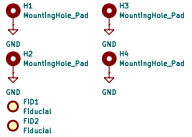




20240901

Sheet: /
File: Nano ITX Carrier for LattePanda Mu.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 1/10

HSIO functions are defined by the BIOS source code,
Cannot be changed in BIOS settings.

Before starting design, you should choose one of
the available BIOS configurations on:
https://github.com/LattePandaTeam/LattePanda-Mu
Or contact techsupport for custom your BIOS

The default functions are defined as follows:

- HSIO0: USB3.2 10Gbps
- HSIO1: USB3.2 10Gbps
- HSIO2: PCIe 3.0 x1
- HSIO3: PCIe 3.0 x1

- HSIO8: PCIe 3.0 x4 (Lane 0)
- HSIO9: PCIe 3.0 x4 (Lane 1)
- HSIO10: PCIe 3.0 x4 (Lane 2)
- HSIO11: PCIe 3.0 x4 (Lane 3)

- HSIO6: PCIe 3.0 x1

DDI/TCP functions are defined by the BIOS source code,
Cannot be changed in BIOS settings.

Before starting design, you should choose one of
the available BIOS configurations on:
https://github.com/LattePandaTeam/LattePanda-Mu

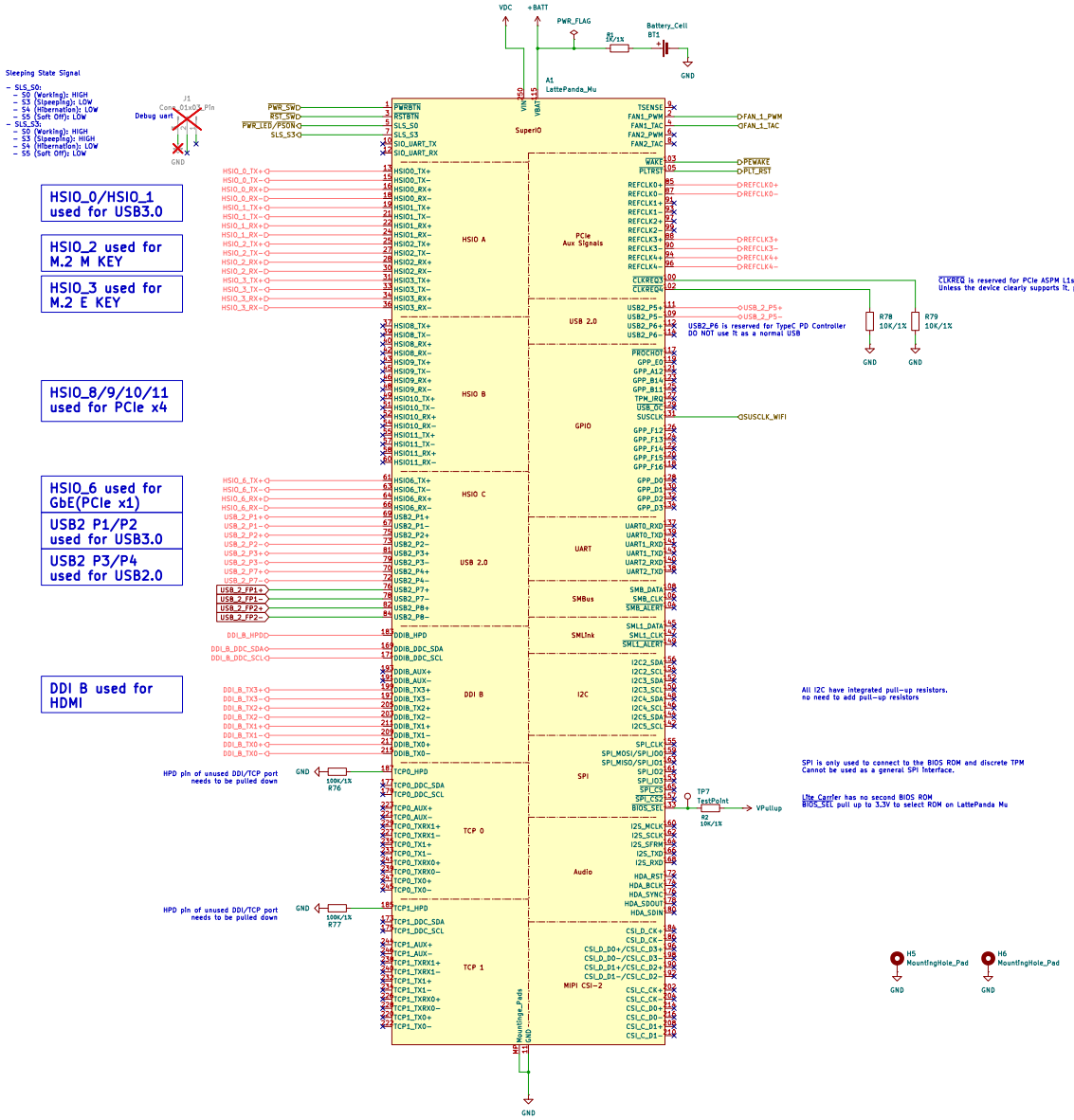
The default functions are defined as follows:

- DDIA: eDP 1.4b (the 40Pin connector on the Mu)

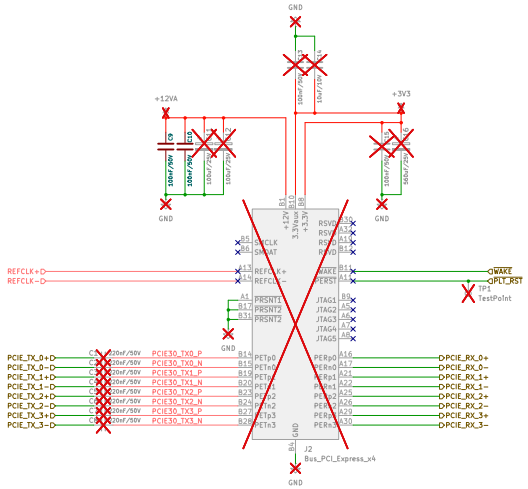
- DDIB: HDMI 2.0

- TCP0: HDMI 2.0

- TCP1: USB TypeC (need external PD controller)



20240901	
Sheet: /LattePanda Module/	
File: LattePanda Module.kicad_sch	
Title: Nano ITX Carrier for LattePanda Mu	
Size: A2	Date: 2024-09-01
KiCad E.D.A. 8.0.4	Id: 2/10
	Rev: V1.0.0

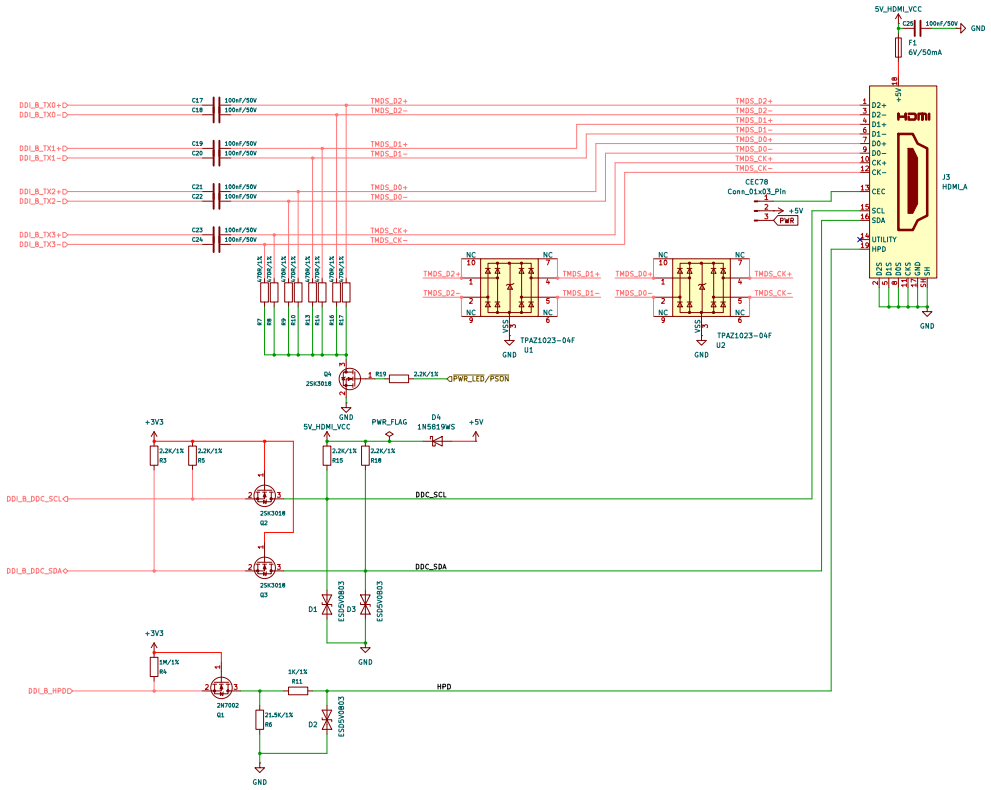


20240901

Sheet: /PCIe x4/
File: PCIe x4.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 3/10

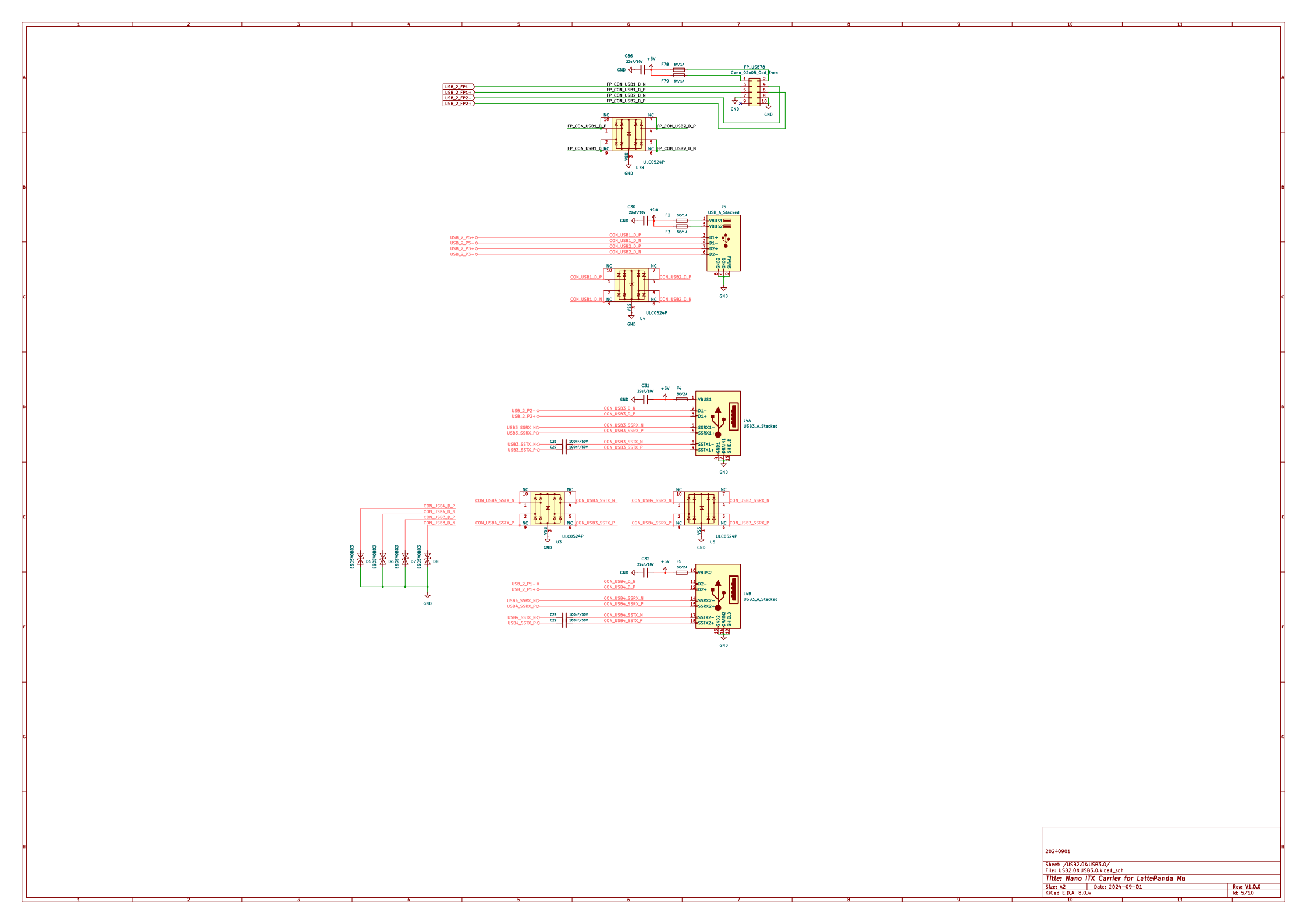


20240901

Sheet: /HDMI/
File: HDMI.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 4/10

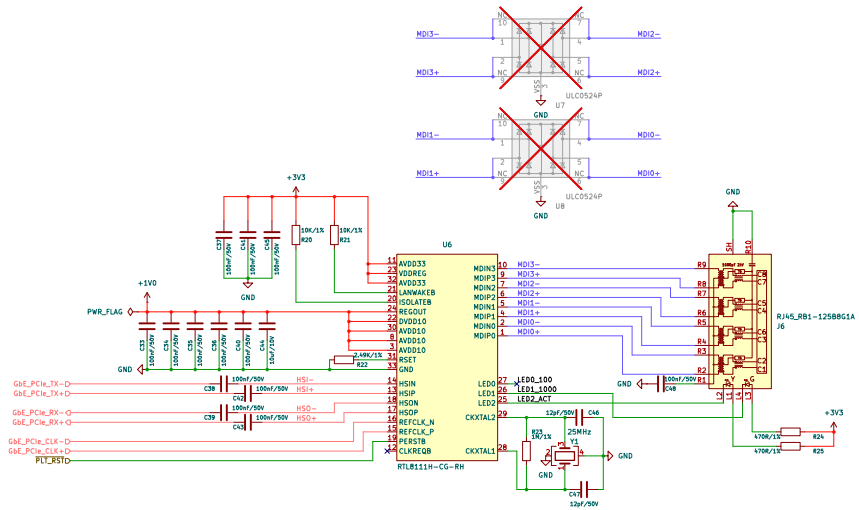


20240901

Sheet: /USB2.0&USB3.0/
File: USB2.0&USB3.0.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 5/10

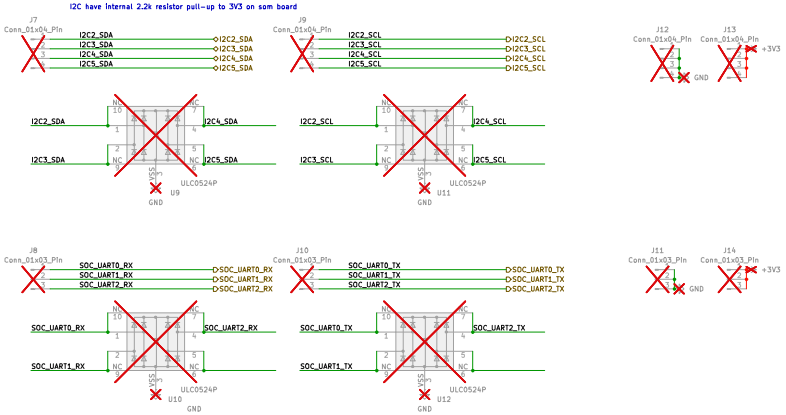


20240901

Sheet: /Gigabit Ethernet/
File: Gigabit Ethernet.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 6/10

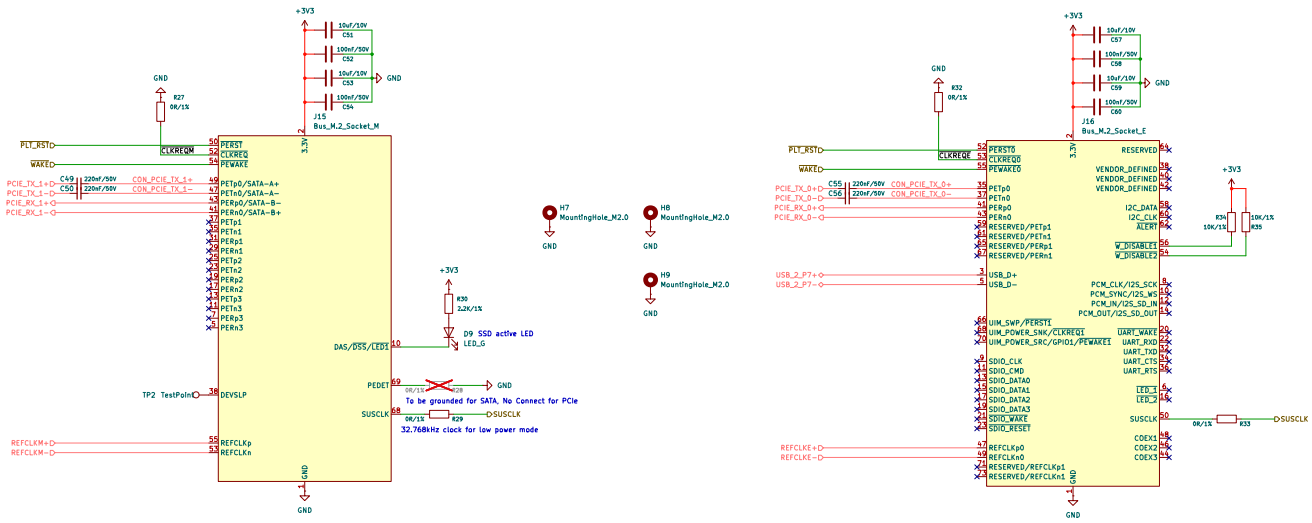


20240901

Sheet: /GPIO/
File: GPIO.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 7/10

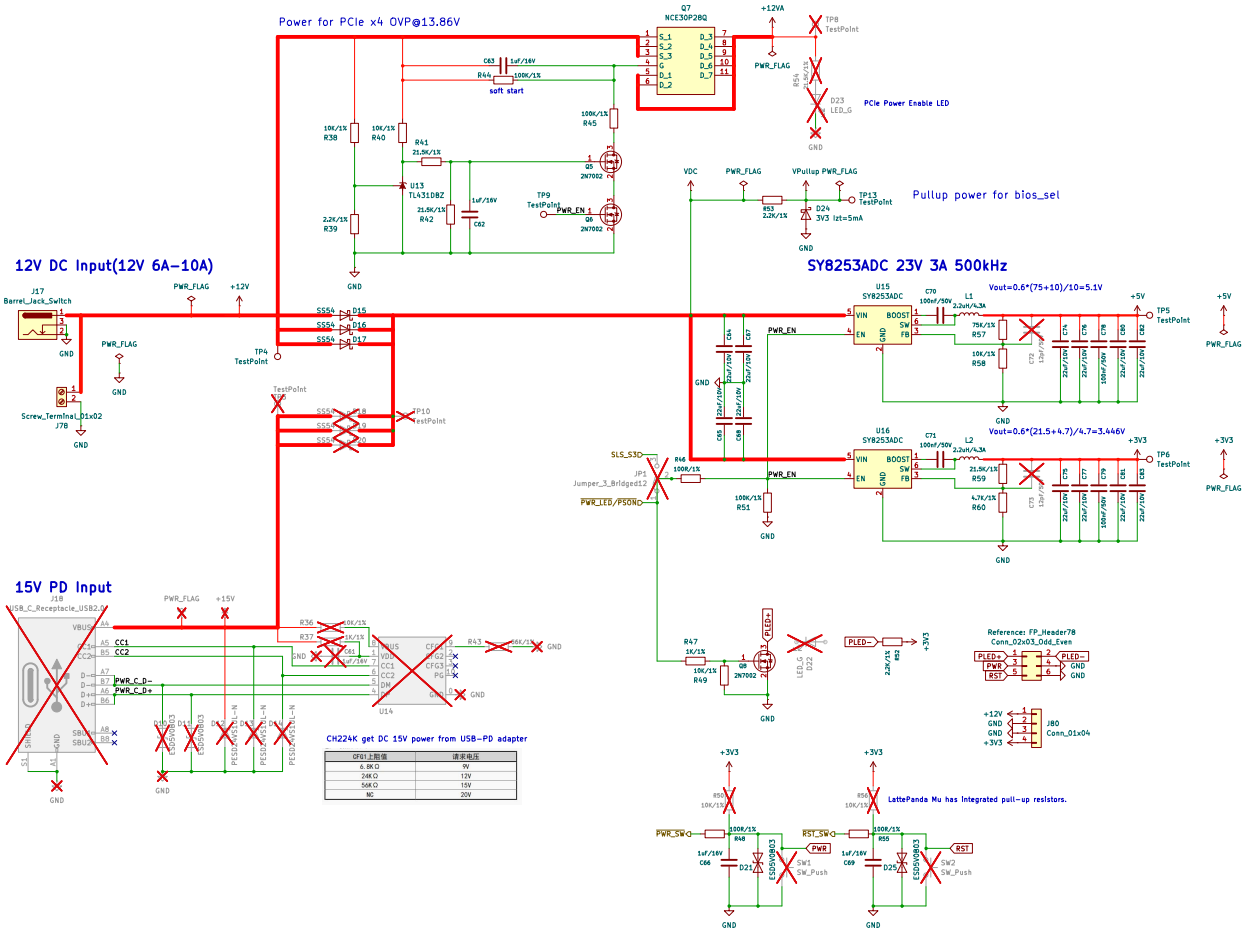


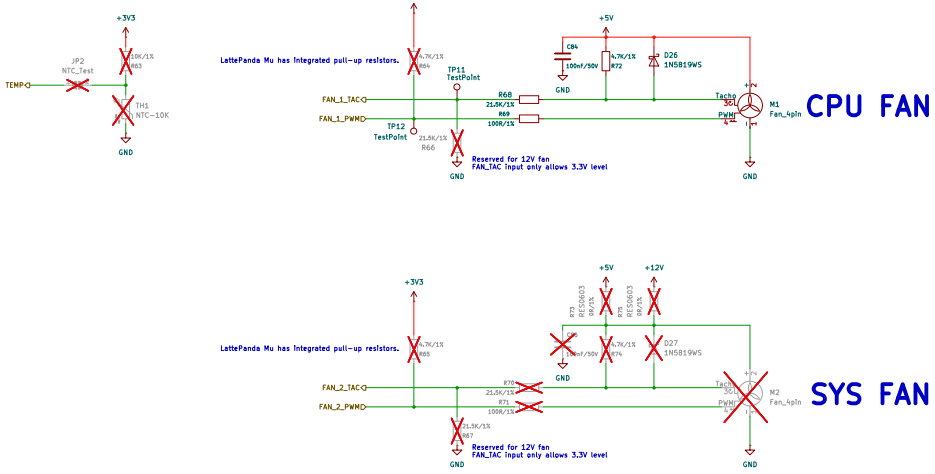
20240901

Sheet: /M.2 E&M Key/
File: M.2 E&M Key.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 8/10


Power for PCIe x4 OVP@13.86V
12V DC Input(12V 6A-10A)
SY8253ADC 23V 3A 500kHz
15V PD Input
Pullup power for bios_sel
PCIe Power Enable LED
soft start
Vout=0.6*(75+10)/10=5.1V
Vout=0.6*(21.5+4.7)/4.7=3.446V
CH224K get DC 15V power from USB-PD adapter
LattePanda Mu has Integrated pull-up resistors.
Reference: FP_Header78
Conn_02x03_Odd_Even
20240901
Sheet: /PSU/
File: PSU.kicad_sch
Title: Nano ITX Carrier for LattePanda Mu
Size: A2
Date: 2024-09-01
Rev: V1.0.0
KiCad E.D.A. 8.0.4
Id: 9/10



20240901

Sheet: /Fan_Temp/
File: fan_temp.kicad_sch

Title: Nano ITX Carrier for LattePanda Mu

Size: A2	Date: 2024-09-01	Rev: V1.0.0
KiCad E.D.A. 8.0.4		Id: 10/10