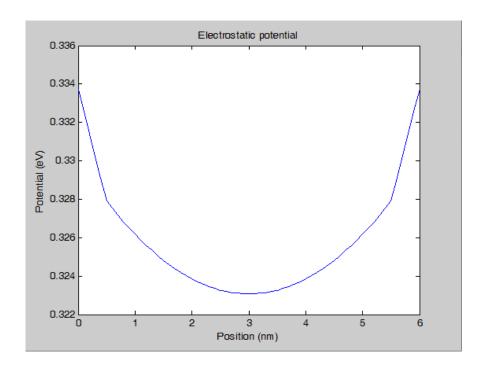
Computational Microelectronics

Assignment #6

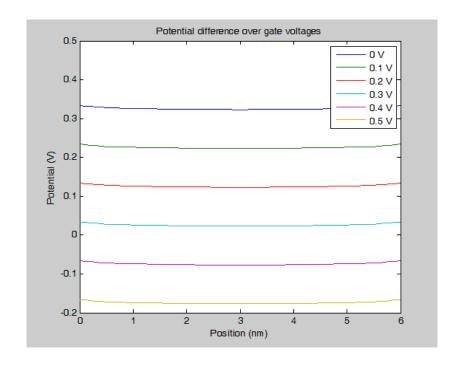
20174009 Choi Pyeunghwi

1. Electrostatic potential at position

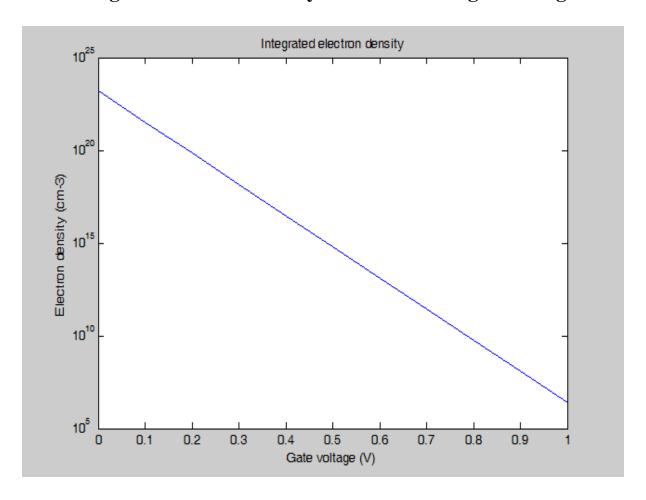
i) When gate voltage = 0 V



i) When gate voltage = $0 \sim 0.5 \text{V}$



2. Integrated electron density as a function of gate voltage



^{&#}x27;Integrated electron density is linearly dependent on gate voltage'