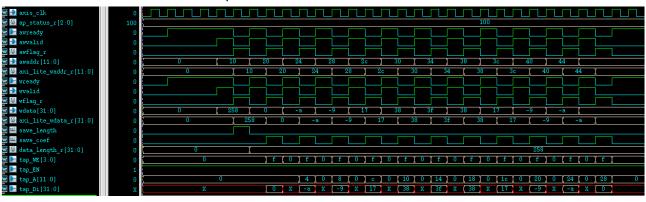
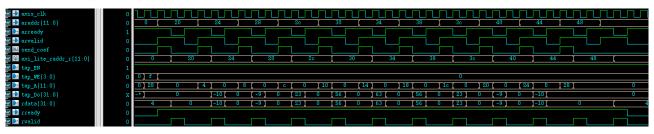
系統晶片設計 lab3 waveform

1. axi-lite寫入fir參數並存入tap sram:

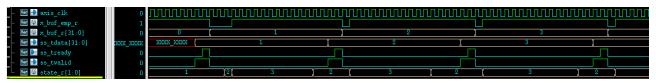


學號:R12943012 姓名:蔡東翰

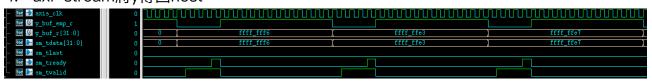
2. axi-lite讀出fir參數:



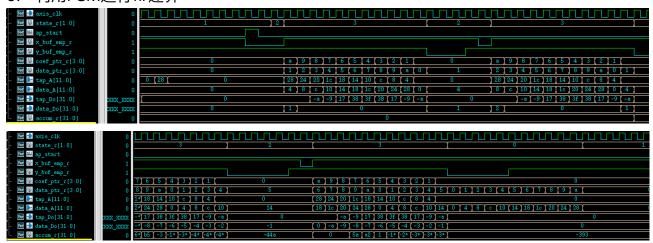
3. axi-stream將x寫入buffer:



4. axi-stream將y傳回host:



5. 利用FSM進行fir運算:



6. ap_idle, ap_done, ap_start:

