

Program #C23-2

A $1.1\mu\text{s}$ 1.56Gb/s/mm^2 Cost-Efficient Large-List
SCL Polar Decoder Using Fully-Reusable LLR Buffers
in 28nm CMOS Technology

Dongyun Kam¹, Byeong Yong Kong² and Youngjoo Lee¹

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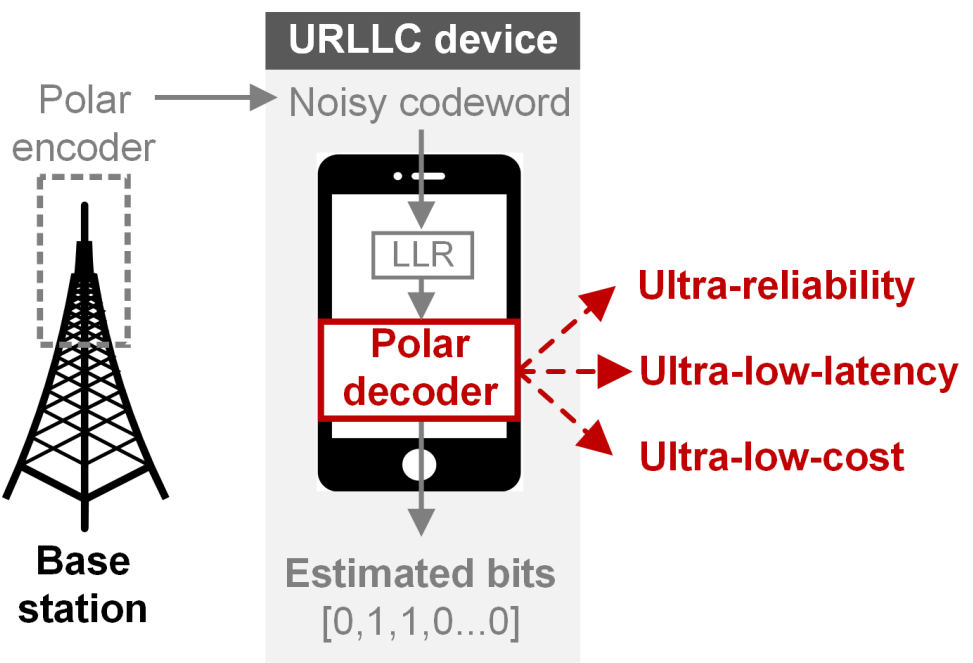
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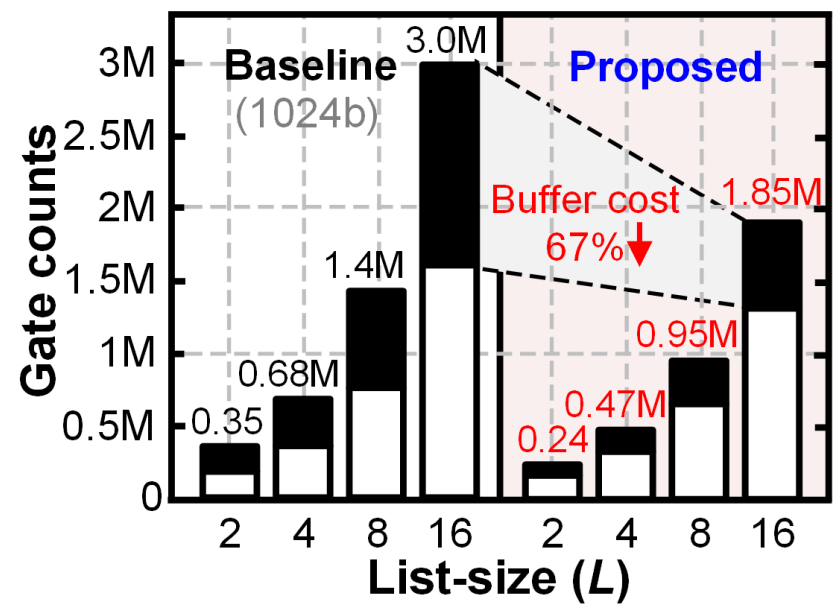
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Ultra-low-cost
(Fully-reusable buffers)



Ultra-reliability
(Large-list)

