This is a tpx3-daq ToTCalib for chip W15-A7.
Run name: ToTCalib_2022-05-17_14-19-37

Parameter	Value
Ibias_CP_PLL	128
Ibias_DiscS2_OFF	8
Ibias_PixelDAC	103
Ibias_TPbufferIn	128
Sense_DAC	29
VTP_fine	260
Vthreshold_fine	394
mask_step	64

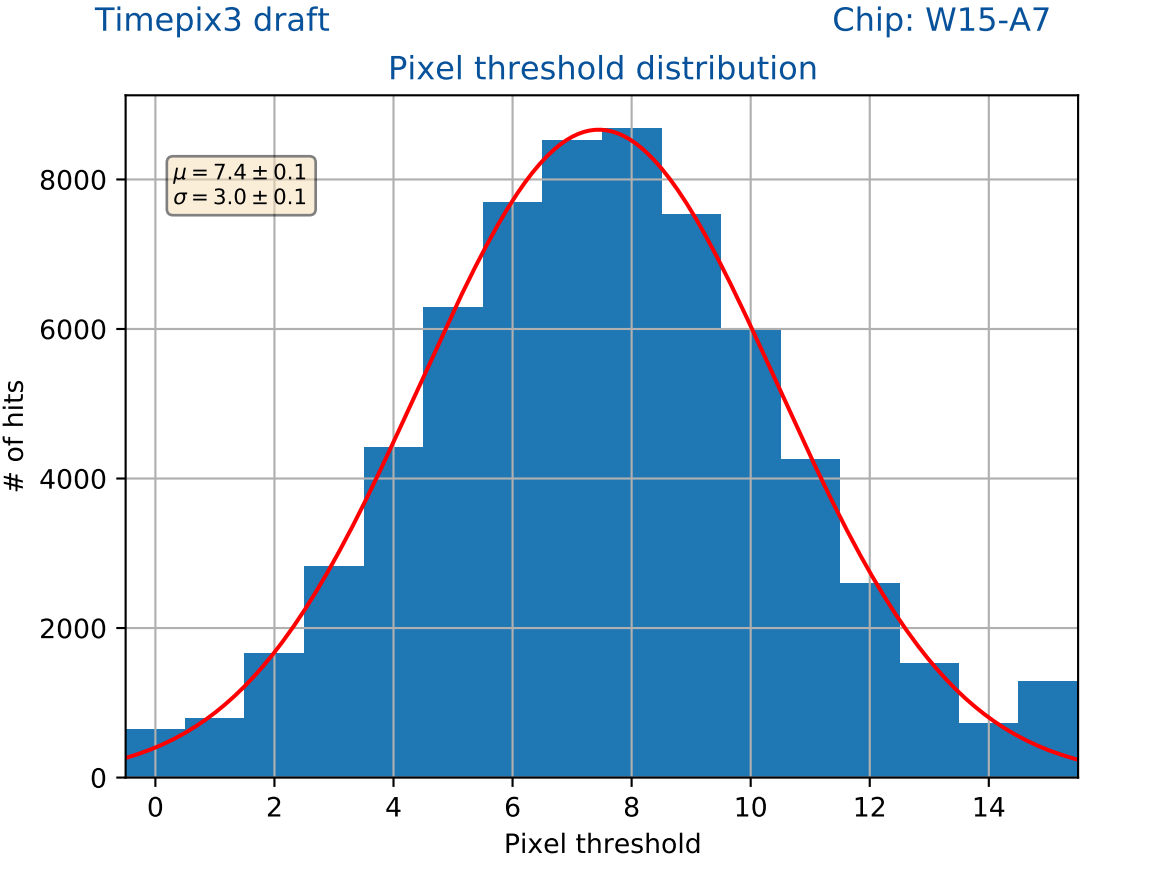
Parameter	Value
Ibias_DiscS1_OFF	8
Ibias_DiscS2_ON	128
Ibias_Preamp_OFF	8
Ibias_TPbufferOut	128
VPreamp_NCAS	128
Vfbk	170
VTP_fine_start	200
tp_period	6

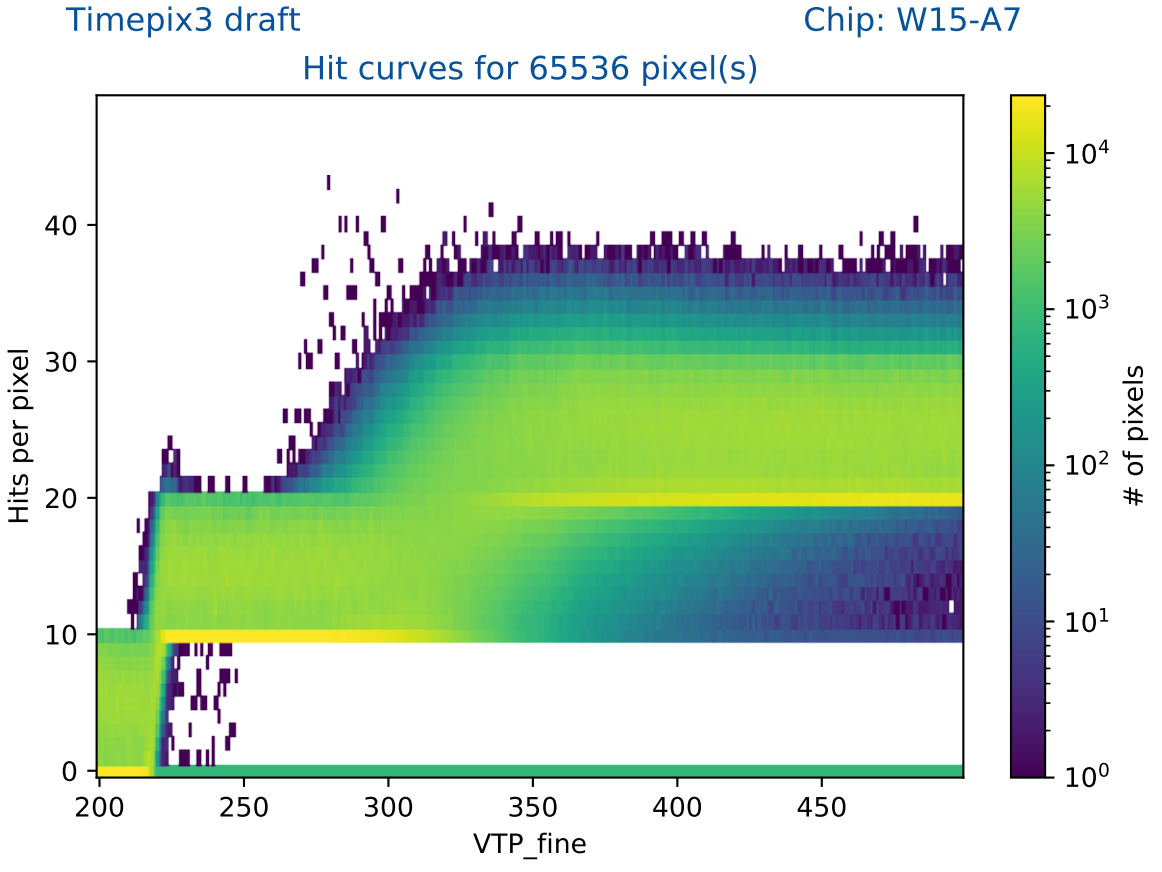
Parameter	Value
Ibias_DiscS1_ON	100
Ibias_Ikrum	4
Ibias_Preamp_ON	150
PLL_Vcntrl	128
VTP_coarse	100
Vthreshold_coarse	9
VTP_fine_stop	500

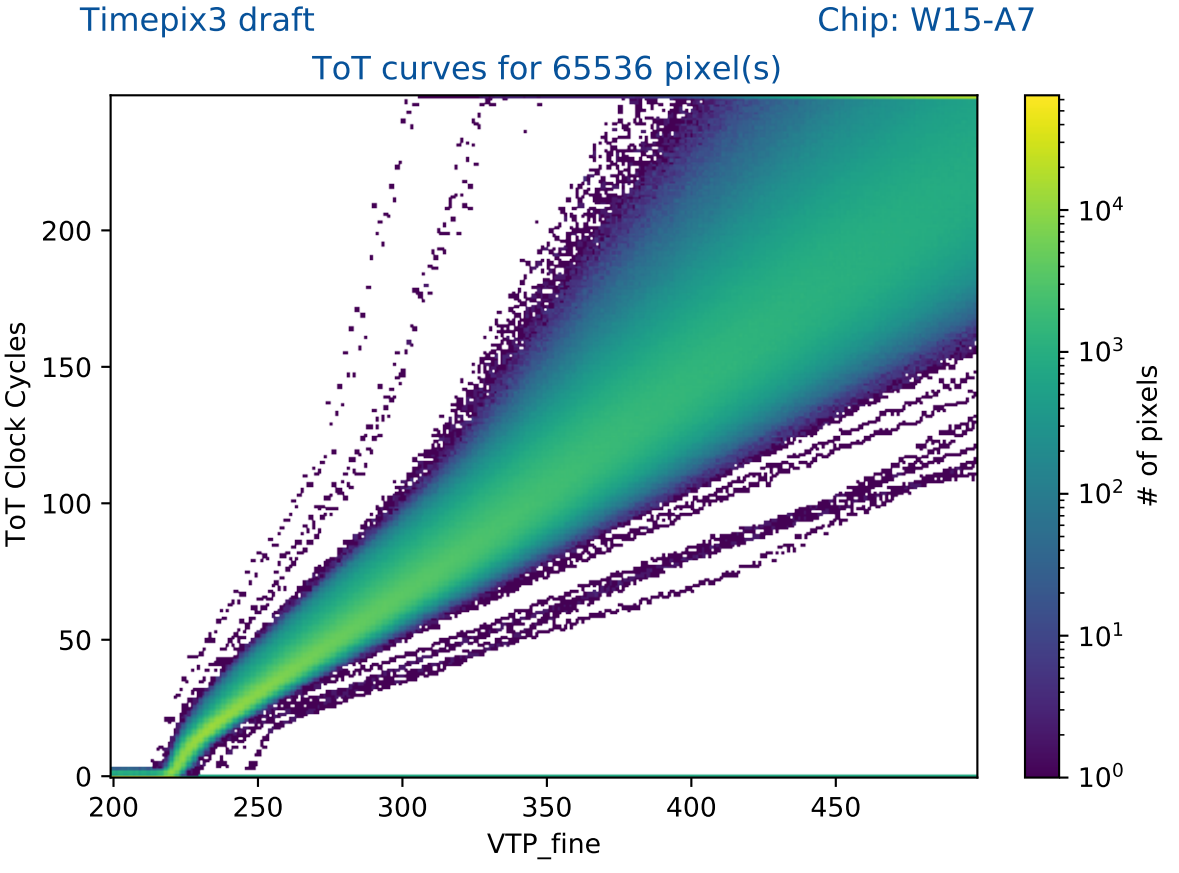
Software version: 0.9.0
Readout board: FECv6
Firmware version: 3

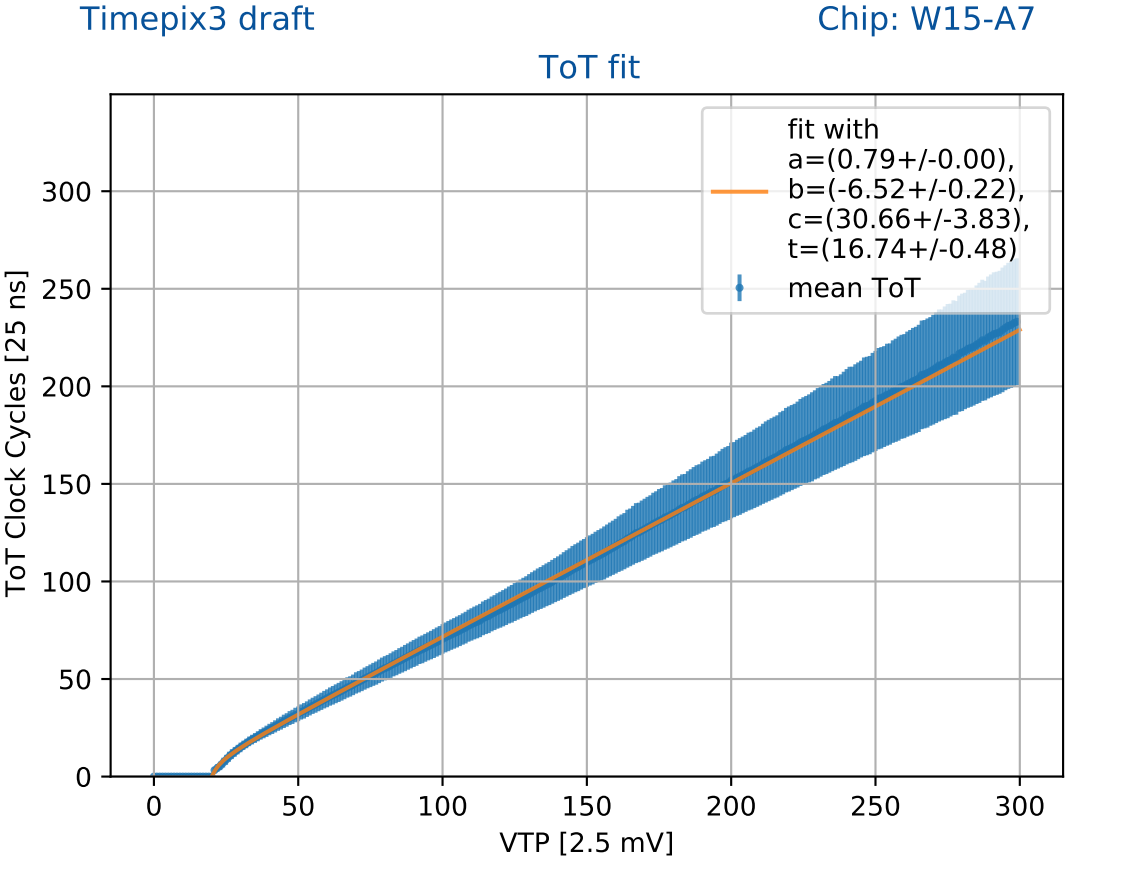
Have a good day!

Equalisation:
/home/tpc/Timepix3/equalisations/W15-A7_equal_2022-05-17_11-04-00.h5
Maskfile:
/home/tpc/Timepix3/masks/W15-A7_mask_2022-02-18_21-23-34.h5


Timepix3 draft
Chip: W15-A7
Pixel threshold distribution
$\mu = 7.4 \pm 0.1$
$\sigma = 3.0 \pm 0.1$
# of hits
Pixel threshold
0
2000
4000
6000
8000
0
2
4
6
8
10
12
14


Timepix3 draft
Chip: W15-A7
Hit curves for 65536 pixel(s)
Hits per pixel
VTP_fine
# of pixels
0
10
20
30
40
200
250
300
350
400
450
10^0
10^1
10^2
10^3
10^4


Timepix3 draft
Chip: W15-A7
ToT curves for 65536 pixel(s)
ToT Clock Cycles
VTP_fine
# of pixels
0
50
100
150
200
200
250
300
350
400
450
10^0
10^1
10^2
10^3
10^4


Timepix3 draft
Chip: W15-A7
ToT fit
fit with
a=(0.79+/-0.00),
b=(-6.52+/-0.22),
c=(30.66+/-3.83),
t=(16.74+/-0.48)
mean ToT
ToT Clock Cycles [25 ns]
VTP [2.5 mV]
0
50
100
150
200
250
300