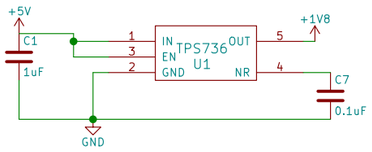
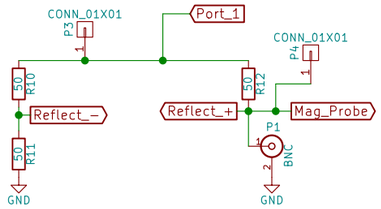
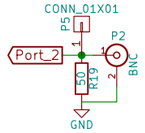
Used to serve as a 1V8 power source
for some of the devices

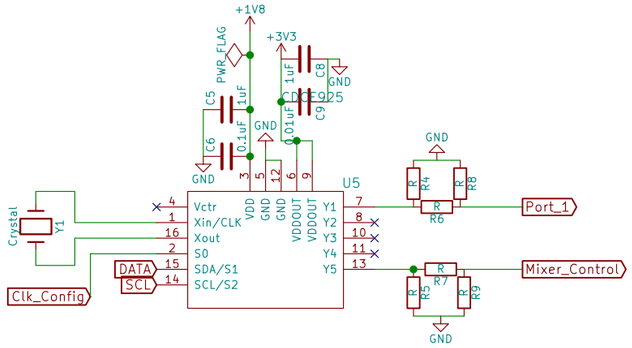


Sensor probes, used to measure the reflection coefficients.





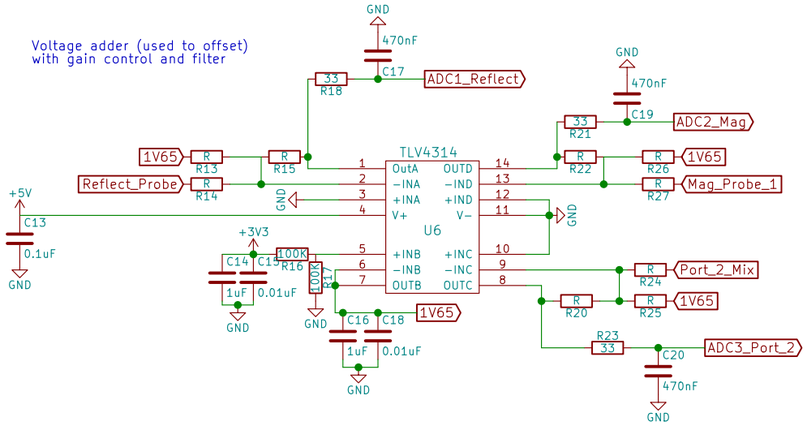
Used to generate two clock signals with a
frequency offset between them (with unlabeled
attenuator to control output power).



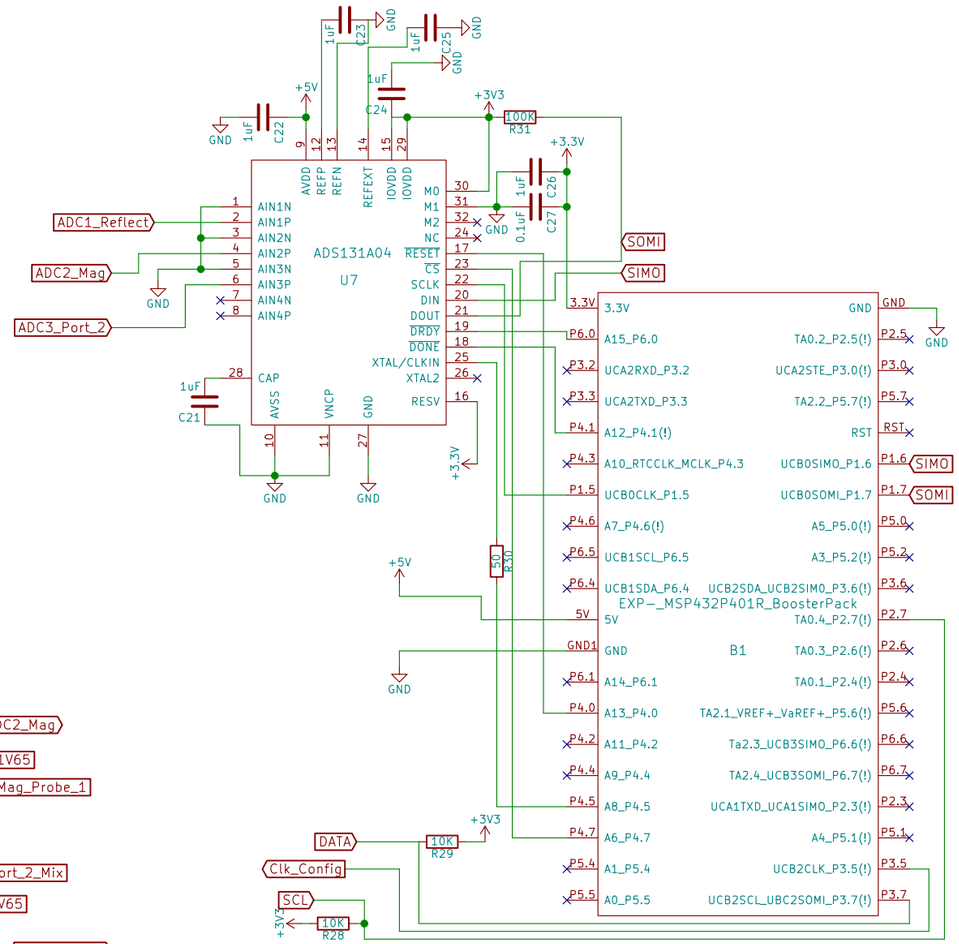
Using SA612 for differential input mixer and to power filter

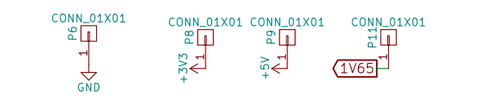
Current setup does not have the components in the
seperate schematic files

Voltage adder (used to offset)
with gain control and filter



Op−Amp used to create a voltage regulator for 1V65 out and
a buffer before the second order filter, which can also be
used to adjust gain from the prior stage.





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Title: Superheterodyne VNA

Size: A3	Date: 2016−05−01	**Rev:**
KiCad E.D.A. kicad 4.0.1−stable		Id: 1/1