

JP1 - Flash ROM A18 Select Open: A18=0 (default); Closed: A18=1

JP5, JP6: 1-2: NTSC; 2-3: PAL (Use wire links instead of jumpers) [Chip Select] [Slot Direction]

				Lice
			Rev:	
A3	Date:	1/1	File: Ultra-MSX.kicad_pcb	
d E.D.A.	kicad (6.0.5)			
	5		6	