1.2 V Micropower, Precision Shunt Voltage Reference

AD1580

FEATURES

Wide operating range: 50 µA to 10 mA
Initial accuracy: ±0.1% maximum
Temperature drift: ±50 ppm/°C maximum
Output impedance: 0.5 Ω maximum
Wideband noise (10 Hz to 10 kHz): 20 µV rms
Operating temperature range: −40°C to +85°C
High ESD rating
    4 kV human body model
    400 V machine model
Compact, surface-mount SOT-23 and SC70 packages

APPLICATIONS

Portable, battery-powered equipment
    Cellular phones, notebook computers, PDAs, GPSs,
    and DMMs
Computer workstations
    Suitable for use with a wide range of video RAMDACs
Smart industrial transmitters
PCMCIA cards
Automotive
3 V/5 V, 8-bit to 12-bit data converters

GENERAL DESCRIPTION

The AD1580[1] is a low cost, 2-terminal (shunt), precision band gap reference. It provides an accurate 1.225 V output for input currents between 50 µA and 10 mA.

The superior accuracy and stability of the AD1580 is made possible by the precise matching and thermal tracking of on-chip components. Proprietary curvature correction design techniques have been used to minimize the nonlinearities in the voltage output temperature characteristics. The AD1580 is stable with any value of capacitive load.

The low minimum operating current makes the AD1580 ideal for use in battery-powered 3 V or 5 V systems. However, the wide operating current range means that the AD1580 is extremely versatile and suitable for use in a wide variety of high current applications.

The AD1580 is available in two grades, A and B, both of which are provided in the SOT-23 and SC70 packages, the smallest surface-mount packages available. Both grades are specified over the industrial temperature range of −40°C to +85°C.
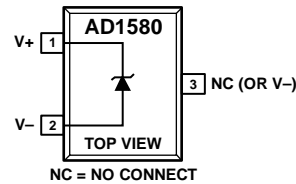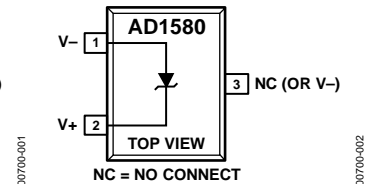
PIN CONFIGURATIONS
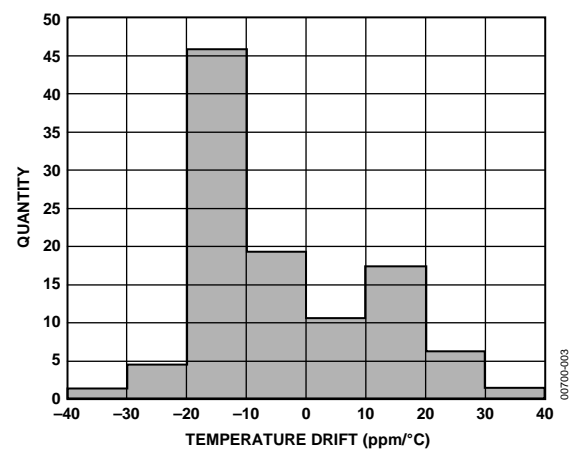

Figure 1. SOT-23

Figure 2. SC70

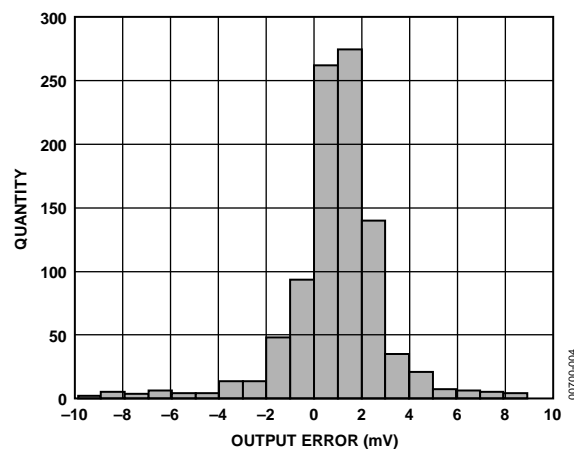*Figure 3. Reverse Voltage Temperature Drift Distribution*

Figure 4. Reverse Voltage Error Distribution

---

[1] Protected by U.S. Patent No. 5,969,657.

Rev. F

AD1580

TABLE OF CONTENTS

REVISION HISTORY

SPECIFICATIONS

$T_A = 25°C$, $I_{IN} = 100$ μA, unless otherwise noted.

Table 1.

Model	AD1580A			AD1580B			Unit
	Min	Typ	Max	Min	Typ	Max	
REVERSE VOLTAGE OUTPUT (SOT-23)	1.215	1.225	1.235	1.224	1.225	1.226	V
REVERSE VOLTAGE OUTPUT (SC70)				1.2225	1.225	1.2275	V
REVERSE VOLTAGE TEMPERATURE DRIFT							
−40°C to +85°C			100			50	ppm/°C
MINIMUM OPERATING CURRENT, T_{MIN} to T_{MAX}			50			50	μA
REVERSE VOLTAGE CHANGE WITH REVERSE CURRENT							
50 μA < I_{IN} < 10 mA, T_{MIN} to T_{MAX}		2.5	6		2.5	6	mV
50 μA < I_{IN} < 1 mA, T_{MIN} to T_{MAX}		0.5			0.5		mV
DYNAMIC OUTPUT IMPEDANCE ($\Delta V_R/\Delta I_R$)							
I_{IN} = 1 mA ± 100 μA (f = 120 Hz)		0.4	1		0.4	0.5	Ω
OUTPUT NOISE							
RMS Noise Voltage: 10 Hz to 10 kHz		20			20		μV rms
Low Frequency Noise Voltage: 0.1 Hz to 10 Hz		5			5		μV p-p
TURN-ON SETTLING TIME TO 0.1%[1]		5			5		μs
OUTPUT VOLTAGE HYSTERESIS[2]		80			80		μV
TEMPERATURE RANGE							
Specified Performance, T_{MIN} to T_{MAX}	−40		+85	−40		+85	°C
Operating Range[3]	−55		+125	−55		+125	°C

[1] Measured with no load capacitor.
[2] Output hysteresis is defined as the change in the +25°C output voltage after a temperature excursion to +85°C and then to −40°C.
[3] The operating temperature range is defined as the temperature extremes at which the device continues to function. Parts may deviate from their specified performance.

ABSOLUTE MAXIMUM RATINGS

Table 2.

Parameter	Rating
Reverse Current	25 mA
Forward Current	20 mA
Internal Power Dissipation[1]	
SOT-23 (RT)	0.3 W
Storage Temperature Range	−65°C to +150°C
Operating Temperature Range	
AD1580/RT	−55°C to +125°C
Lead Temperature, Soldering	
Vapor Phase (60 sec)	215°C
Infrared (15 sec)	220°C
ESD Susceptibility[2]	
Human Body Model	4 kV
Machine Model	400 V

[1] Specification is for device in free air at 25°C, SOT-23 package. θ_{JA} = 300°C/W.
[2] The human body model is a 100 pF capacitor discharged through 1.5 kΩ. For the machine model, a 200 pF capacitor is discharged directly into the device.

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.
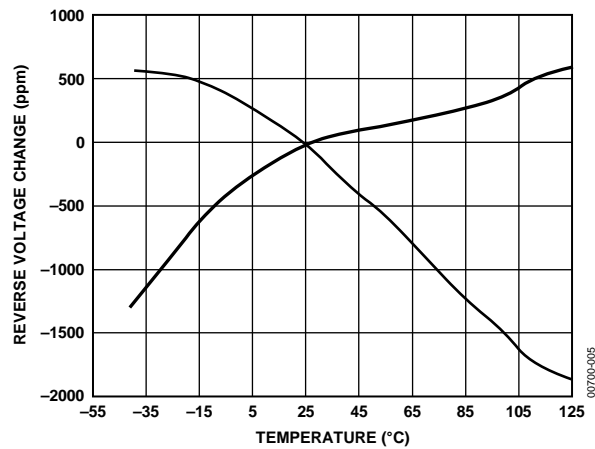
TYPICAL PERFORMANCE CHARACTERISTICS

Figure 5. Output Drift for Different Temperature Characteristics
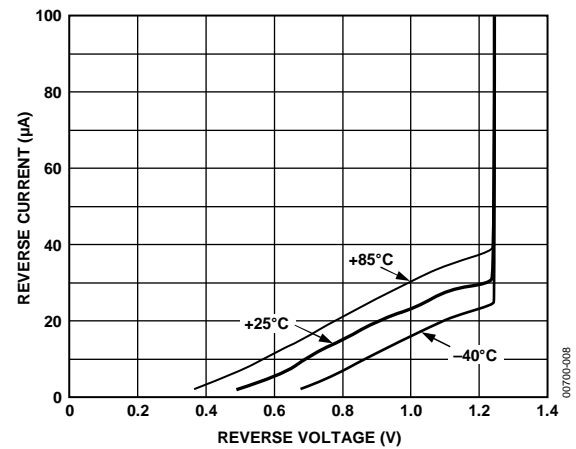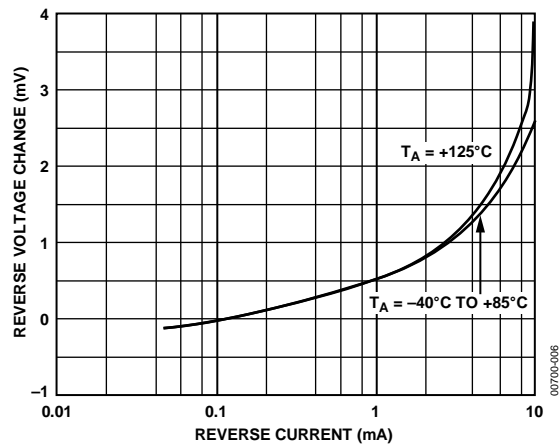

Figure 8. Reverse Current vs. Reverse Voltage

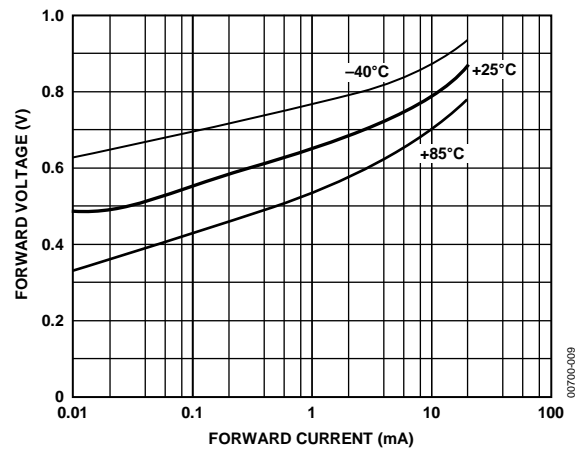Figure 6. Reverse Voltage Change vs. Reverse Current

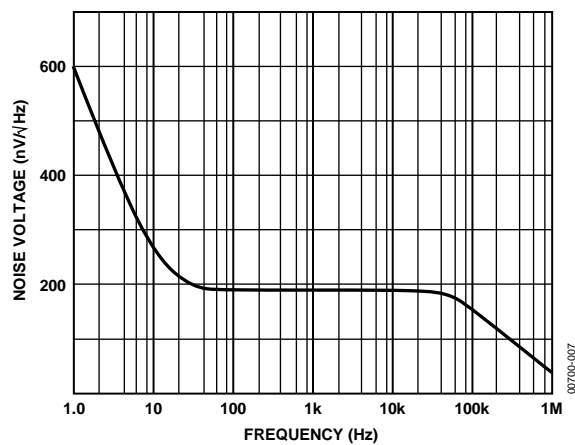Figure 9. Forward Voltage vs. Forward Current

Figure 7. Noise Spectral Density

THEORY OF OPERATION

The AD1580 uses the band gap concept to produce a stable, low temperature coefficient voltage reference suitable for high accuracy data acquisition components and systems. The device makes use of the underlying physical nature of a silicon transistor base emitter voltage in the forward biased operating region. All such transistors have an approximately −2 mV/°C temperature coefficient (TC), which is unsuitable for use directly as a low TC reference; however, extrapolation of the temperature characteristic of any one of these devices to absolute zero (with collector current proportional to absolute temperature) reveals that its V_{BE} goes to approximately the silicon band gap voltage. Thus, if a voltage could be developed with an opposing temperature coefficient to sum with V_{BE}, a zero TC reference would result. The AD1580 circuit in Figure 10 provides such a compensating voltage, V1, by driving two transistors at different current densities and amplifying the resultant V_{BE} difference (ΔV_{BE}, which has a positive TC). The sum of V_{BE} and V1 provides a stable voltage reference.
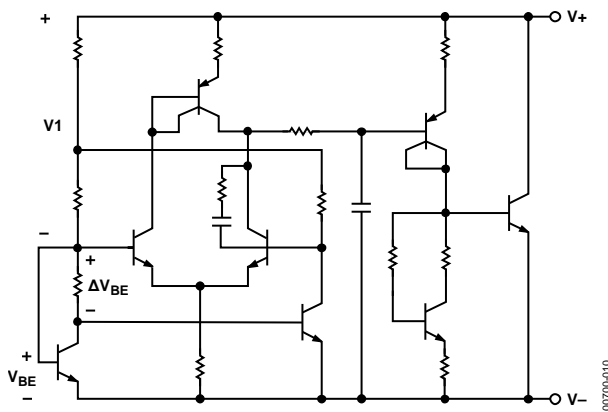

Figure 10. Schematic Diagram

APPLYING THE AD1580

The AD1580 is simple to use in virtually all applications. To operate the AD1580 as a conventional shunt regulator (see Figure 11), an external series resistor is connected between the supply voltage and the AD1580. For a given supply voltage, the series resistor, R_S, determines the reverse current flowing through the AD1580. The value of R_S must be chosen to accommodate the expected variations of the supply voltage, V_S; load current, I_L; and the AD1580 reverse voltage, V_R; while maintaining an acceptable reverse current, I_R, through the AD1580.

The minimum value for R_S should be chosen when V_S is at its minimum and I_L and V_R are at their maximum, while maintaining the minimum acceptable reverse current.

The value of R_S should be large enough to limit I_R to 10 mA when V_S is at its maximum and I_L and V_R are at their minimum.

The equation for selecting R_S is as follows:

$$R_S = (V_S - V_R)/(I_R + I_L)$$

Figure 12 shows a typical connection of the AD1580BRT operating at a minimum of 100 μA. This connection can provide ±1 mA to the load while accommodating ±10% power supply variations.
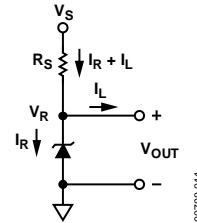

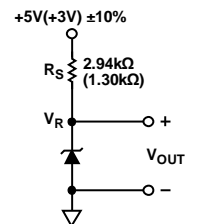Figure 11. Typical Connection Diagram

Figure 12. Typical Connection Diagram

TEMPERATURE PERFORMANCE

The AD1580 is designed for reference applications where stable temperature performance is important. Extensive temperature testing and characterization ensure that the device's performance is maintained over the specified temperature range.

Some confusion exists in the area of defining and specifying reference voltage error over temperature. Historically, references have been characterized using a maximum deviation per degree Celsius, for example, 50 ppm/°C. However, because of nonlinearities in temperature characteristics that originated in standard Zener references (such as S type characteristics), most manufacturers now use a maximum limit error band approach to specify devices. This technique involves the measurement of the output at three or more different temperatures to guarantee that the voltage falls within the given error band. The proprietary curvature correction design techniques used to minimize the AD1580 nonlinearities allow the temperature performance to be guaranteed using the maximum deviation method. This method is of more use to a designer than the one that simply guarantees the maximum error band over the entire temperature change.

Figure 13 shows a typical output voltage drift for the AD1580 and illustrates the methodology. The maximum slope of the two diagonals drawn from the initial output value at +25°C to the output values at +85°C and −40°C determines the performance grade of the device. For a given grade of the AD1580, the designer can easily determine the maximum total error from the initial tolerance plus temperature variation.
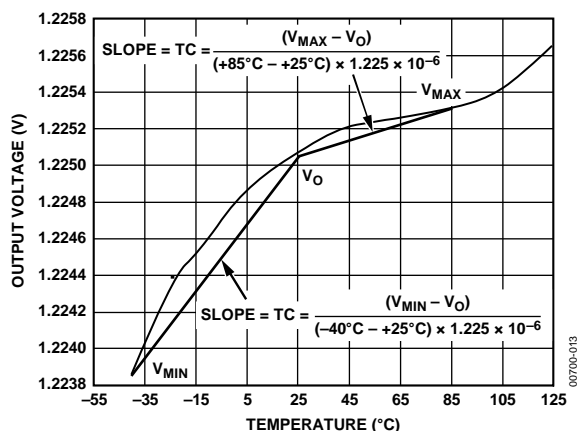
Figure 13. Output Voltage vs. Temperature

For example, the AD1580BRT initial tolerance is ±1 mV; a ±50 ppm/°C temperature coefficient corresponds to an error band of ±4 mV ($50 \times 10^{-6} \times 1.225$ V × 65°C). Thus, the unit is guaranteed to be 1.225 V ± 5 mV over the operating temperature range.

Duplication of these results requires a combination of high accuracy and stable temperature control in a test system. Evaluation of the AD1580 produces a curve similar to that in Figure 5 and Figure 13.

VOLTAGE OUTPUT NONLINEARITY vs. TEMPERATURE

When a reference is used with data converters, it is important to understand how temperature drift affects the overall converter performance. The nonlinearity of the reference output drift represents an additional error that is not easily calibrated out of the system. This characteristic (see Figure 14) is generated by normalizing the measured drift characteristic to the end point average drift. The residual drift error of approximately 500 ppm shows that the AD1580 is compatible with systems that require 10-bit accurate temperature performance.
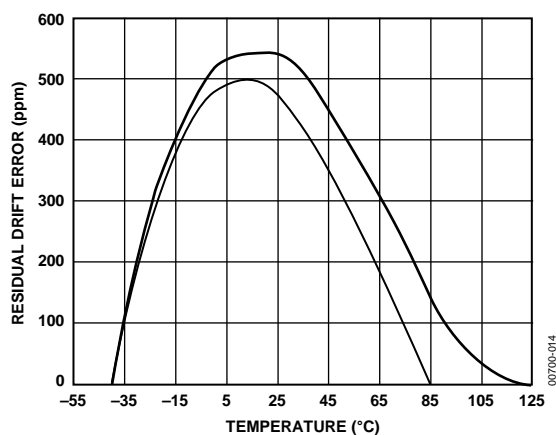

Figure 14. Residual Drift Error

REVERSE VOLTAGE HYSTERESIS

A major requirement for high performance industrial equipment manufacturers is a consistent output voltage at nominal temperature following operation over the operating temperature range. This characteristic is generated by measuring the difference between the output voltage at +25°C after operation at +85°C and the output, at +25°C after operation at −40°C. Figure 15 displays the hysteresis associated with the AD1580. This characteristic exists in all references and has been minimized in the AD1580.
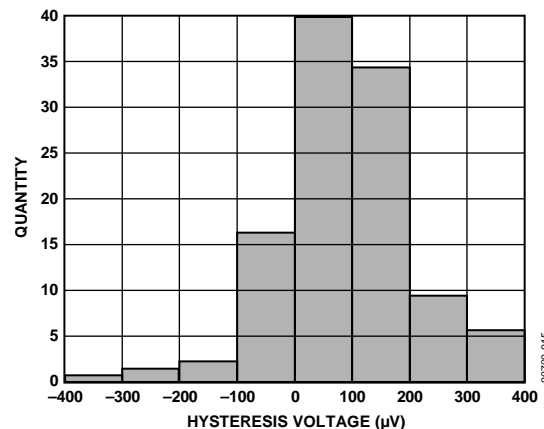

Figure 15. Reverse Voltage Hysteresis Distribution

OUTPUT IMPEDANCE vs. FREQUENCY

Understanding the effect of the reverse dynamic output impedance in a practical application may be important to successfully apply the AD1580. A voltage divider is formed by the AD1580 output impedance and the external source impedance. When an external source resistor of about 30 kΩ (I_R = 100 μA) is used, 1% of the noise from a 100 kHz switching power supply is developed at the output of the AD1580. Figure 16 shows how a 1 μF load capacitor connected directly across the AD1580 reduces the effect of power supply noise to less than 0.01%.
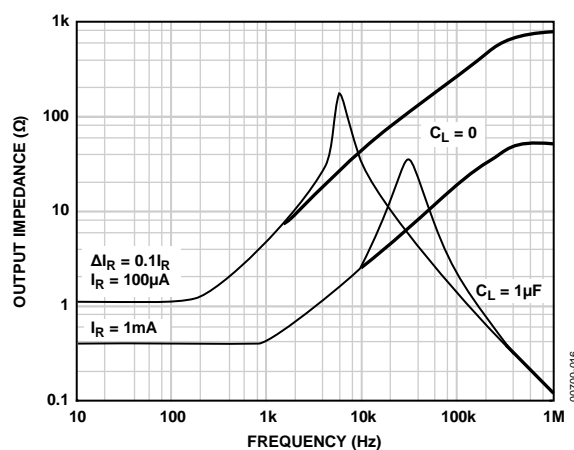

Figure 16. Output Impedance vs. Frequency

AD1580

NOISE PERFORMANCE AND REDUCTION

The noise generated by the AD1580 is typically less than 5 µV p-p over the 0.1 Hz to 10 Hz band. Figure 17 shows the 0.1 Hz to 10 Hz noise of a typical AD1580. Noise in a 10 Hz to 10 kHz bandwidth is approximately 20 µV rms (see Figure 18a). If further noise reduction is desired, a 1-pole low-pass filter can be added between the output pin and ground. A time constant of 0.2 ms has a −3 dB point at about 800 Hz and reduces the high frequency noise to about 6.5 µV rms (see Figure 18b). A time constant of 960 ms has a −3 dB point at 165 Hz and reduces the high frequency noise to about 2.9 µV rms (see Figure 18c).
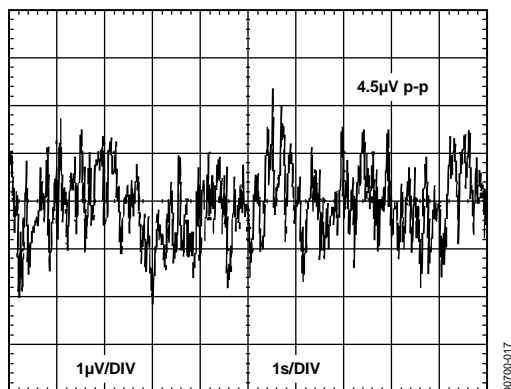

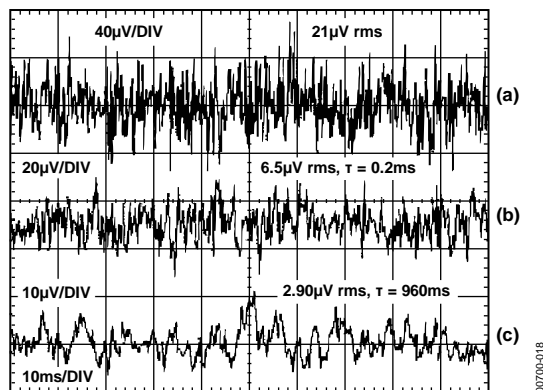*Figure 17. 0.1 Hz to 10 Hz Voltage Noise*

Figure 18. Total RMS Noise

TURN-ON TIME

Many low power instrument manufacturers are becoming increasingly concerned with the turn-on characteristics of components being used in their systems. Fast turn-on components often enable the end user to keep power off when not needed, and yet those components respond quickly when the power is turned on for operation. Figure 19 displays the turn-on characteristic of the AD1580.

Upon application of power (cold start), the time required for the output voltage to reach its final value within a specified error is the turn-on settling time. Two components normally associated with this are time for active circuits to settle and time for thermal gradients on the chip to stabilize. This characteristic is generated from cold start operation and represents the true turn-on waveform after power-up. Figure 21 shows both the coarse and fine turn-on settling characteristics of the device; the total settling time to within 1.0 mV is about 6 µs, and there is no long thermal tail when the horizontal scale is expanded to 2 ms/div.
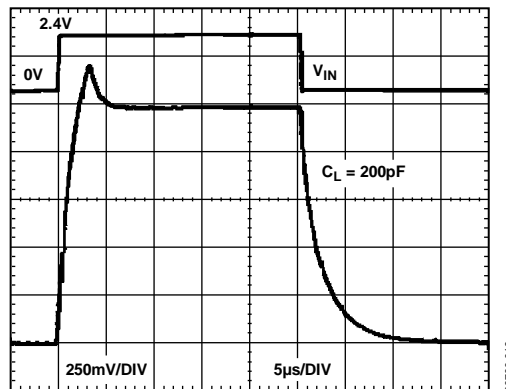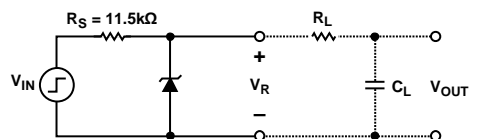

Figure 19. Turn-On Response Time

Figure 20. Turn-On, Settling, and Transient Test Circuit

Output turn-on time is modified when an external noise reduction filter is used. When present, the time constant of the filter dominates overall settling.
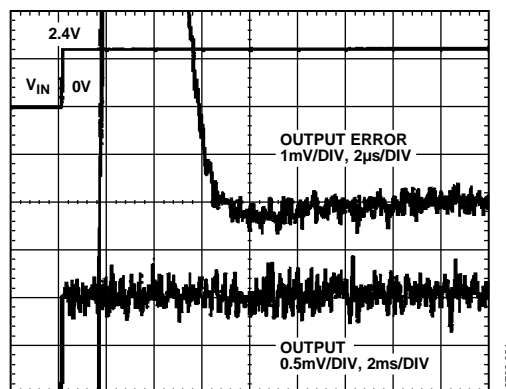

Figure 21. Turn-On Settling

TRANSIENT RESPONSE

Many ADC and DAC converters present transient current loads to the reference. Poor reference response can degrade the converter's performance.

Figure 22 displays both the coarse and fine settling characteristics of the device to load transients of ±50 μA.
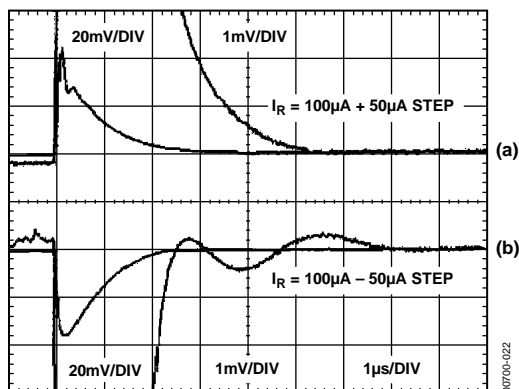

Figure 22. Transient Settling

Figure 22a shows the settling characteristics of the device for an increased reverse current of 50 μA. Figure 22b shows the response when the reverse current is decreased by 50 μA. The transients settle to 1 mV in about 3 μs.

Attempts to drive a large capacitive load (in excess of 1000 pF) may result in ringing, as shown in the step response (see Figure 23). This is due to the additional poles formed by the load capacitance and the output impedance of the reference. A recommended method of driving capacitive loads of this magnitude is shown in Figure 20. A resistor isolates the capacitive load from the output stage, while the capacitor provides a single-pole low-pass filter and lowers the output noise.
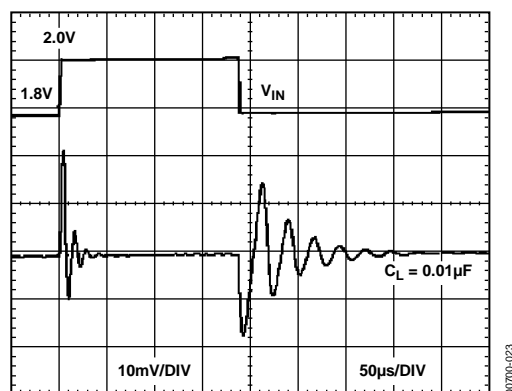

Figure 23. Transient Response with Capacitive Load

PRECISION MICROPOWER LOW DROPOUT REFERENCE

The circuit in Figure 24 provides an ideal solution for making a stable voltage reference with low standby power consumption, low input/output dropout capability, and minimum noise output. The amplifier both buffers and optionally scales up the AD1580 output voltage, V_R. Output voltages as high as 2.1 V can supply 1 mA of load current. A one-pole filter connected between the AD1580 and the OP193 input can be used to achieve low output noise. The nominal quiescent power consumption is 200 μW.

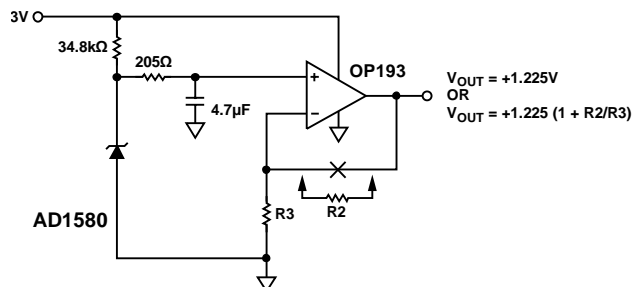Figure 24. Micropower Buffered Reference

USING THE AD1580 WITH 3 V DATA CONVERTERS

The AD1580 low output drift (50 ppm/°C) and compact subminiature SOT-23 package make it ideally suited for today's high performance converters in space critical applications.

One family of ADCs for which the AD1580 is well suited is the AD7714-3 and AD7715-3. The AD7714/AD7715 are charge-balancing (ΣΔ) ADCs with on-chip digital filtering intended for the measurement of wide dynamic range, low frequency signals such as those representing chemical, physical, or biological processes. Figure 25 shows the AD1580 connected to the AD7714-3/AD7715-3 for 3 V operation.
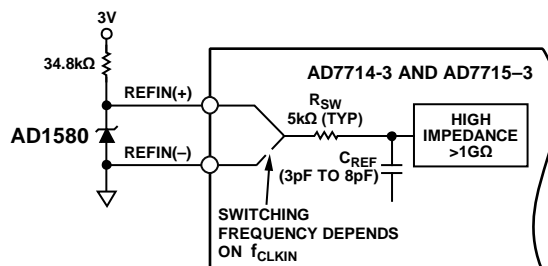

Figure 25. Reference Circuit for the AD7714-3 and AD7715-3

AD1580

The AD1580 is ideal for creating the reference level to use with 12-bit multiplying DACs, such as the AD7943, AD7945, and AD7948. In the single-supply bias mode (see Figure 26), the impedance seen looking into the I_{OUT2} terminal changes with DAC code. If the AD1580 drives I_{OUT2} and AGND directly, less than 0.2 LSBs of additional linearity error results. The buffer amp eliminates any linearity degradation that could result from variations in the reference level.

Figure 26. Single-Supply System
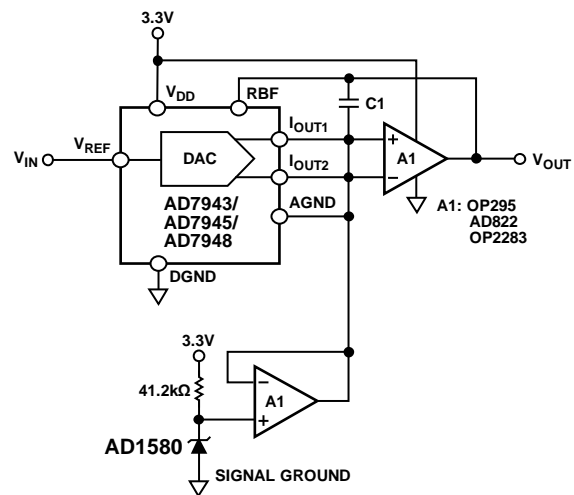
OUTLINE DIMENSIONS
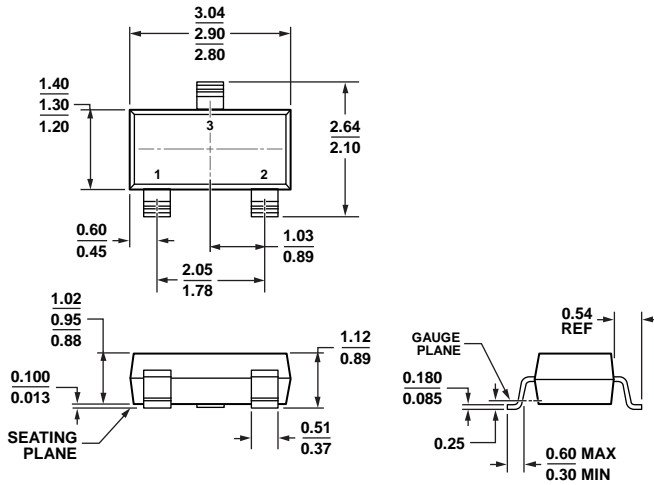
COMPLIANT TO JEDEC STANDARDS TO-236-AB

Figure 27. 3-Lead Small Outline Transistor Package [SOT-23-3]
(RT-3)
Dimensions shown in millimeters
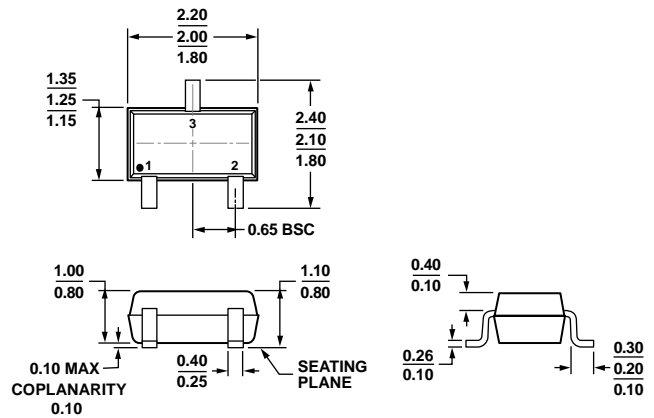
ALL DIMENSIONS COMPLIANT WITH EIAJ SC70

Figure 28. 3-Lead Thin Shrink Small Outline Transistor Package [SC70]
(KS-3)
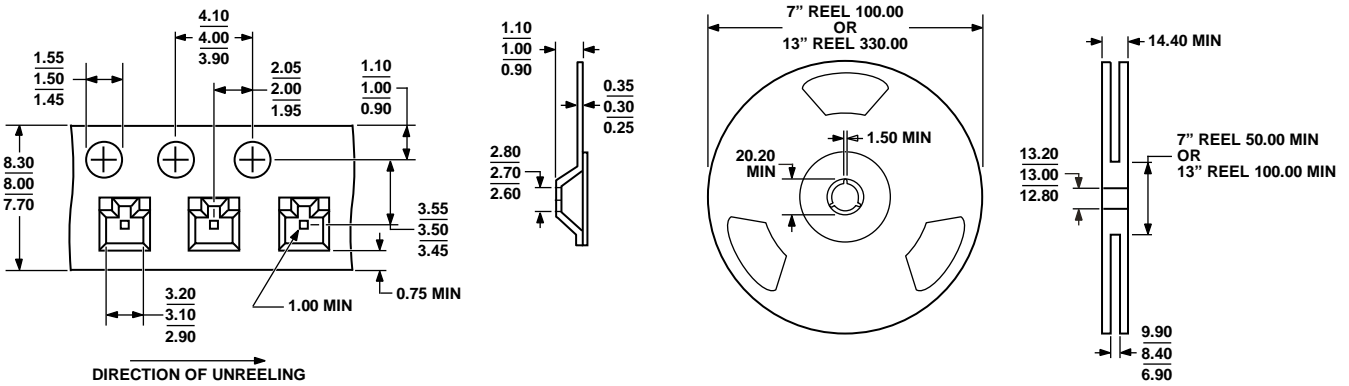*Dimensions shown in millimeters*

DIRECTION OF UNREELING

Figure 29. Tape and Reel Dimensions
(RT-3 and KS-3)
Dimensions shown in millimeters

AD1580

ORDERING GUIDE

Model[1]	Temperature Range	Initial Output Error	Temperature Coefficient	Package Description	Package Option	Branding
AD1580ART-REEL	−40°C to +85°C	10 mV	100 ppm/°C	3-Lead SOT-23-3	RT-3	0Axx
AD1580ARTZ-REEL	−40°C to +85°C	10 mV	100 ppm/°C	3-Lead SOT-23-3	RT-3	R0Y
AD1580ARTZ-REEL7	−40°C to +85°C	10 mV	100 ppm/°C	3-Lead SOT-23-3	RT-3	R0Y
AD1580BRT-REEL7	−40°C to +85°C	1 mV	50 ppm/°C	3-Lead SOT-23-3	RT-3	0Bxx
AD1580BRTZ-R2	−40°C to +85°C	1 mV	50 ppm/°C	3-Lead SOT-23-3	RT-3	R2E
AD1580BRTZ-REEL7	−40°C to +85°C	1 mV	50 ppm/°C	3-Lead SOT-23-3	RT-3	R2E
AD1580BKSZ-REEL	−40°C to +85°C	2.5 mV	50 ppm/°C	3-Lead SC70	KS-3	R2E
AD1580BKSZ-REEL7	−40°C to +85°C	2.5 mV	50 ppm/°C	3-Lead SC70	KS-3	R2E

[1] Z = RoHS Compliant Part.

PACKAGE BRANDING INFORMATION

In the SOT-23 package (RT), four marking fields identify the device generic, grade, and date of processing.

The first field is the product identifier. A 0 identifies the generic as the AD1580.

The second field indicates the device grade: A or B.

In the third field, a numeral or letter indicates a calendar year: 5 for 1995, A for 2001.

In the fourth field, letters A through Z represent a two-week window within the calendar year, starting with A for the first two weeks of January.

ANALOG DEVICES

www.analog.com

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Analog Devices Inc.](#):

[AD1580ARTZ-REEL7](#) [AD1580BKSZ-REEL](#) [AD1580BKSZ-REEL7](#) [AD1580BRTZ-REEL7](#) [AD1580BRT-REEL7](#)
[AD1580BRTZ-R2](#)