







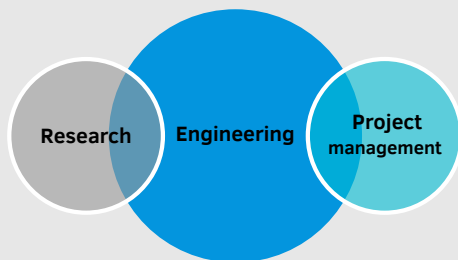
DORIAN BACHELOT

Student

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Skills

Overview



Details

• Software

Visual studio, Android studio, Wamp, Vim.

• Programming languages

C++, C, Java, Python, Bash, CMake, \LaTeX , PHP, Javascript, HTML, SQL, CSS.

• Other

Deep learning, OpenGL, Software reverse engineering, Hardware reverse engineering, Git, MariaDB/MySQL, Windows, Linux, Android.

Languages

Spanish : Limited working proficiency

A2

English : Full professional proficiency

TOEIC : 905

Availability


July 2020 - August 2020

I am a hardworking and ambitious individual with a great passion for cybersecurity research. I am currently studying digital sciences in an engineering school in France and I am also working on the research subject of hardware reverse engineering. I love learning new skills and sharing my knowledge with others.

Education

Engineering Degree


ESIEA, Laval, France

 2017 - now

Currently an engineering student at ESIEA, Graduate Engineering School in Digital Sciences and Technologies. I'm focusing my university curriculum on cybersecurity but I also work on neural networks (Creation of TigiNet : a C++ library for creating neural networks without dependency).

Baccalaureate

Réaumur, Laval, France


 2013 - 2017

Baccalaureate in scientific series with engineering sciences specialty and I.S.N. (Computer science and digital) option. With honors.

Experience

In charge person in a gas station

Total


 July 2018 - August 2018

Summer work in a Total gas station (managerial position).

Research

Research projects


Laval, France

 2019 - now

Research works in collaboration with a French researcher and a Russian laboratory on different subjects such as hardware reverse engineering, cryptography or template matching algorithms. Fork of Degate : software for reverse engineering of silicon chips in C++, and porting it for allowing multiplatform use.

Research projects - Laboratory (C + V)°

ESIEA, Laval, France

 October 2018 - June 2019

Worked on hardware reverse engineering and cryptography at the Laboratory of Cryptology and Operational Virology (C + V)°.

Publications

■ "Rétro-ingénierie matérielle : accéder au silicium – État de l'art des méthodes de décapsulations et de déstratifications de puce de silicium", *Hackable 31*, pp. 98-107, 2019

■ "Rétro-ingénierie matérielle : comprendre le silicium – État de l'art des méthodes d'analyses de puce de silicium", *Hackable 31*, pp. 108-114, 2019

■ "Le coût de la rétro-ingénierie du silicium – Matériel nécessaire et risques associés", *Hackable 32*, pp. 106-115, 2019

■ "Se protéger de la rétro-ingénierie matérielle – État de l'art des méthodes de protection existantes", *Hackable 33*, pp. 68-83, 2020

Hobbies

-  Programming
-  Research
-  Rugby
-  Travel
-  Electronic
-  Astronomy