

COMP303 - Computer Architecture

Homework 3: (return by **16.12.2018**).

PS: HWs submitted after the deadline will not be accepted

Email your HW to: msasongko17@ku.edu.tr and nizamettinaydin@gmail.com

You should place **CAr-hw3** in the "Subject" field.

You must name your file by using your matriculation number

(ie, **hw3-53745.asm**)

Implement the "**Booth's signed multiplication algorithm**" in MIPS32 assembler.
Data size of the signed integer is 32 bit.

You can use either **SPIM** or **MARS** to implement and test your programs.

Booth's multiplication algorithm is a multiplication algorithm that multiplies two signed binary numbers in two's complement notation.

Flowchart of Booth's Multiplier:


