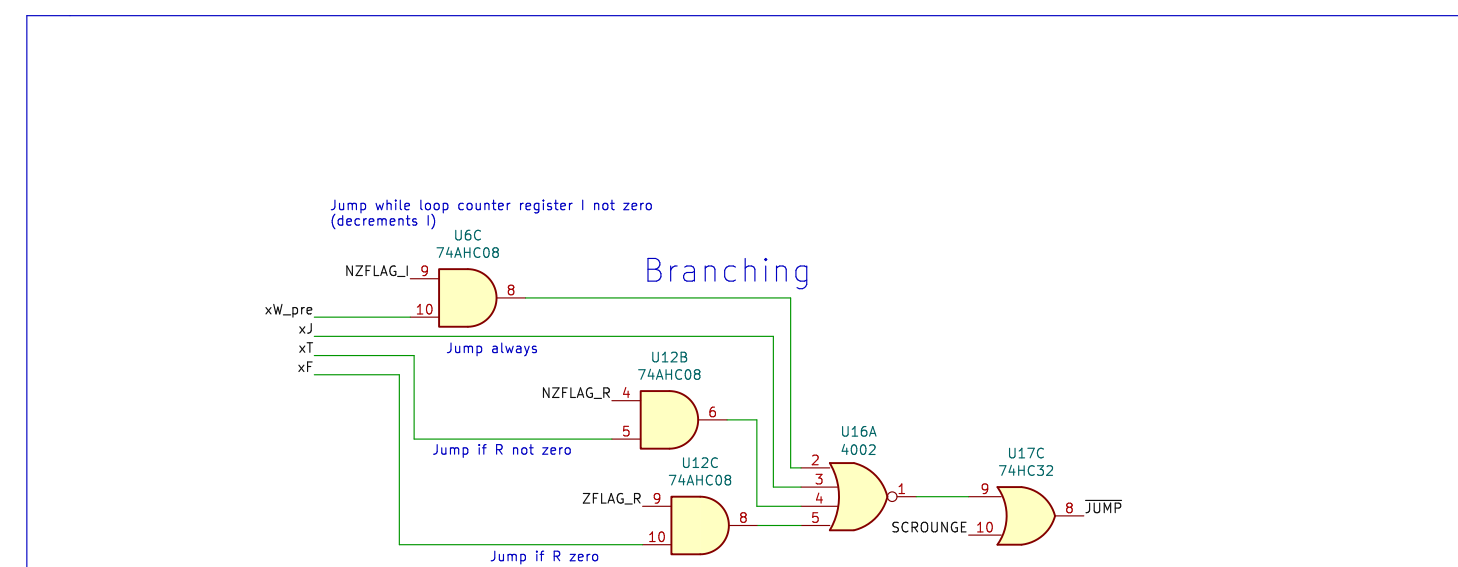
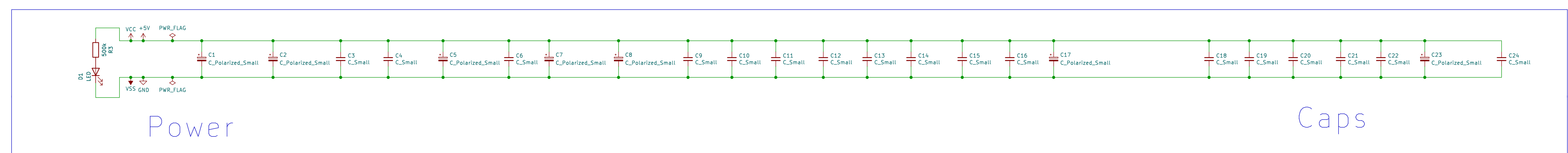


# Sonne-8 Microcontroller Reference Schematics

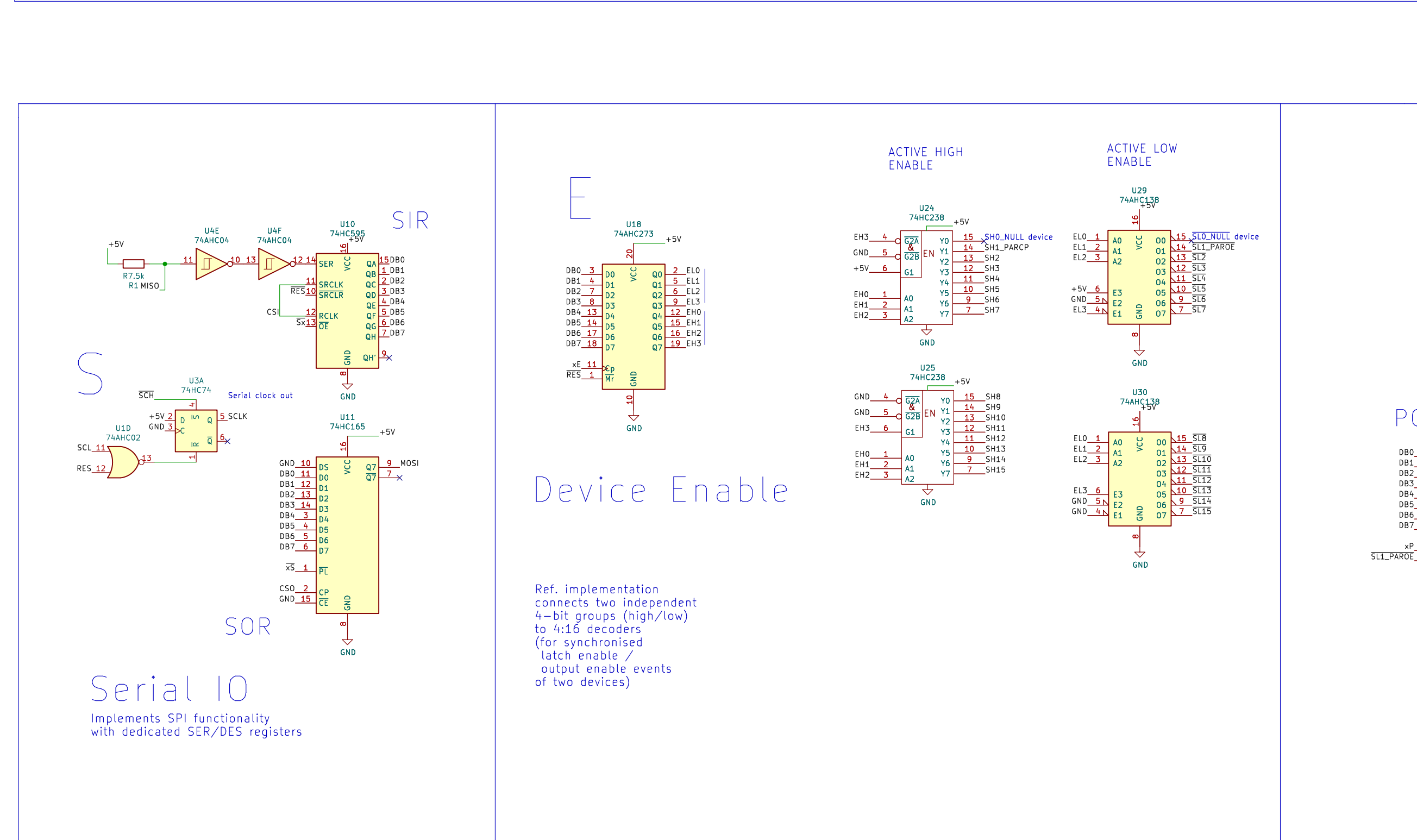
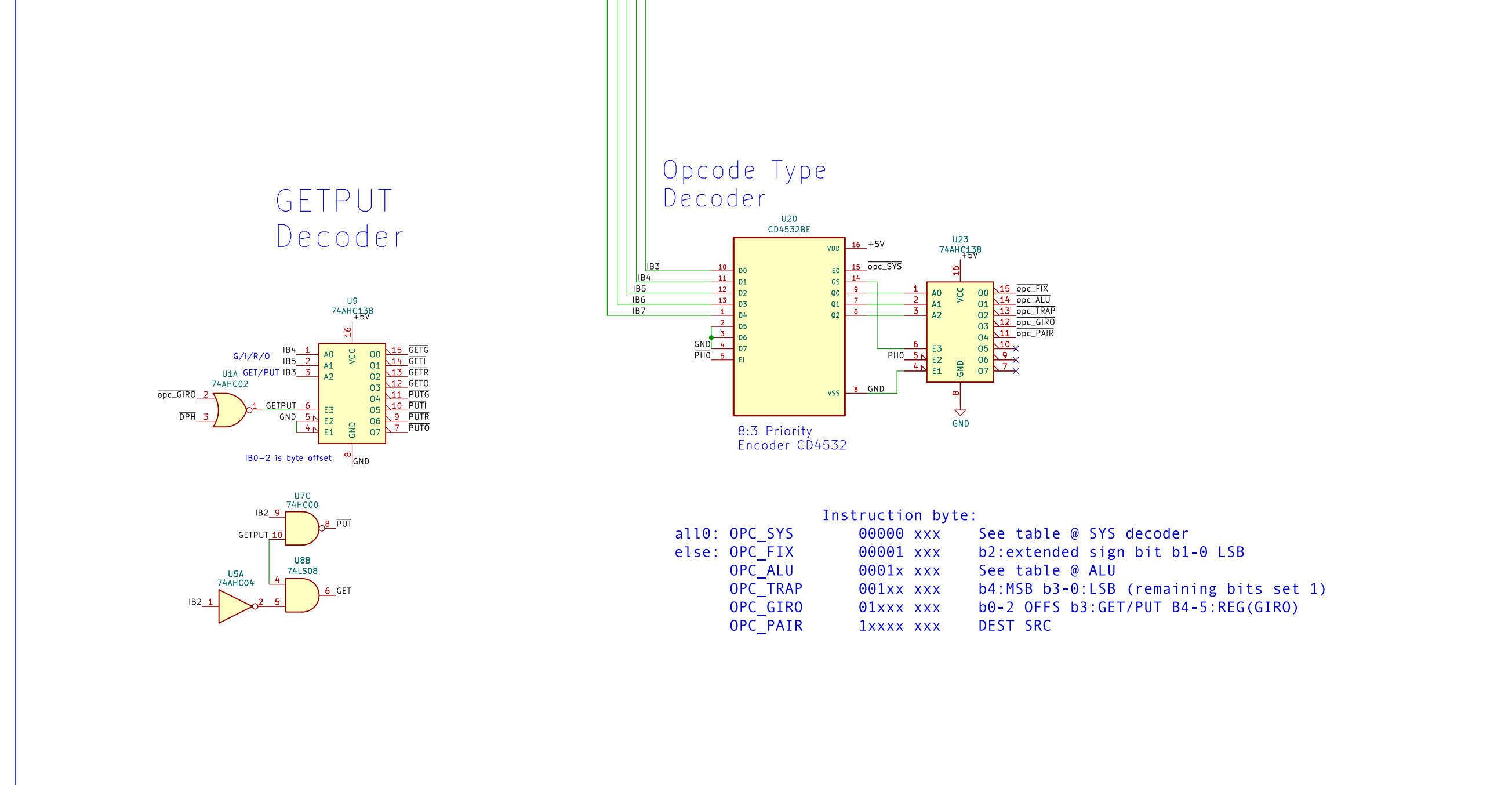
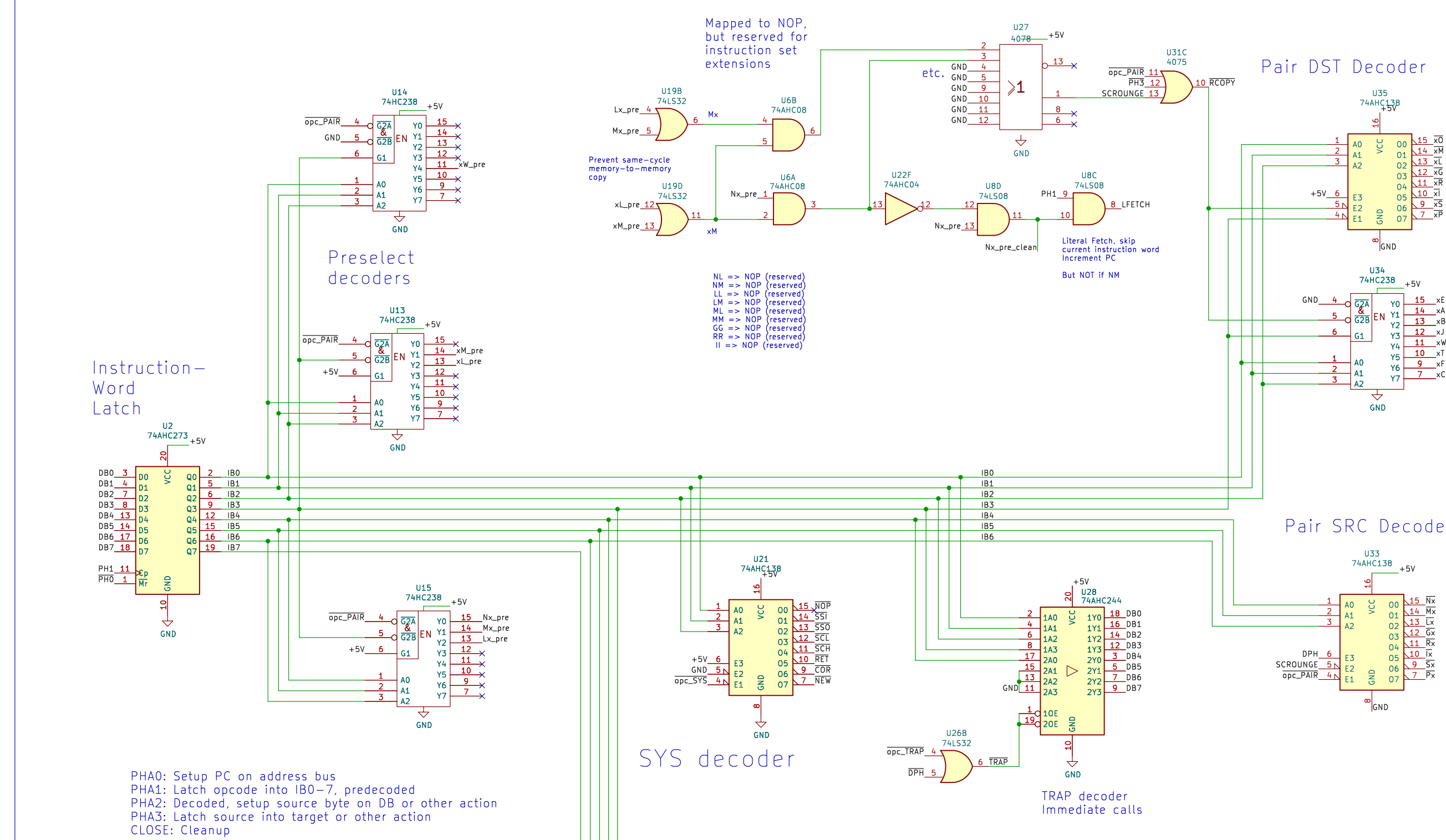
Rev. Myth/LOX



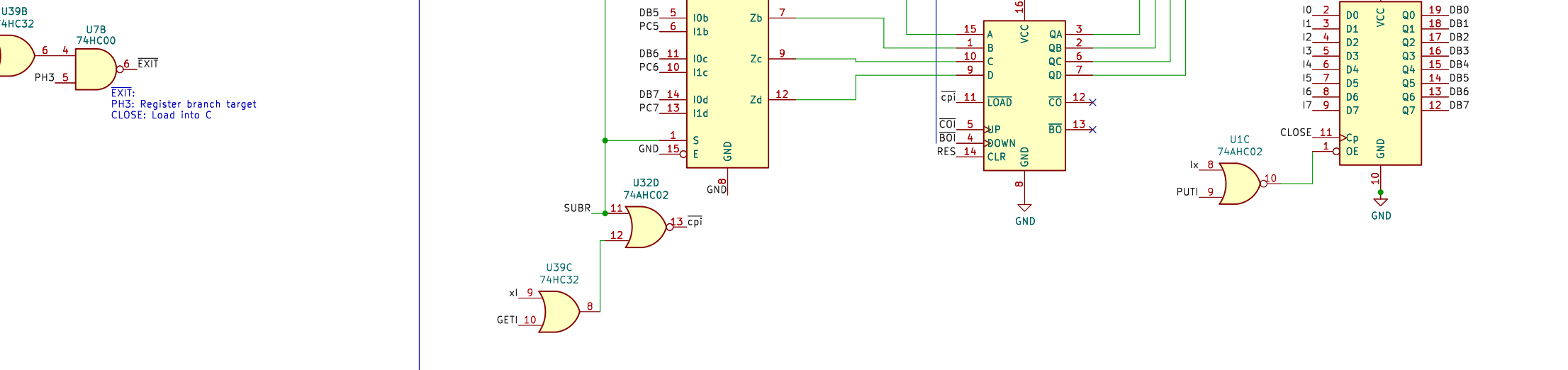
## Instruction Decoder

Scrounger

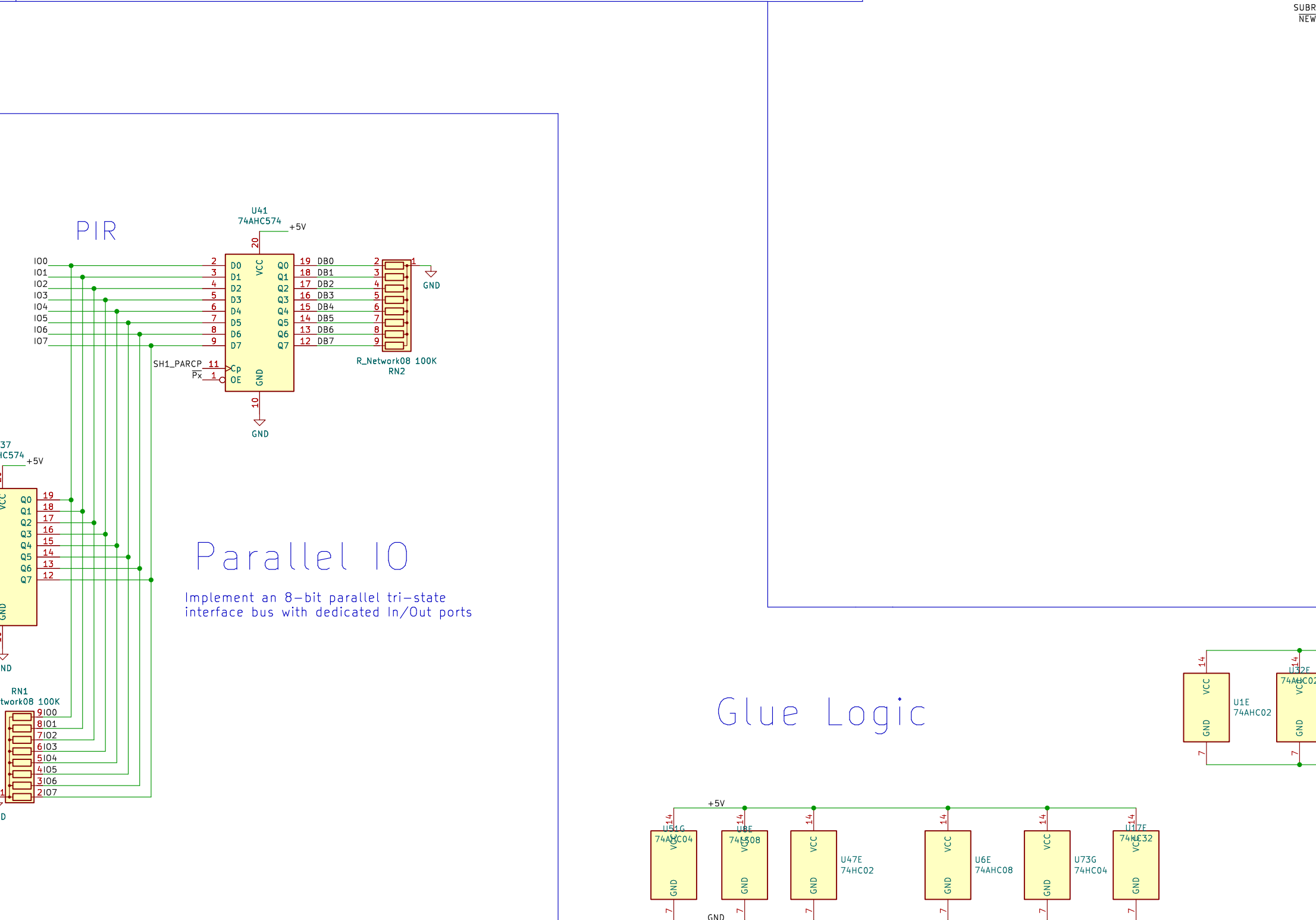
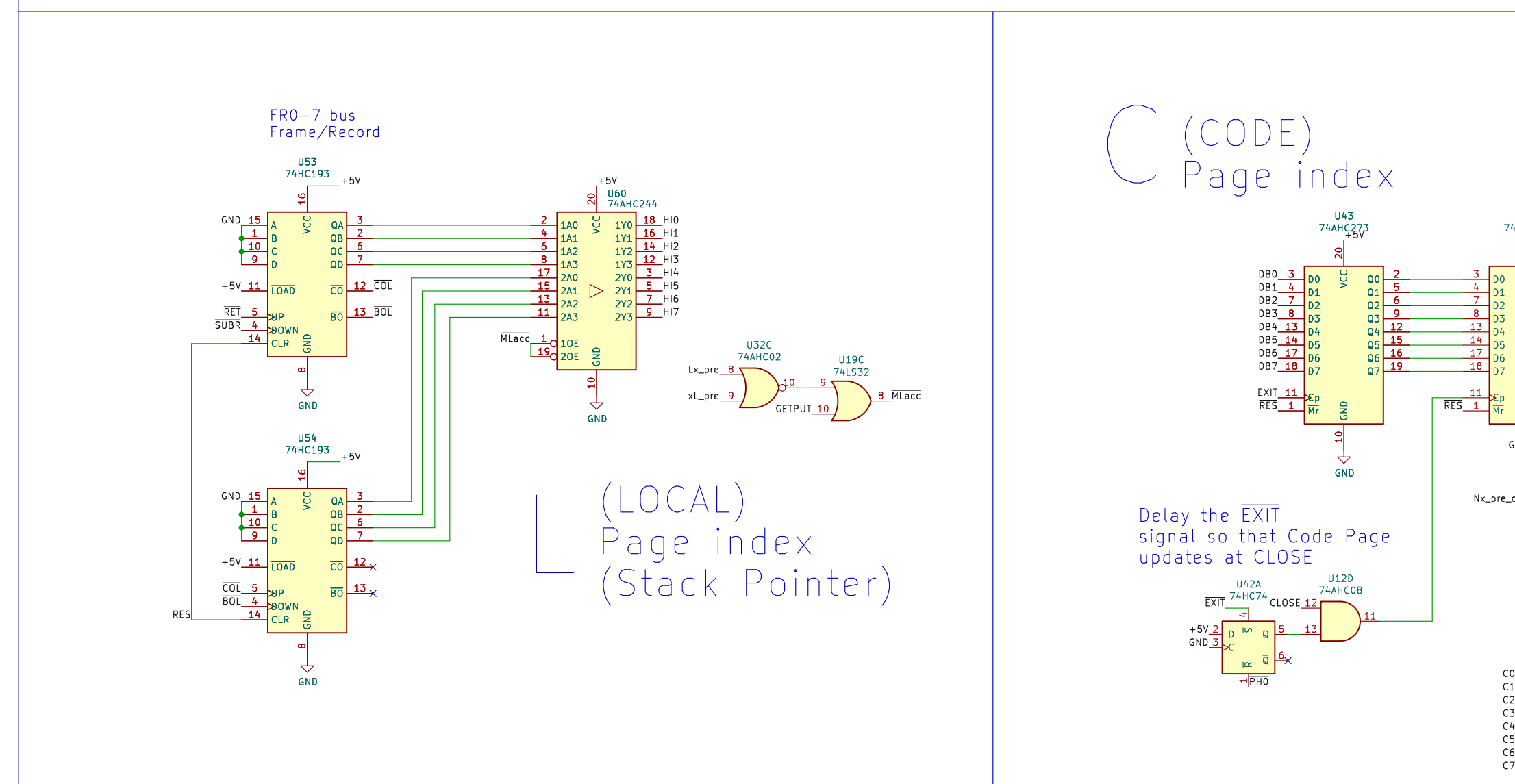
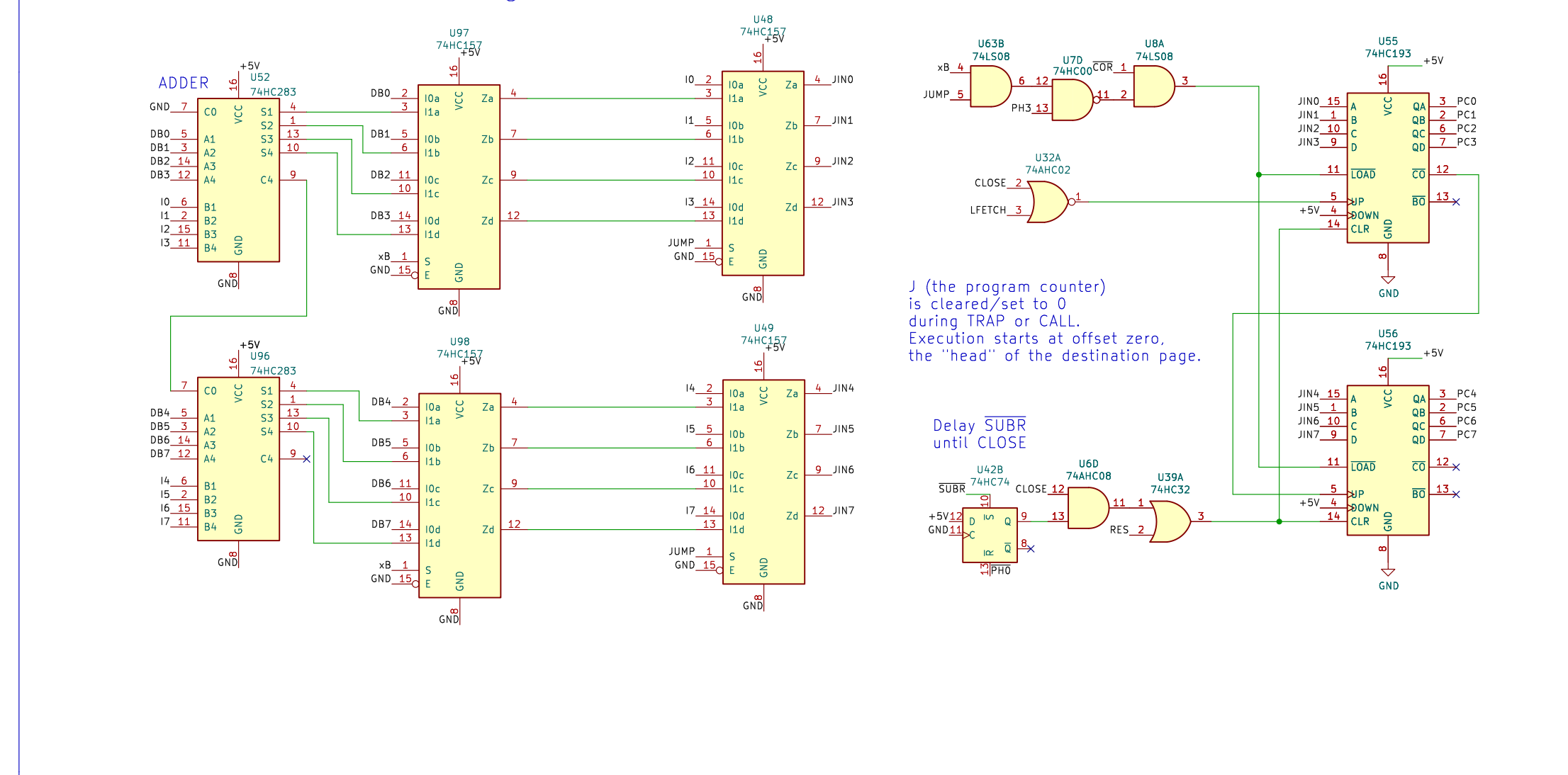
Remap 'scrounger' 66, RR, II etc and impracticable opcodes such as NM



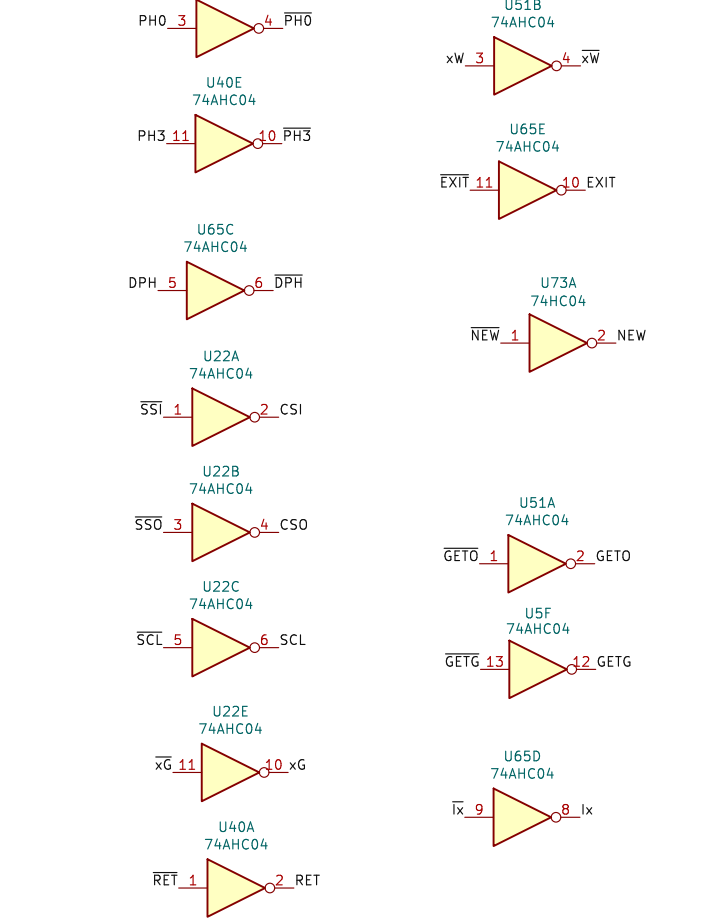
## Control Tree



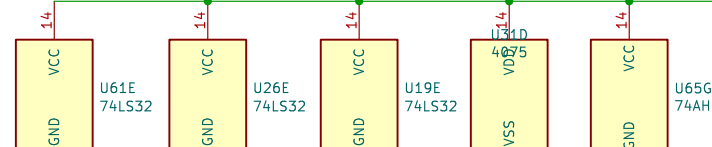
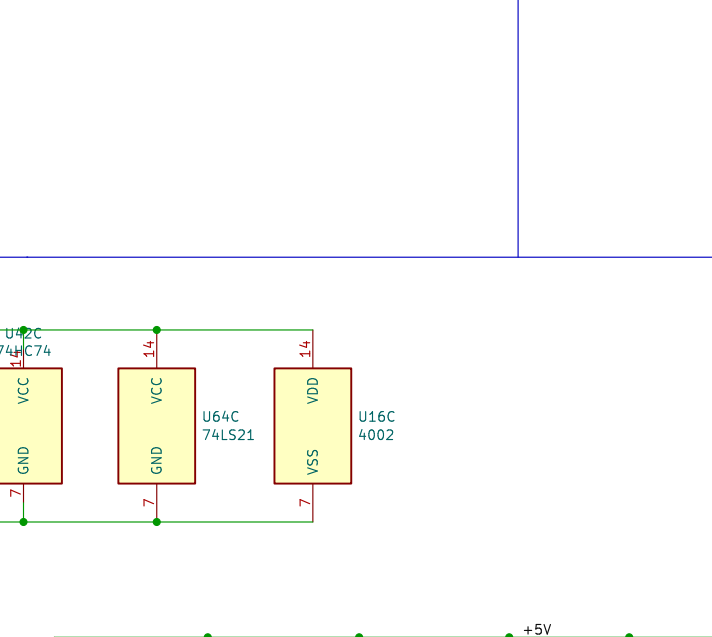
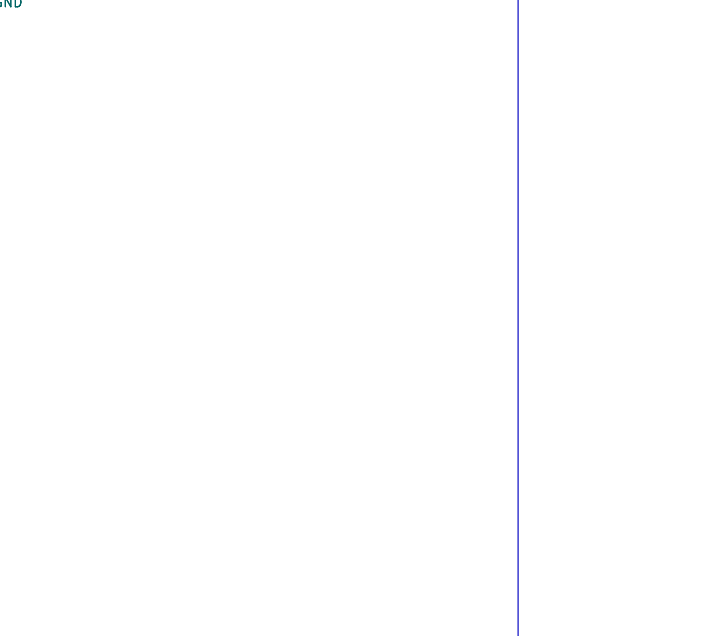
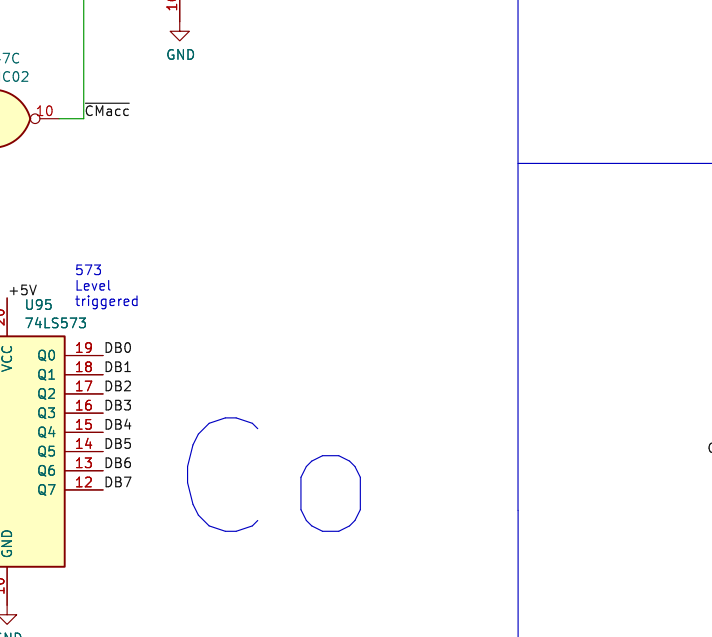
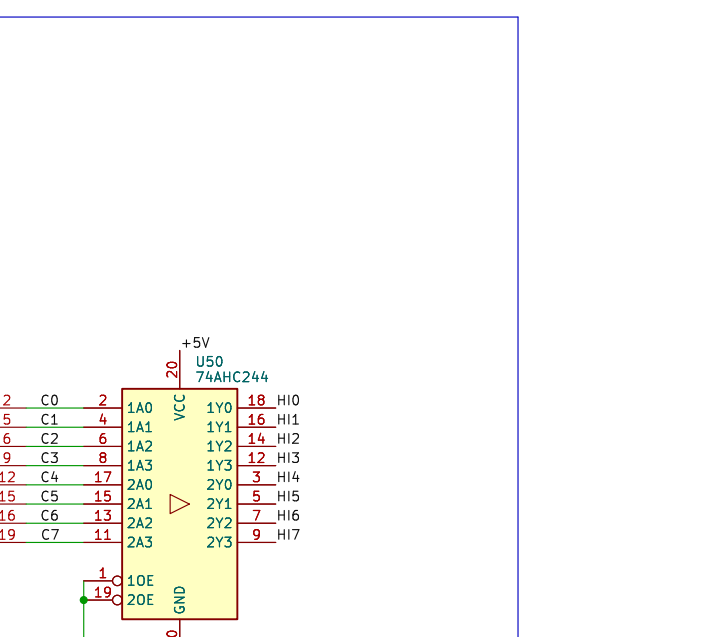
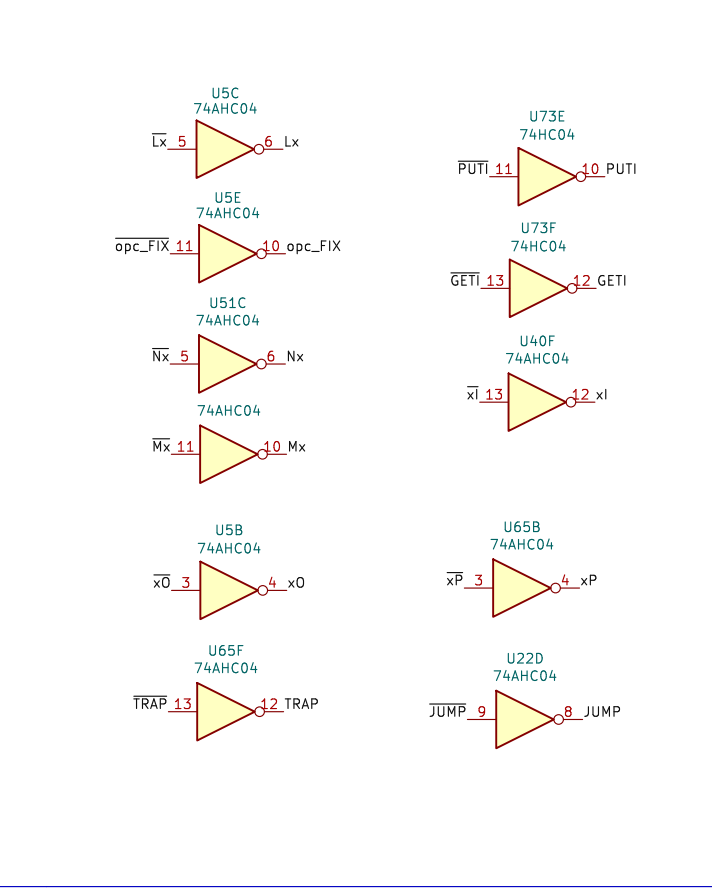
## Relative Addressing Unit



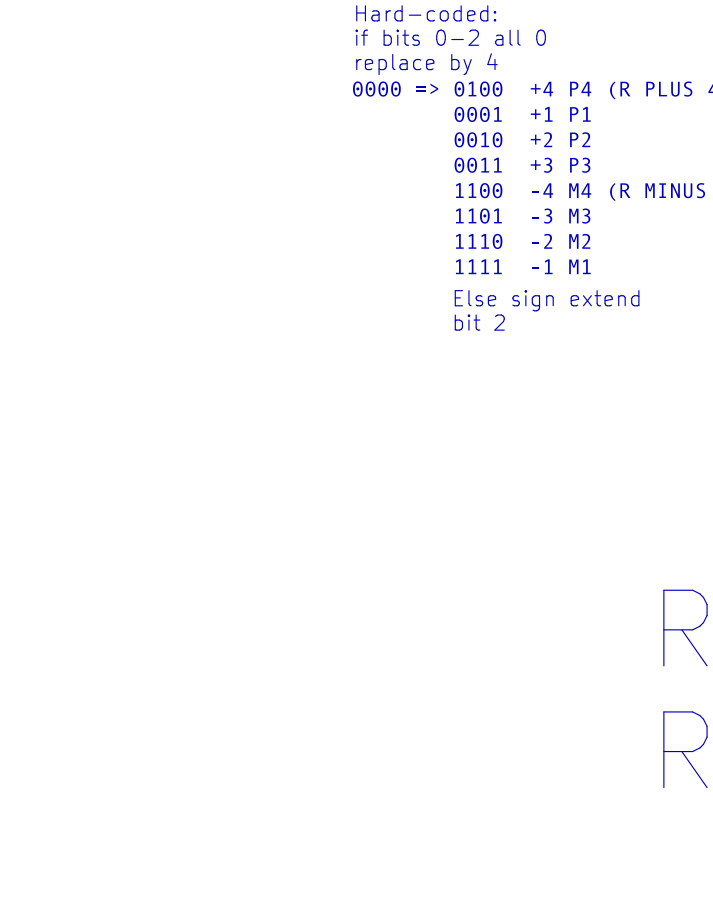
## I (INNER Loop Register)



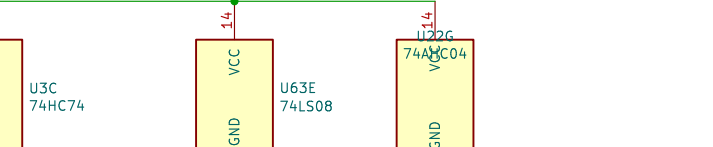
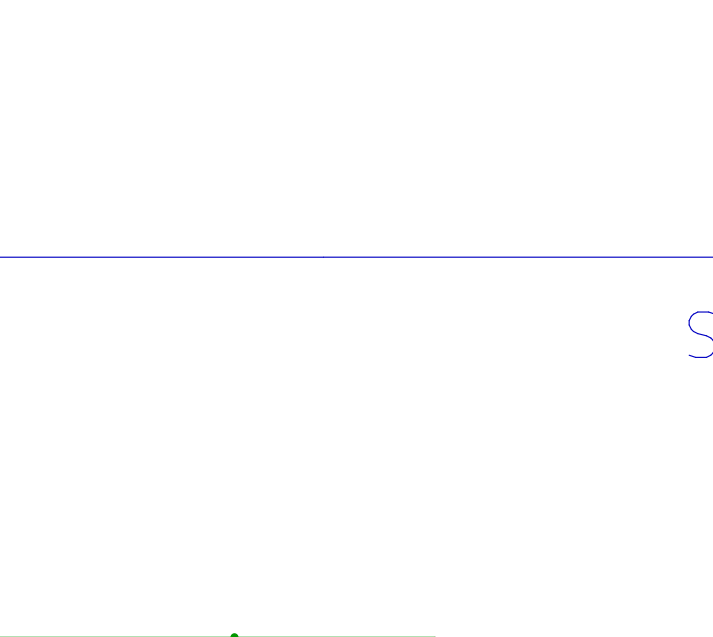
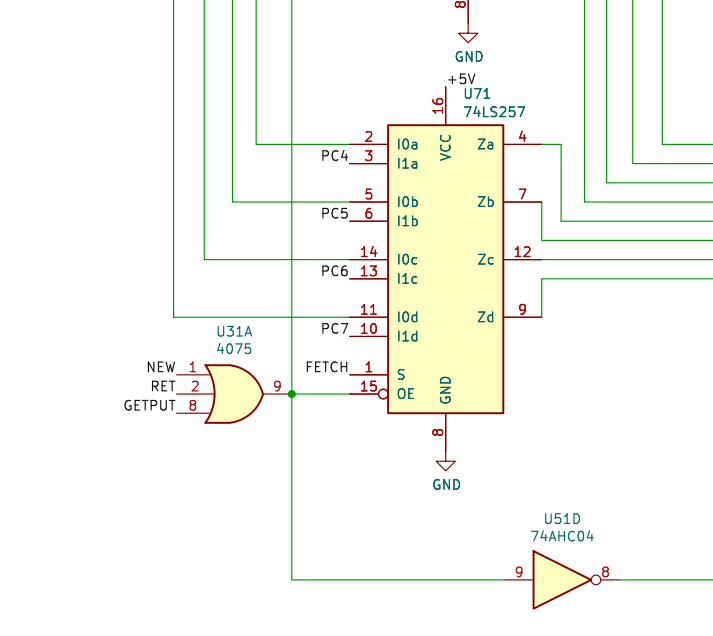
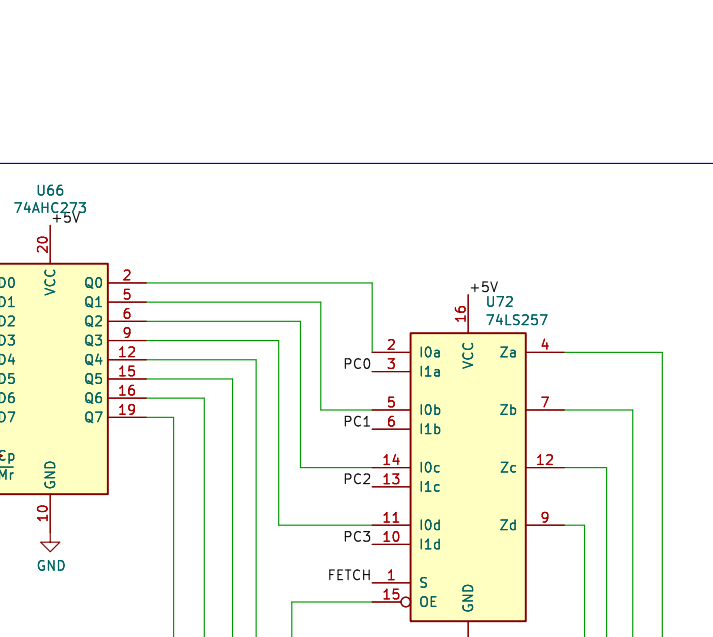
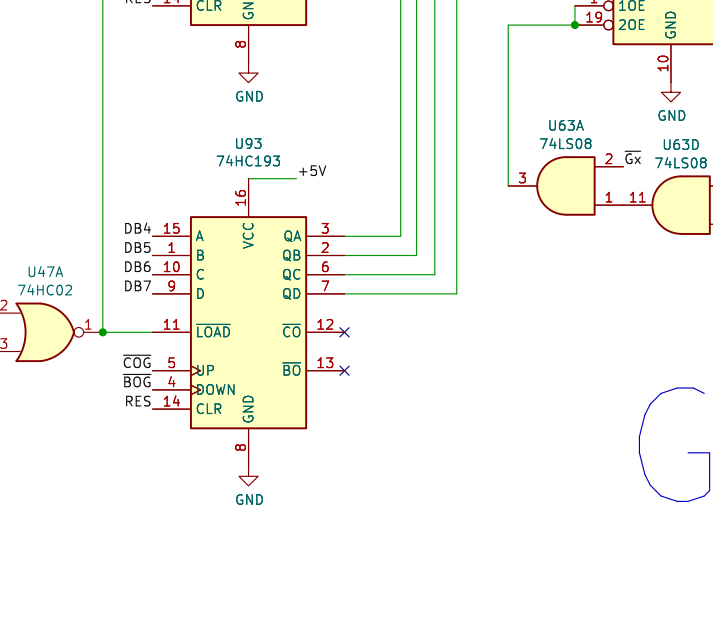
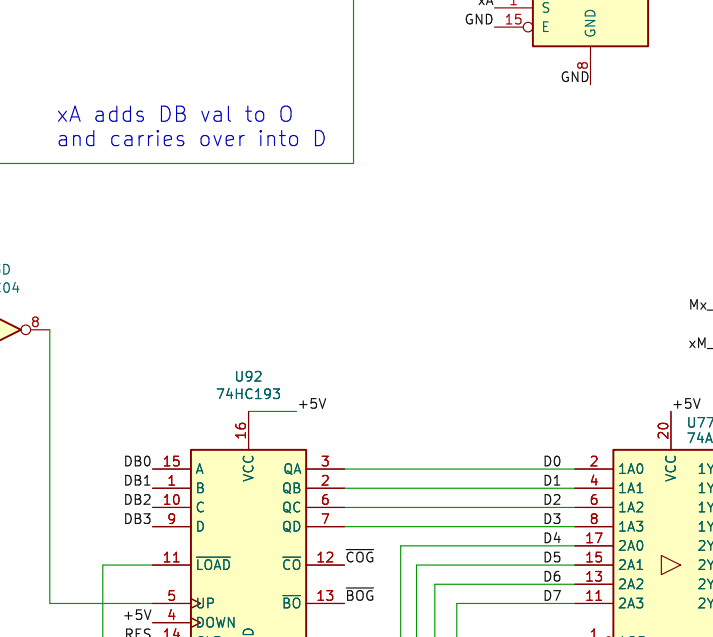
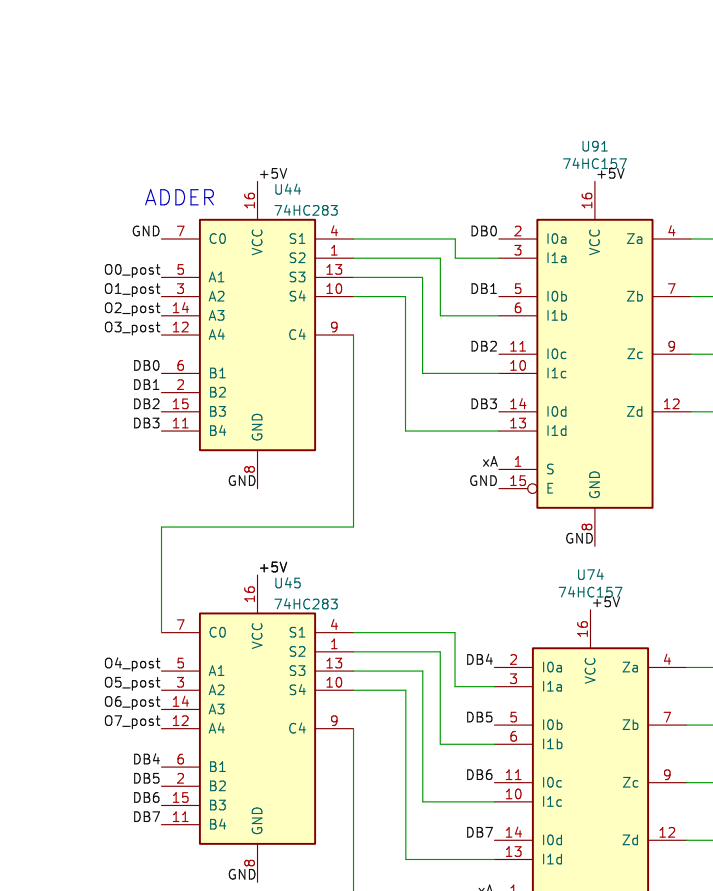
## Inverters



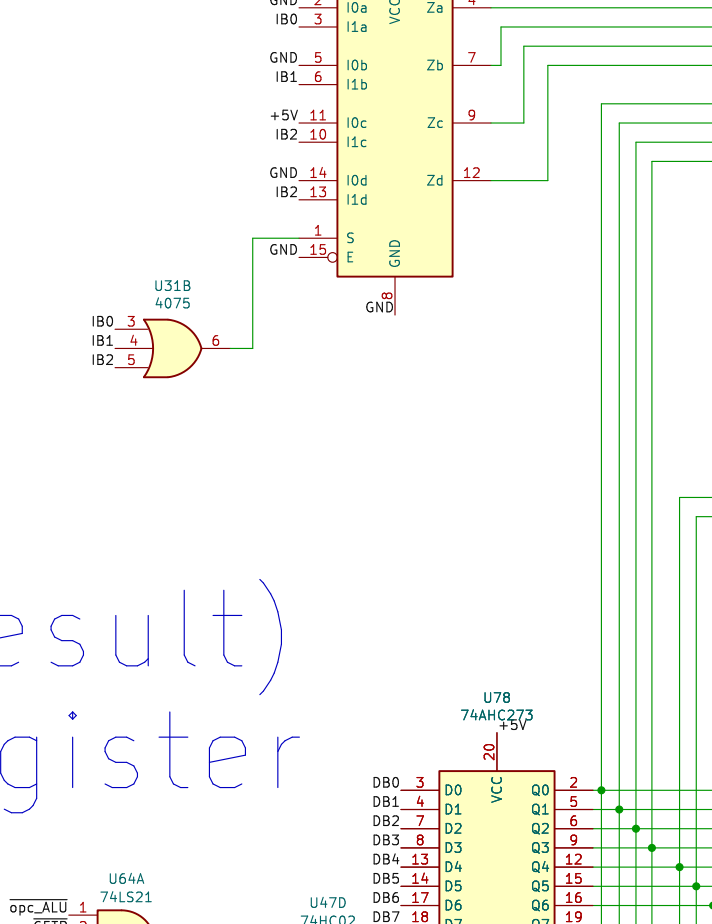
## R-Fix



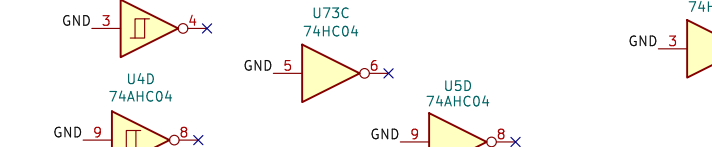
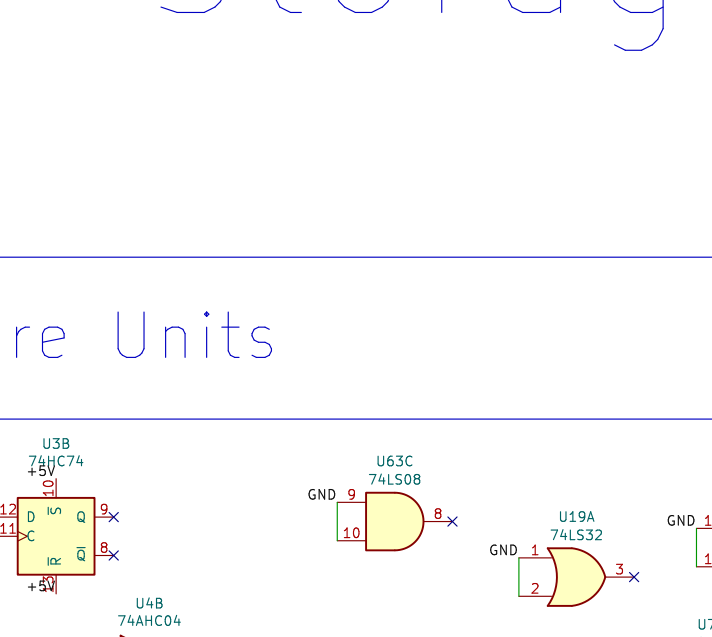
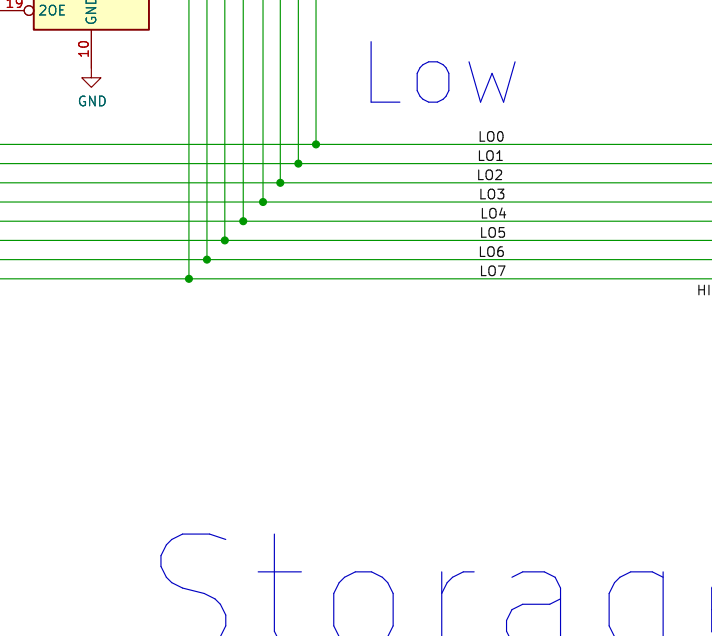
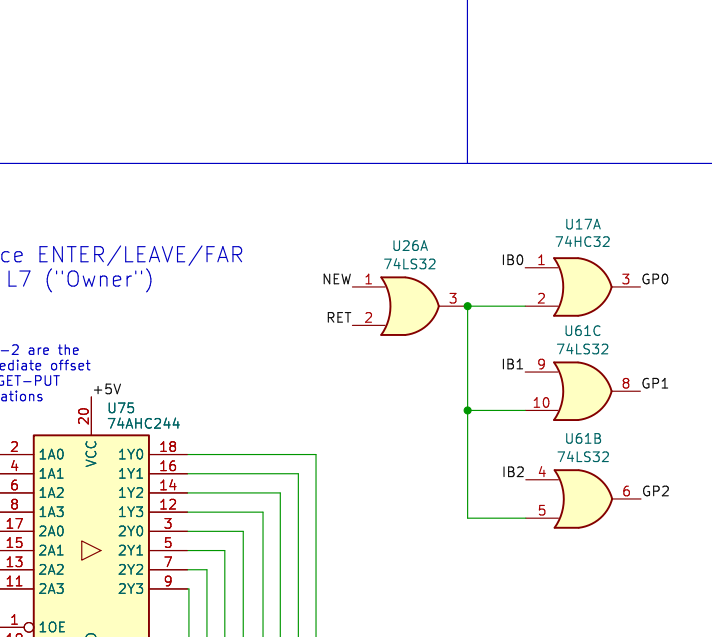
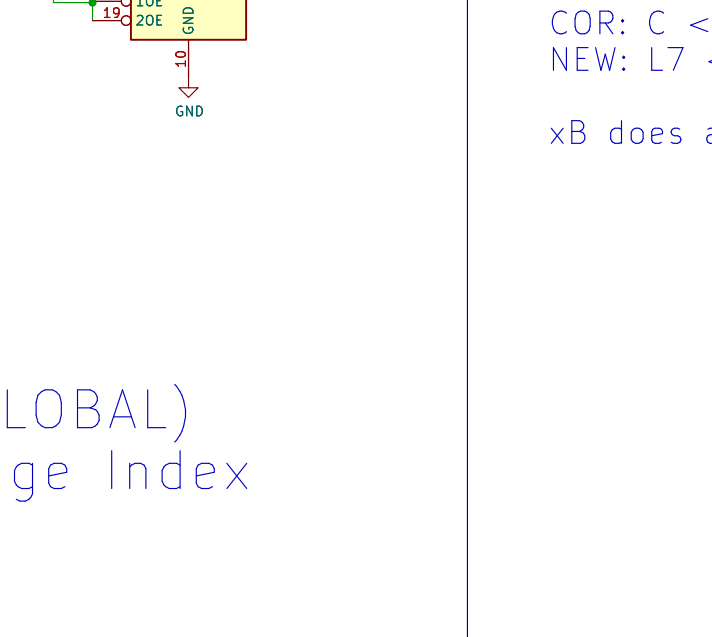
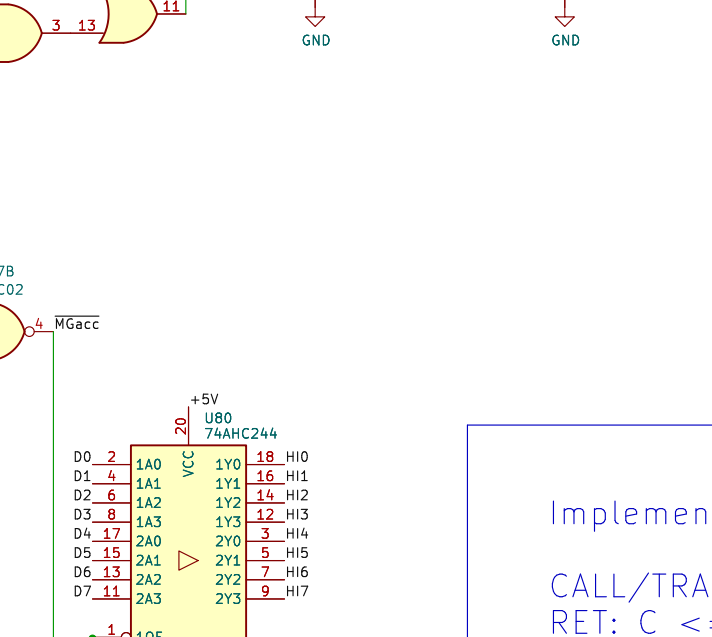
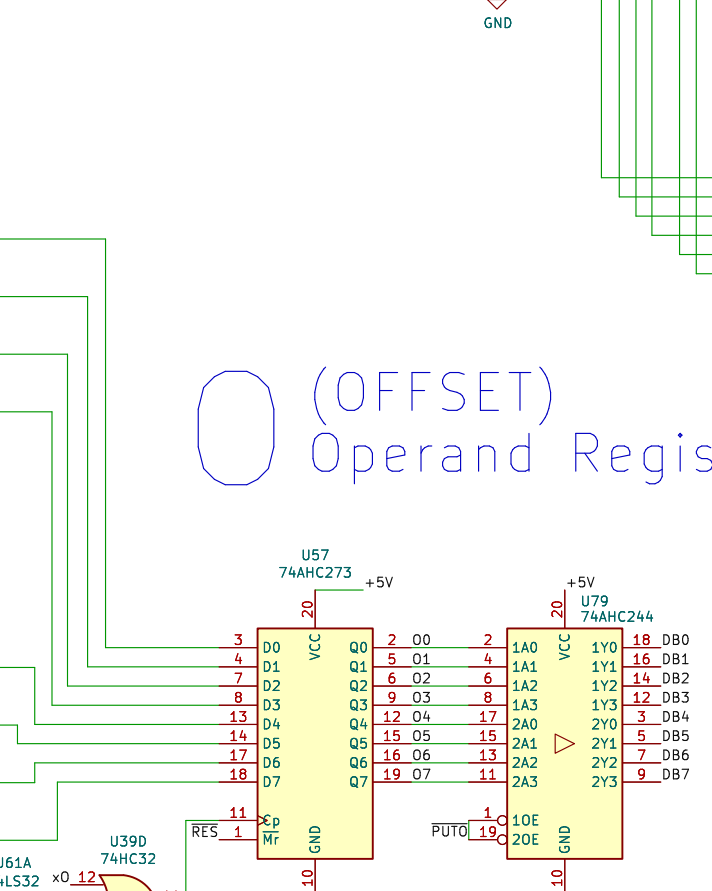
## R(result) Register



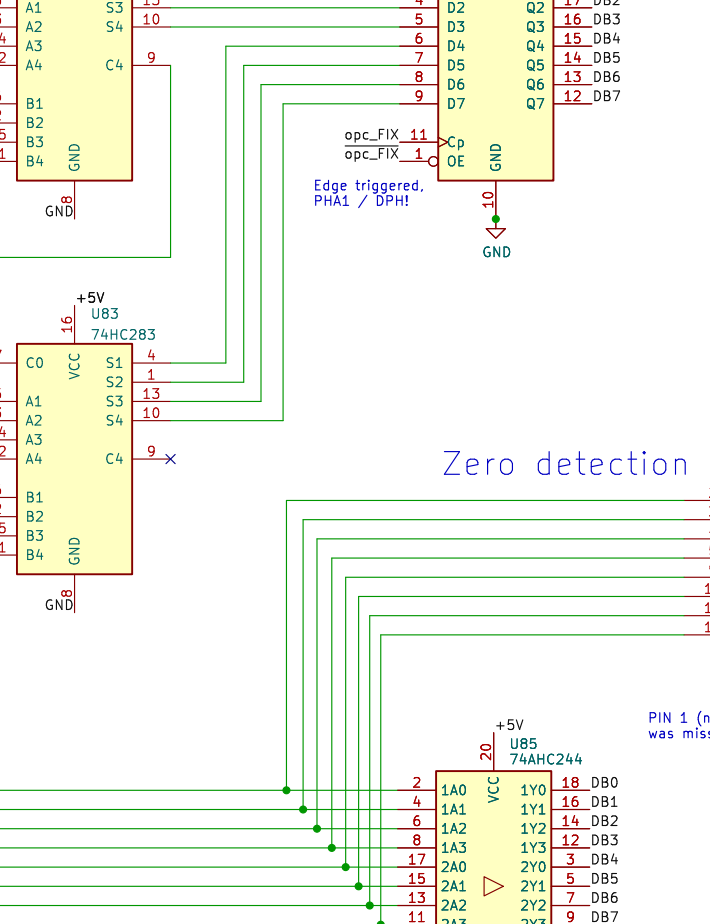
## O (OFFSET) Operand Register



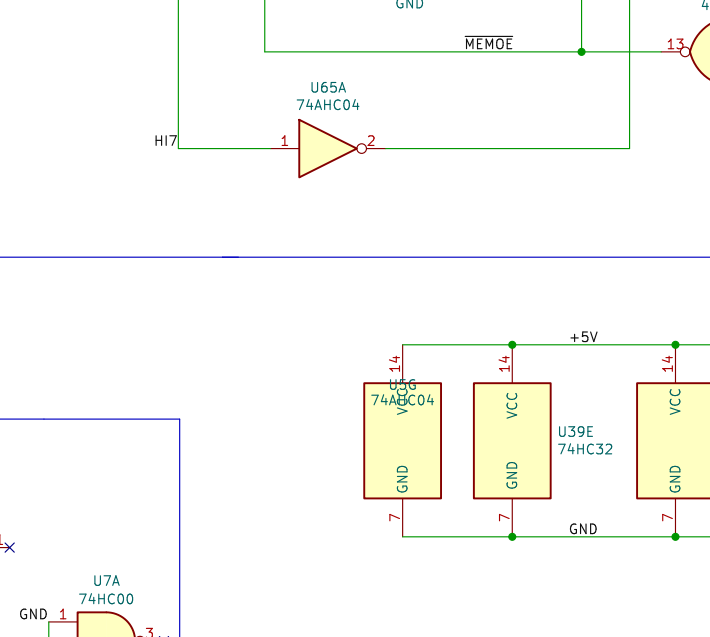
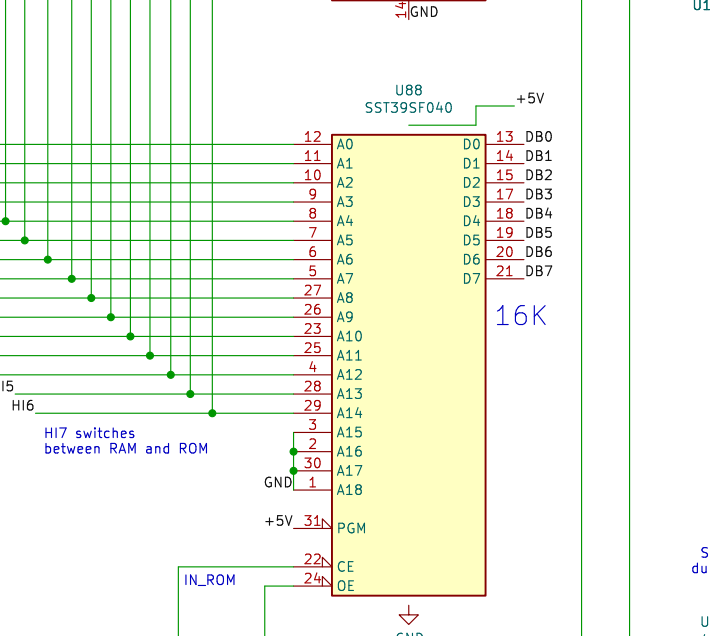
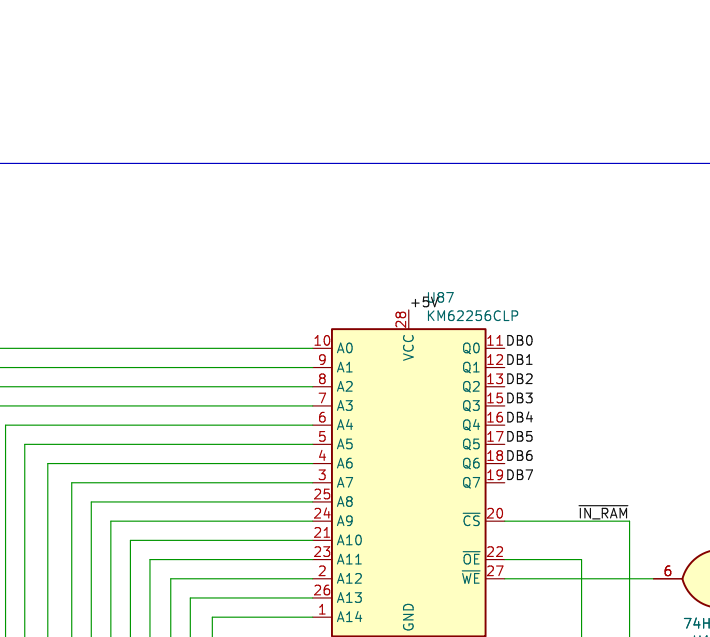
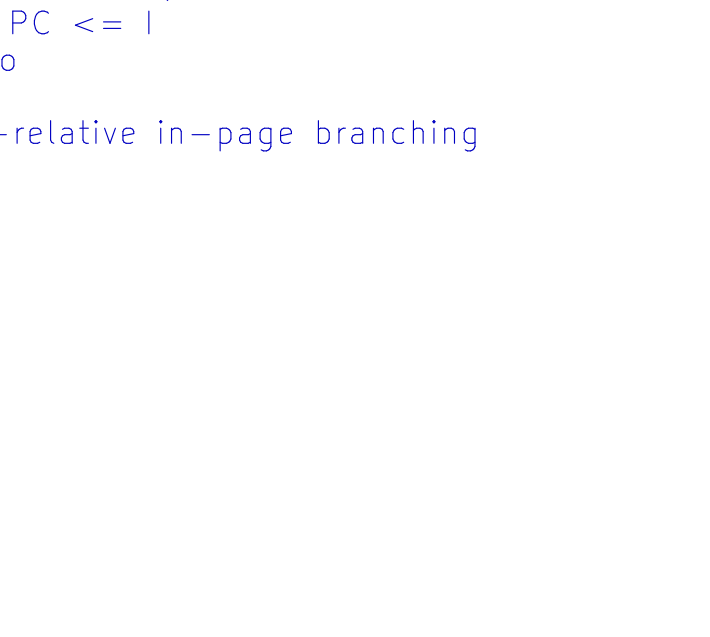
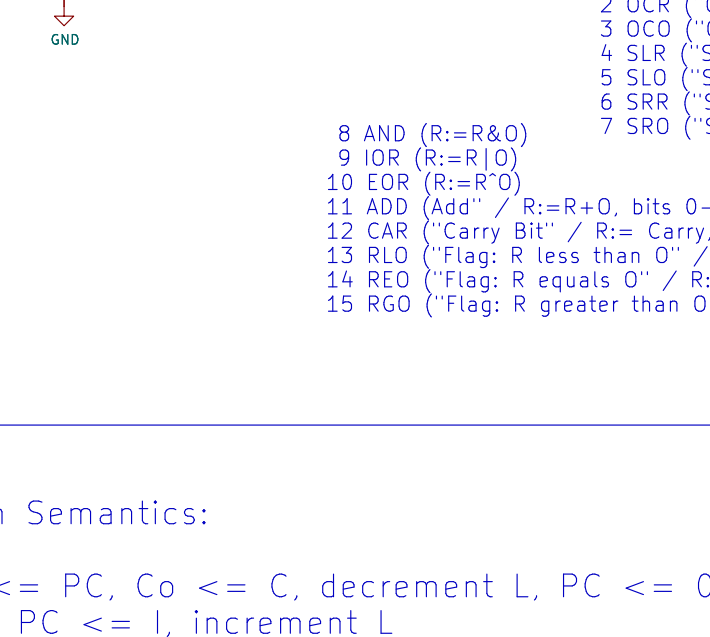
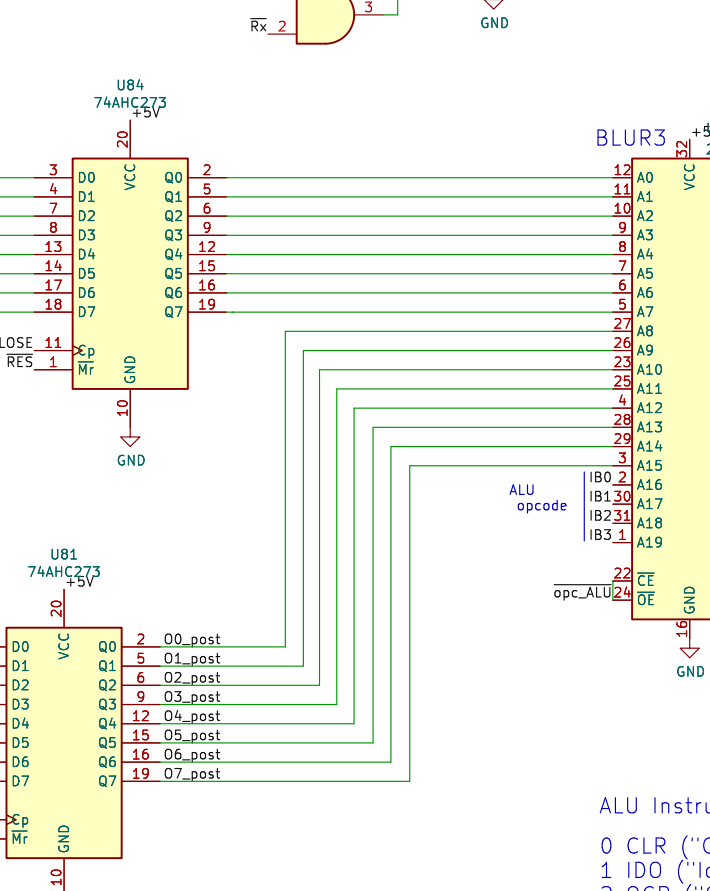
## GLOBAL Page Index



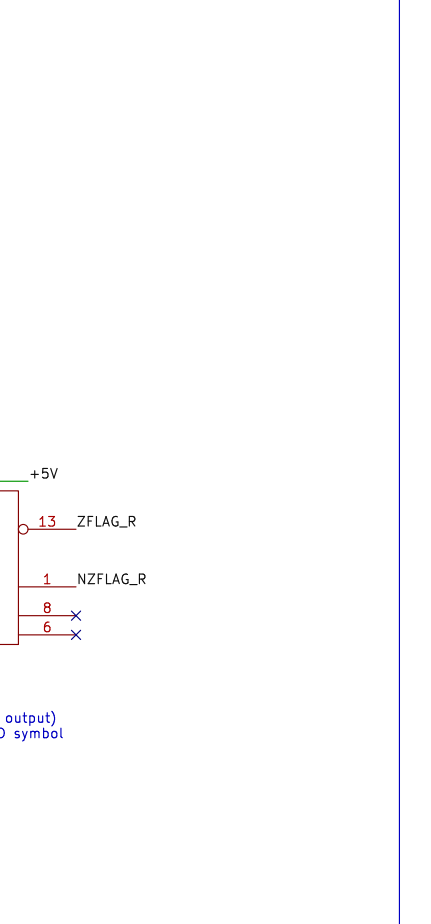
## Low



## Storage



## Memory



## Spare Units

