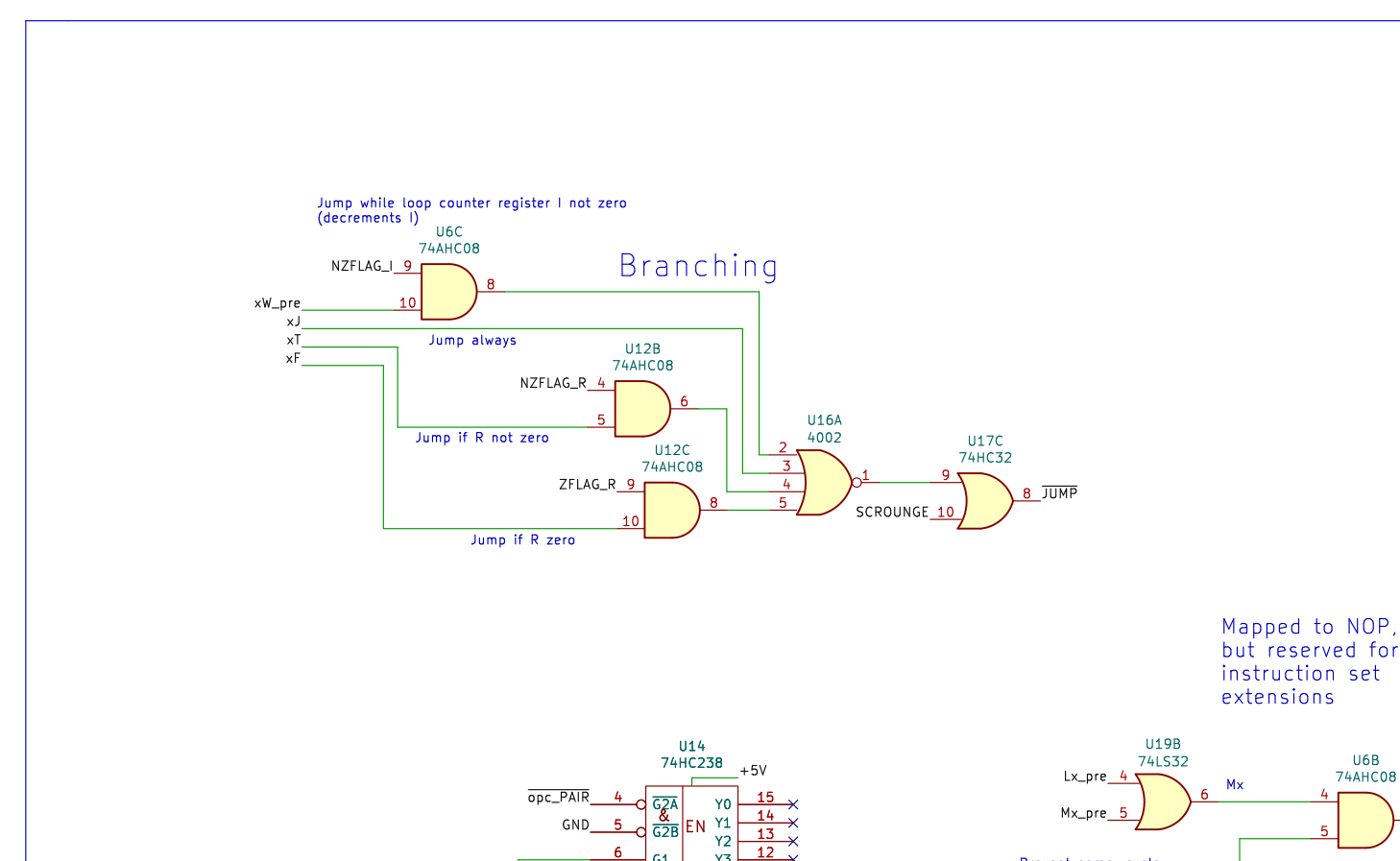
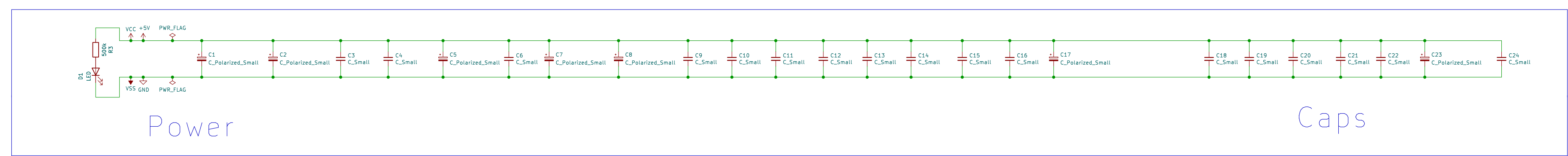


# Sonne-8 Microcontroller Reference Schematics

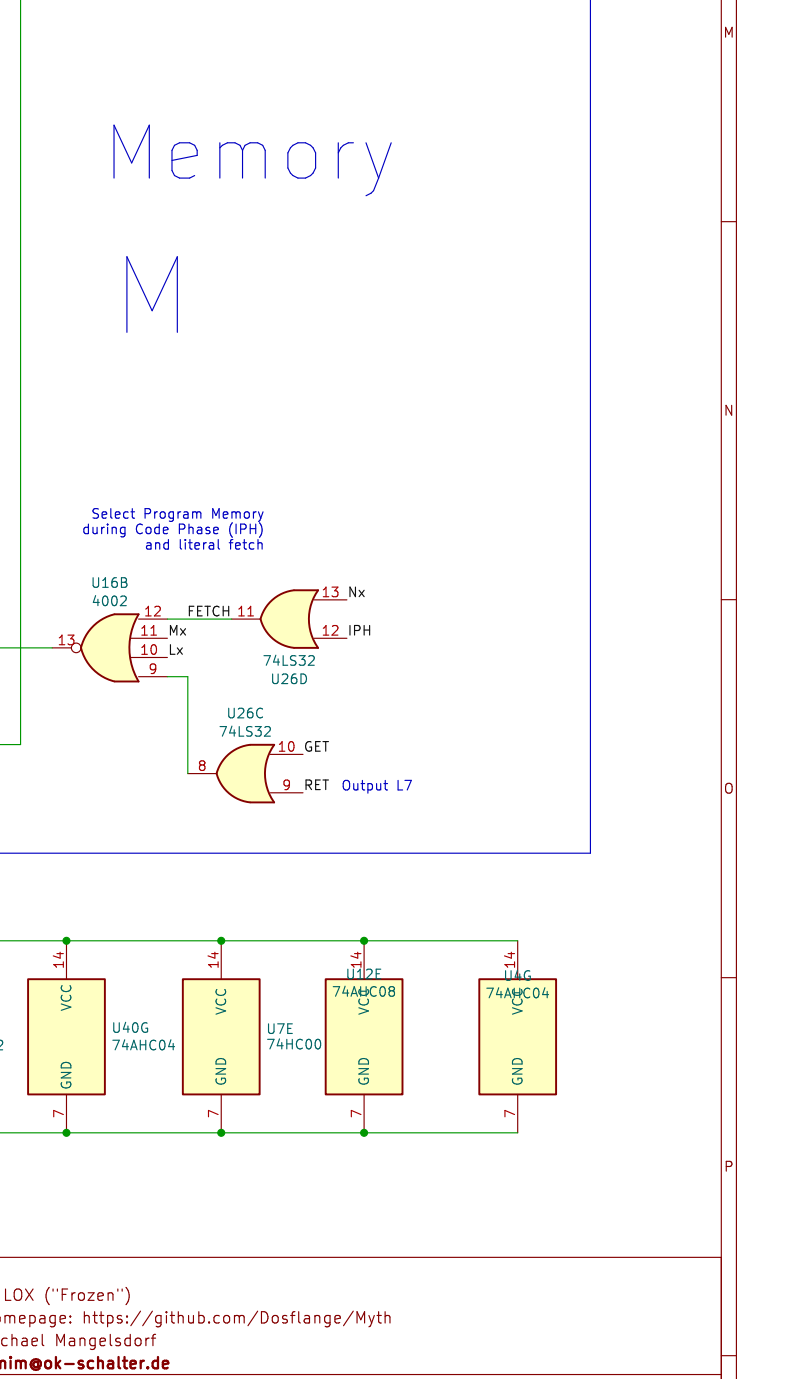
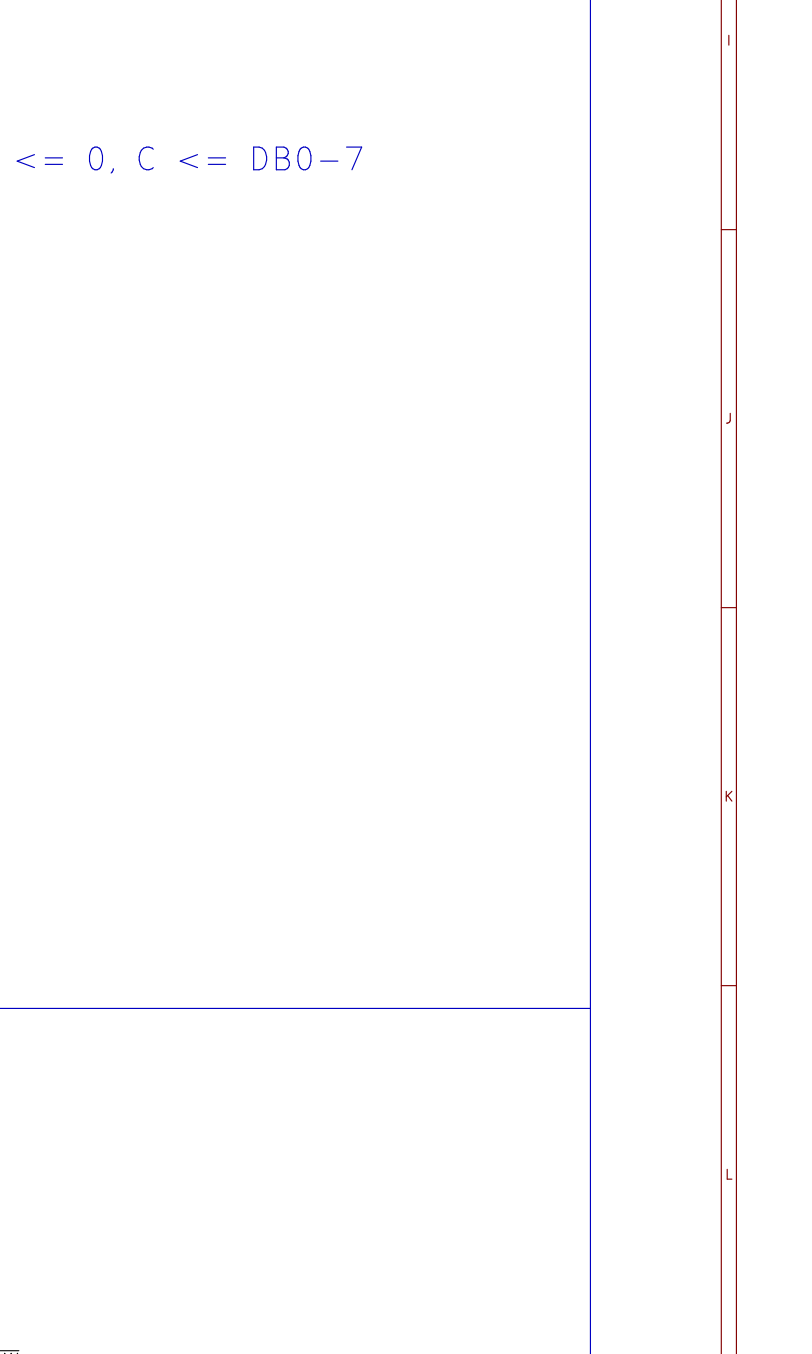
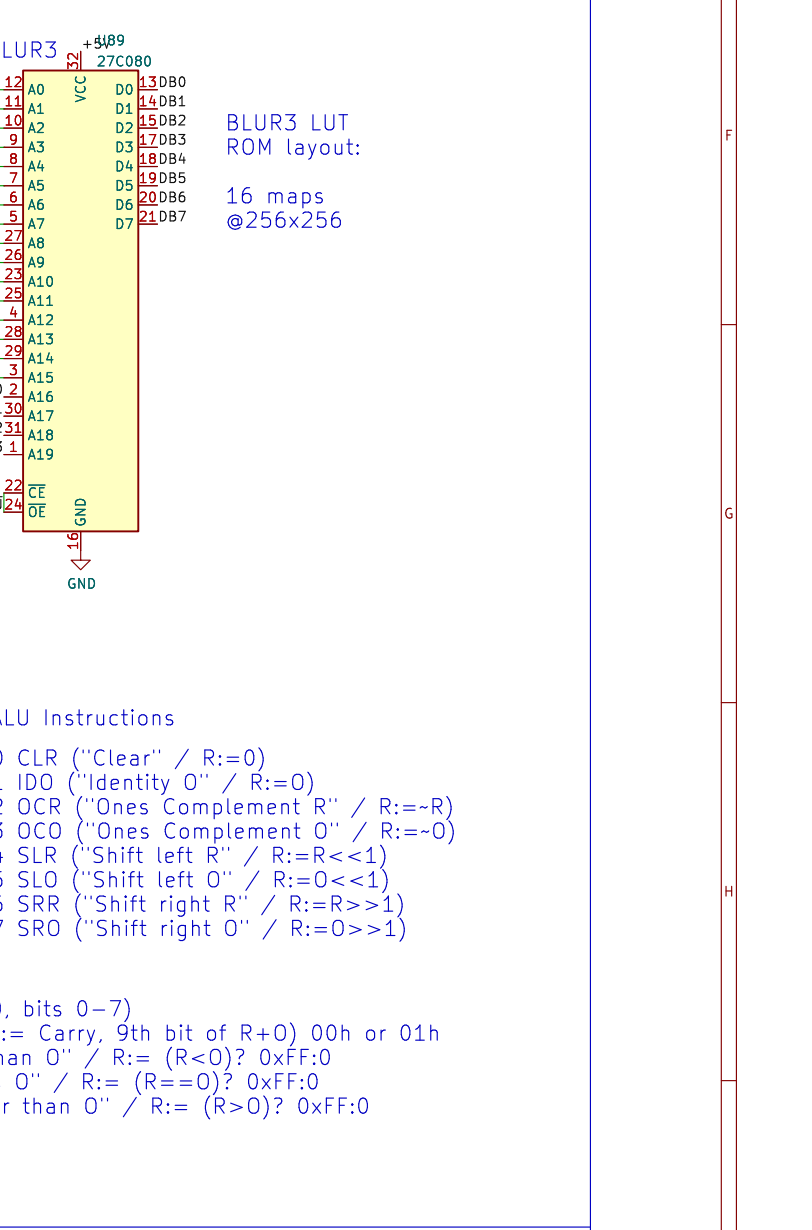
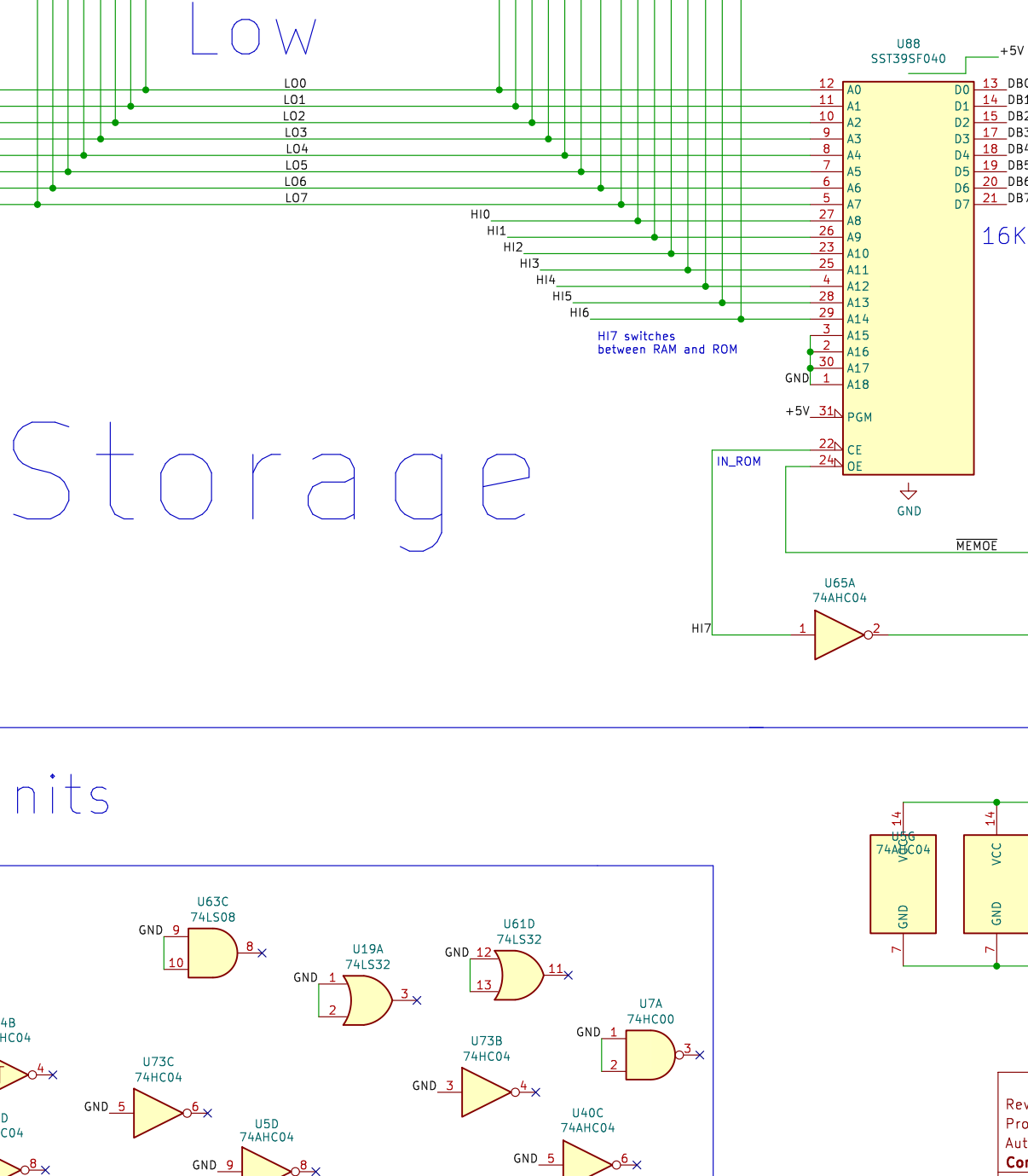
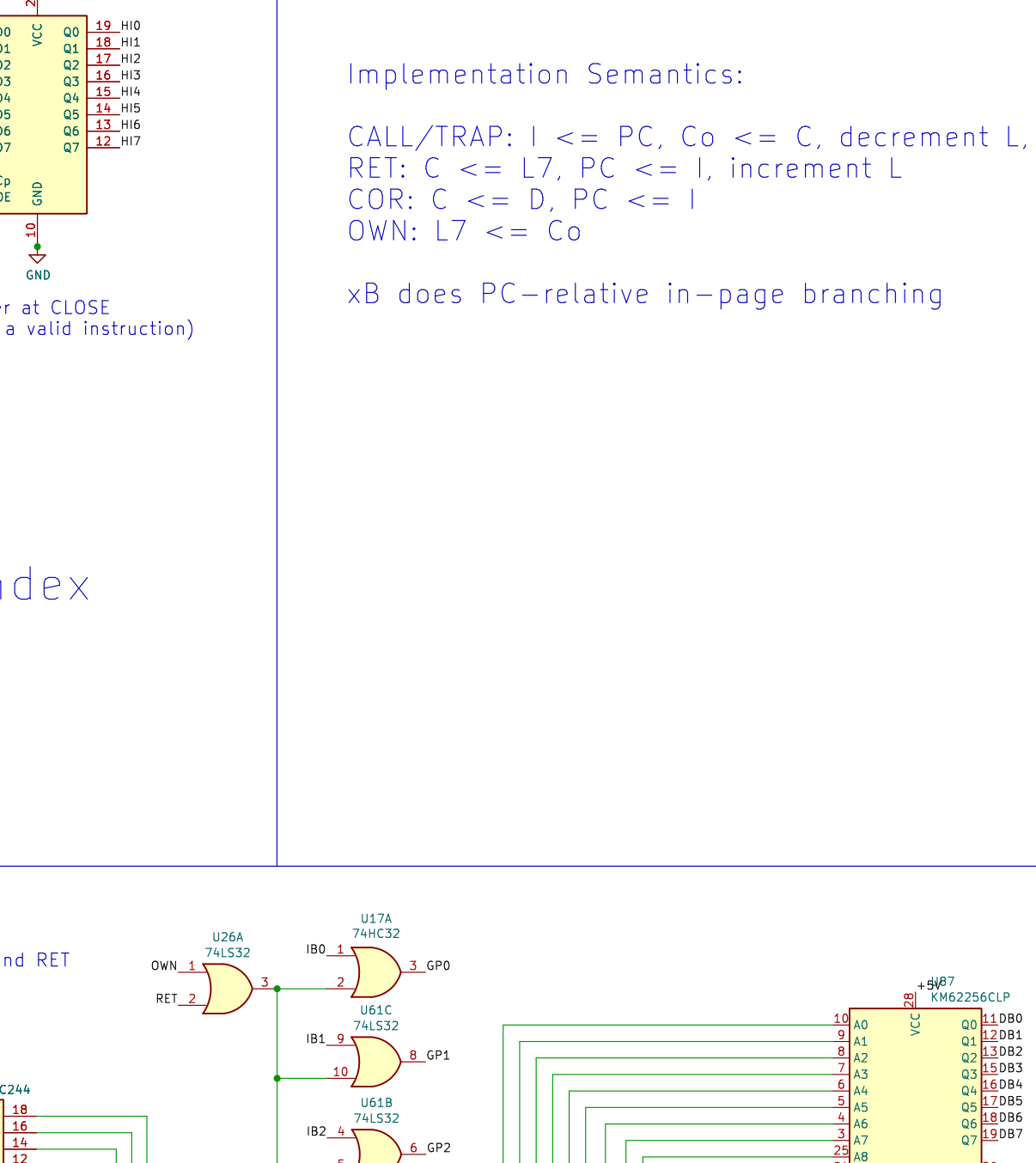
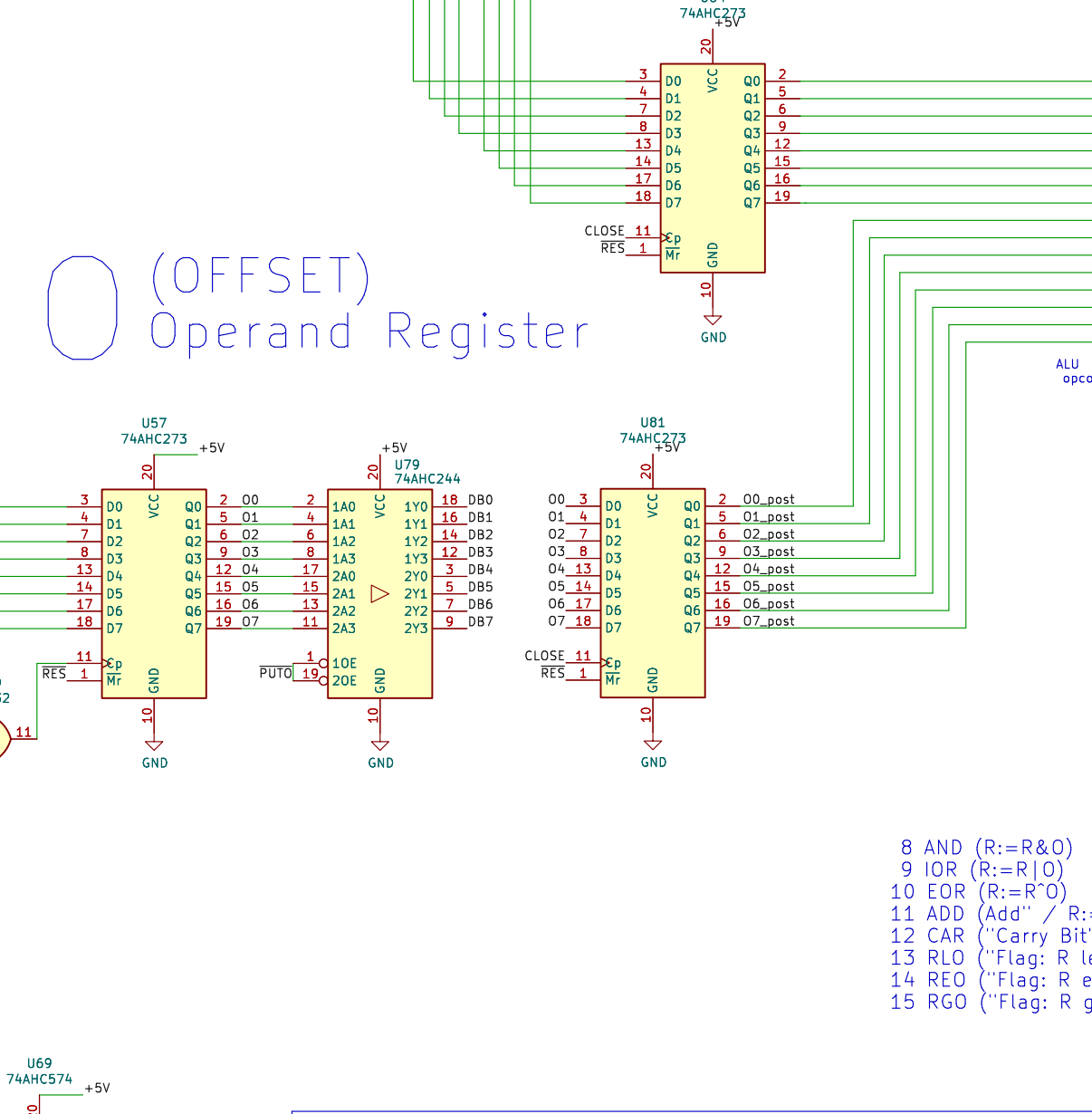
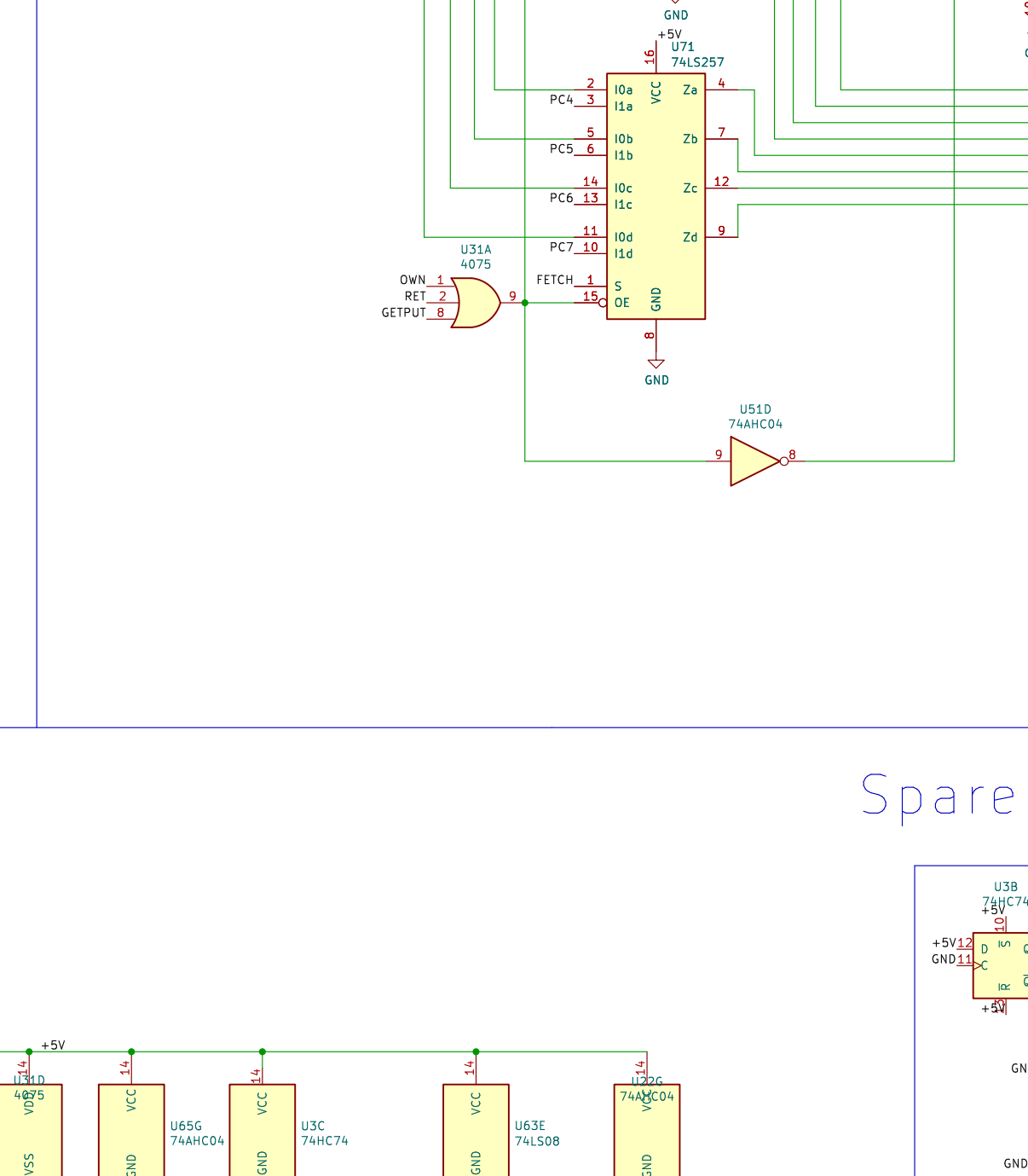
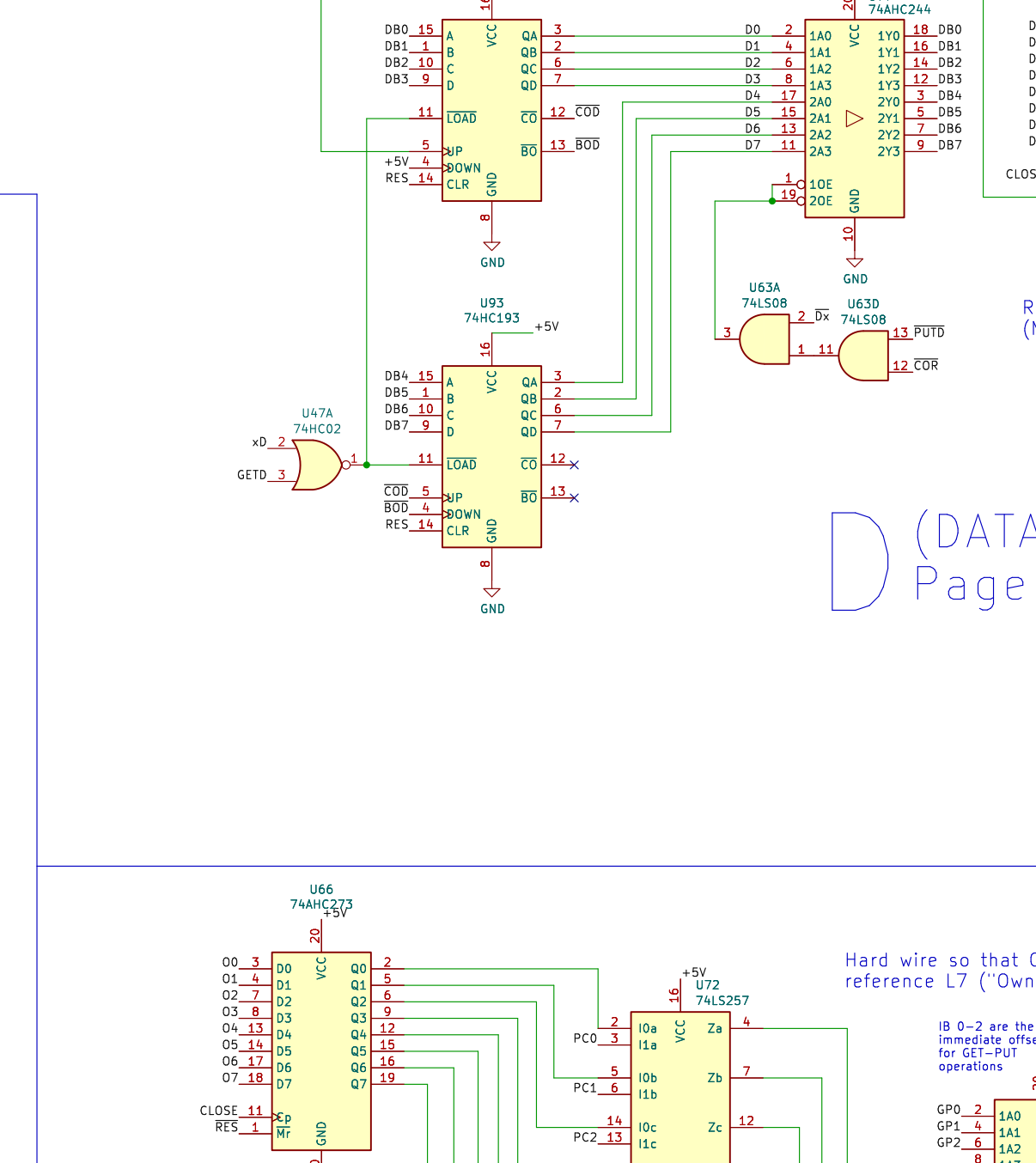
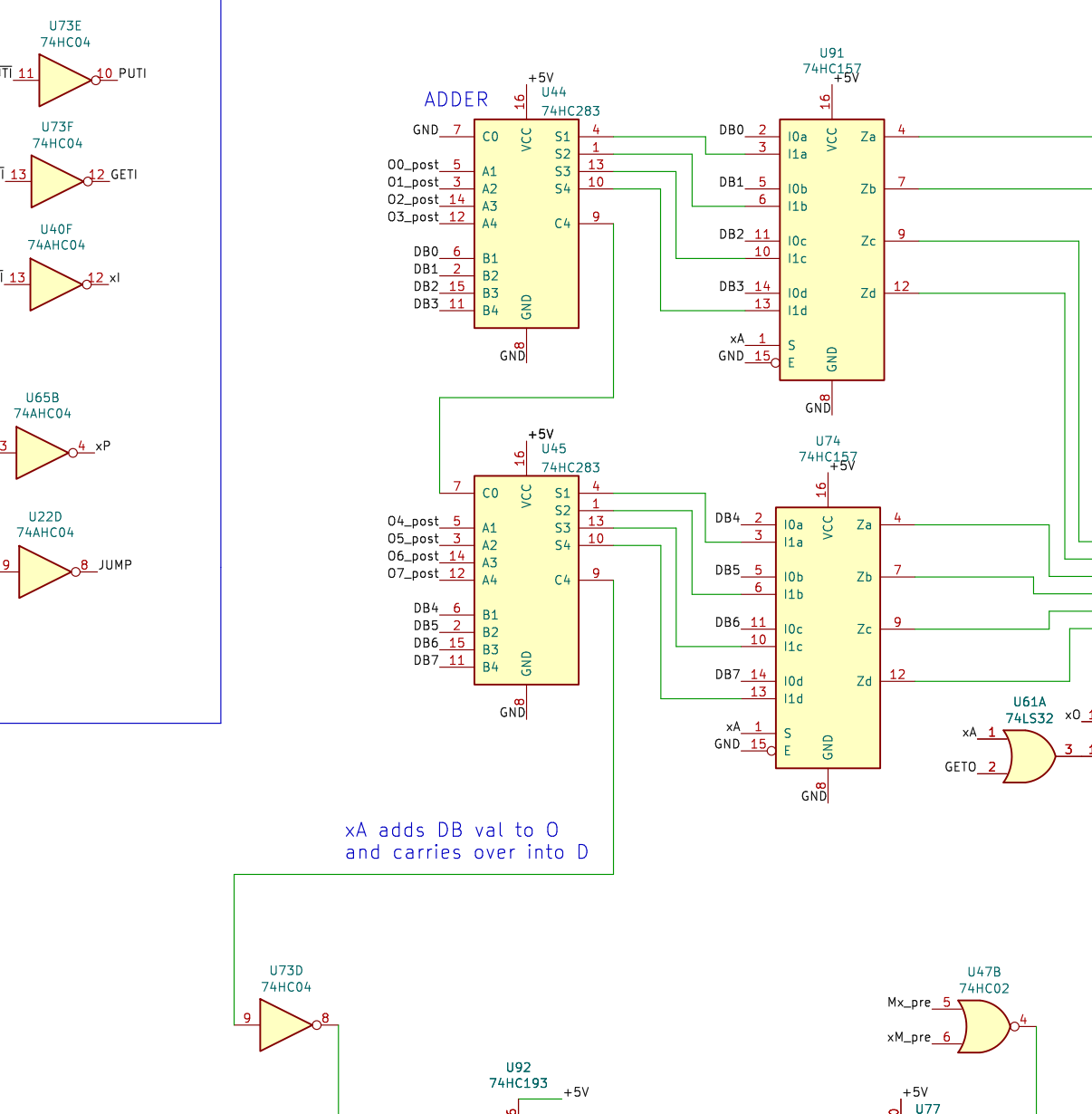
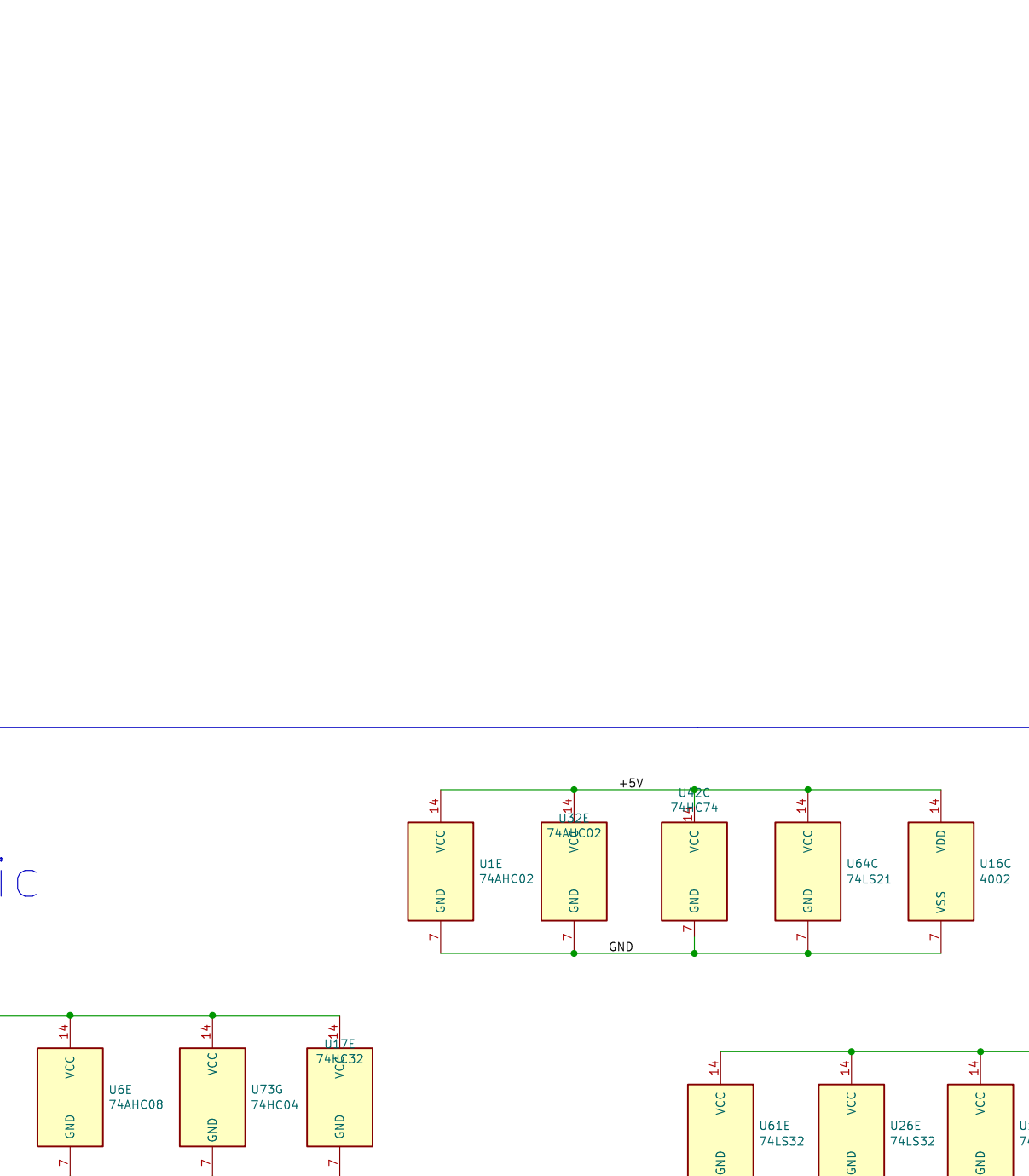
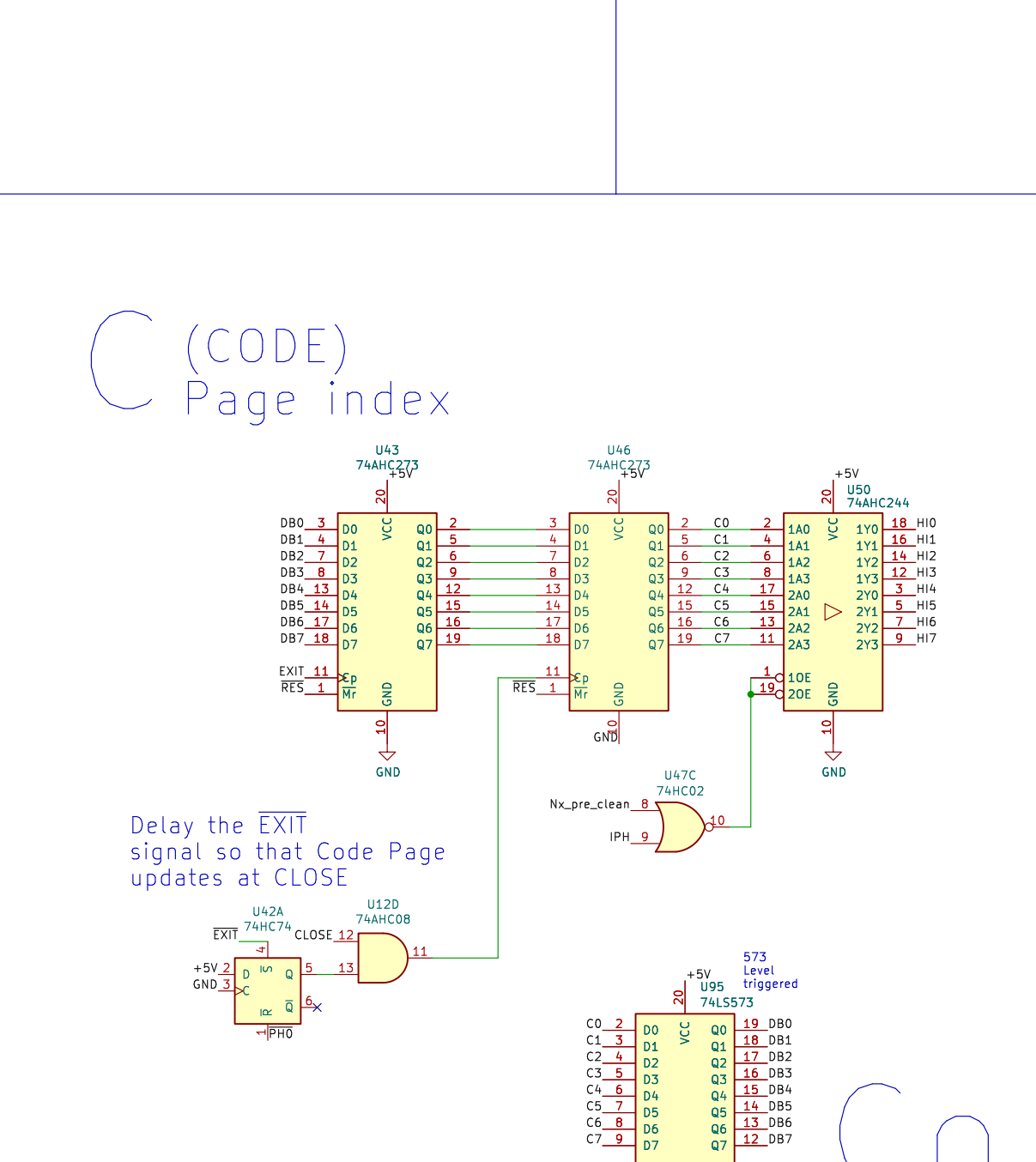
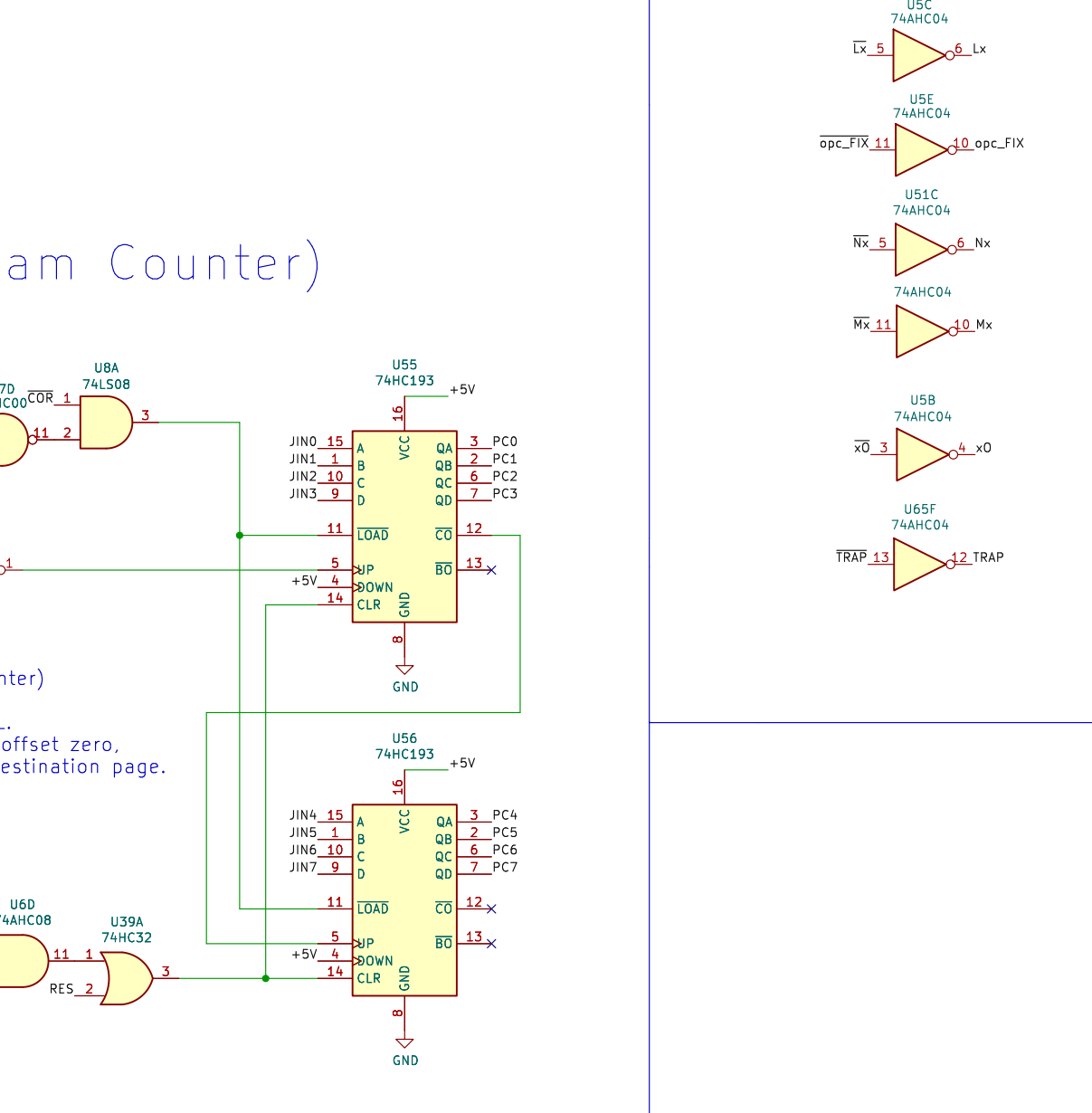
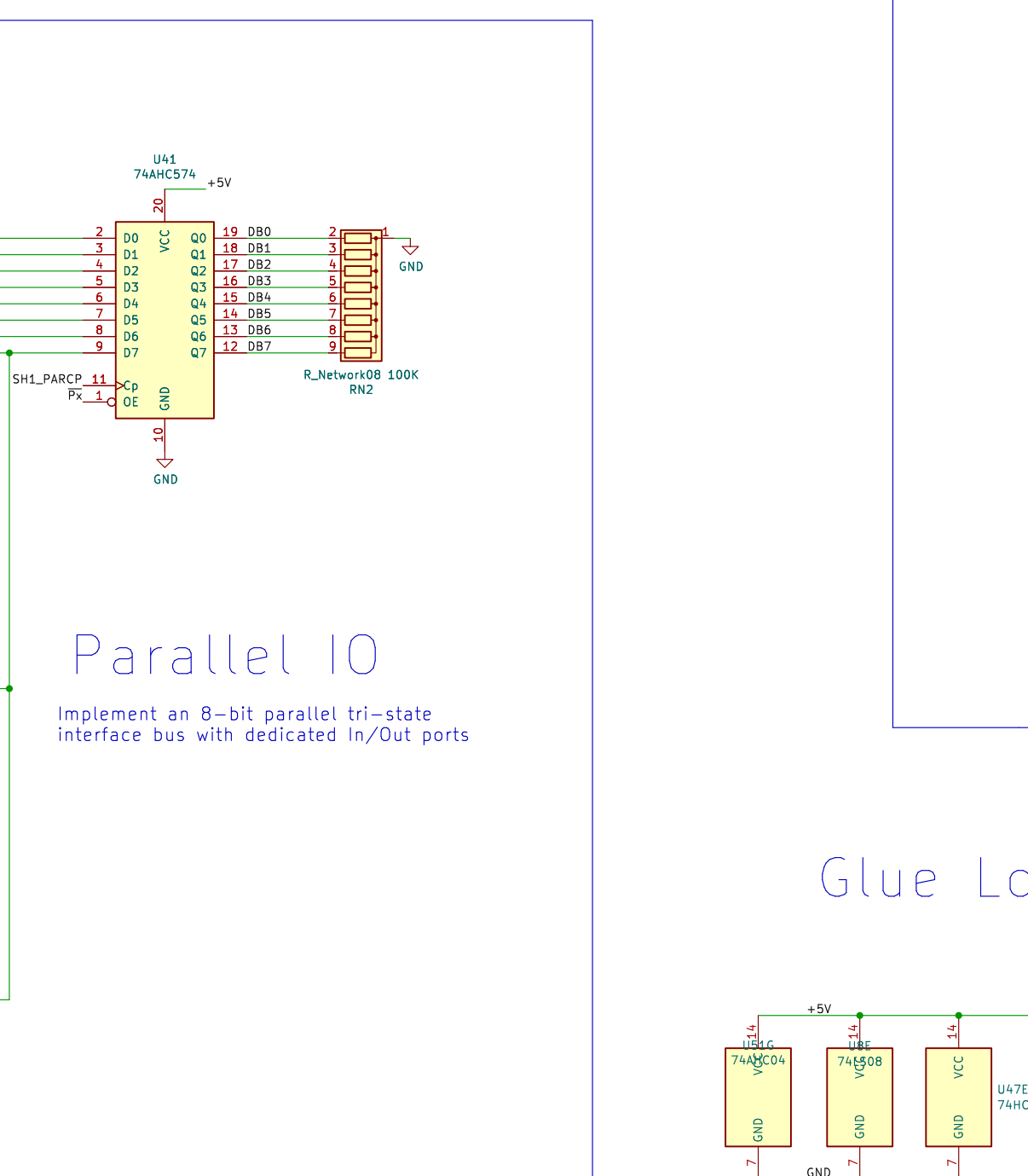
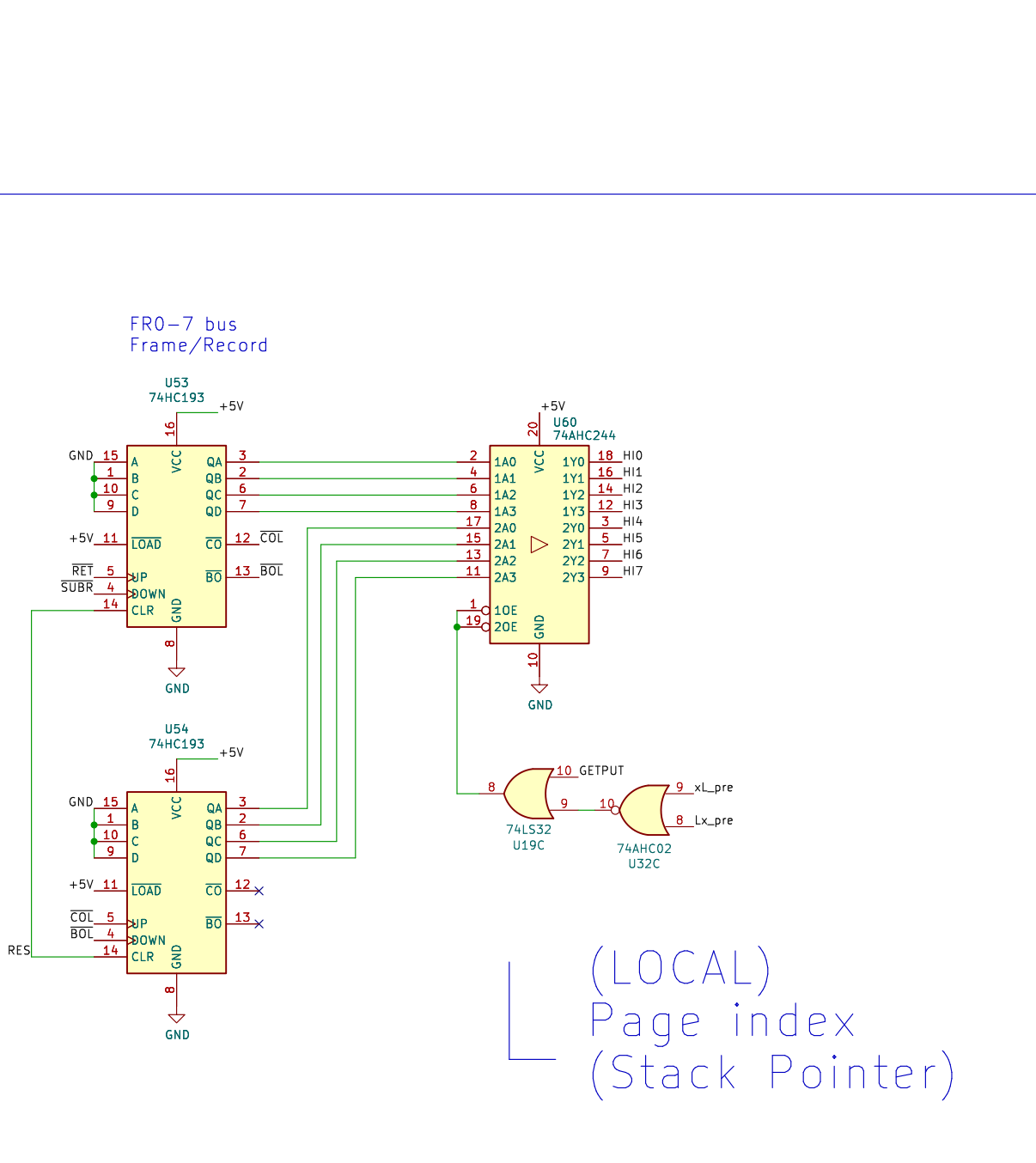
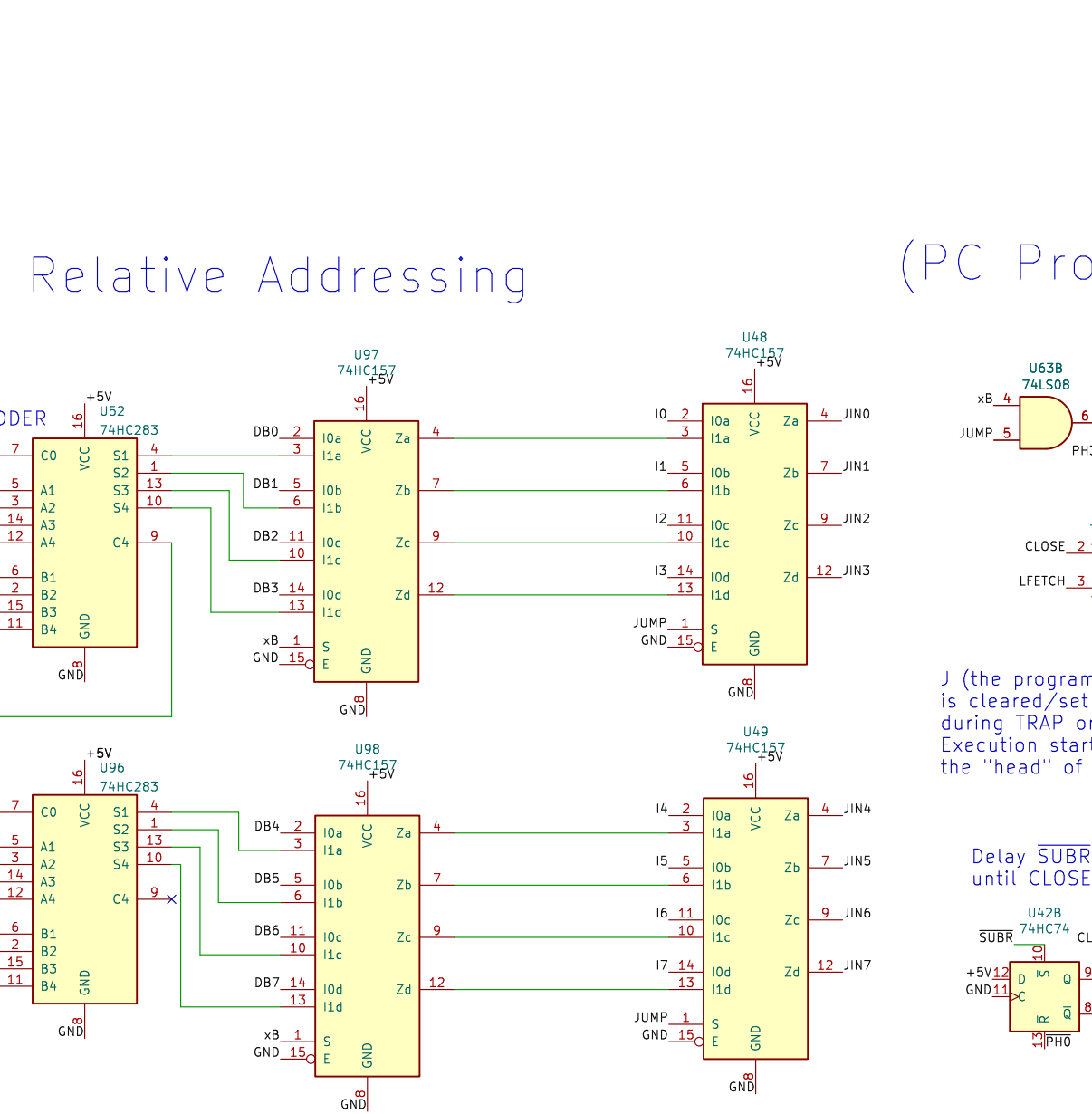
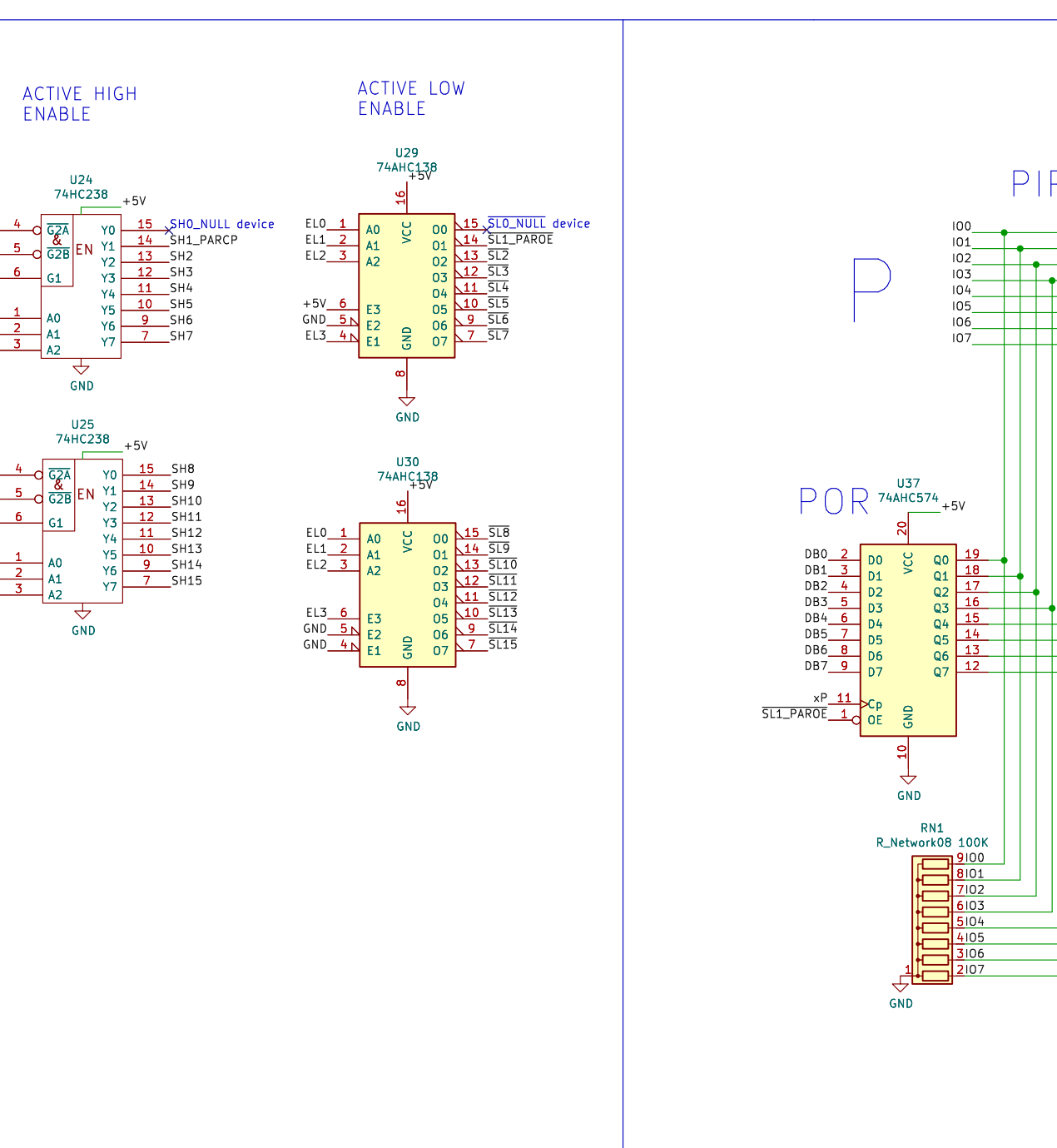
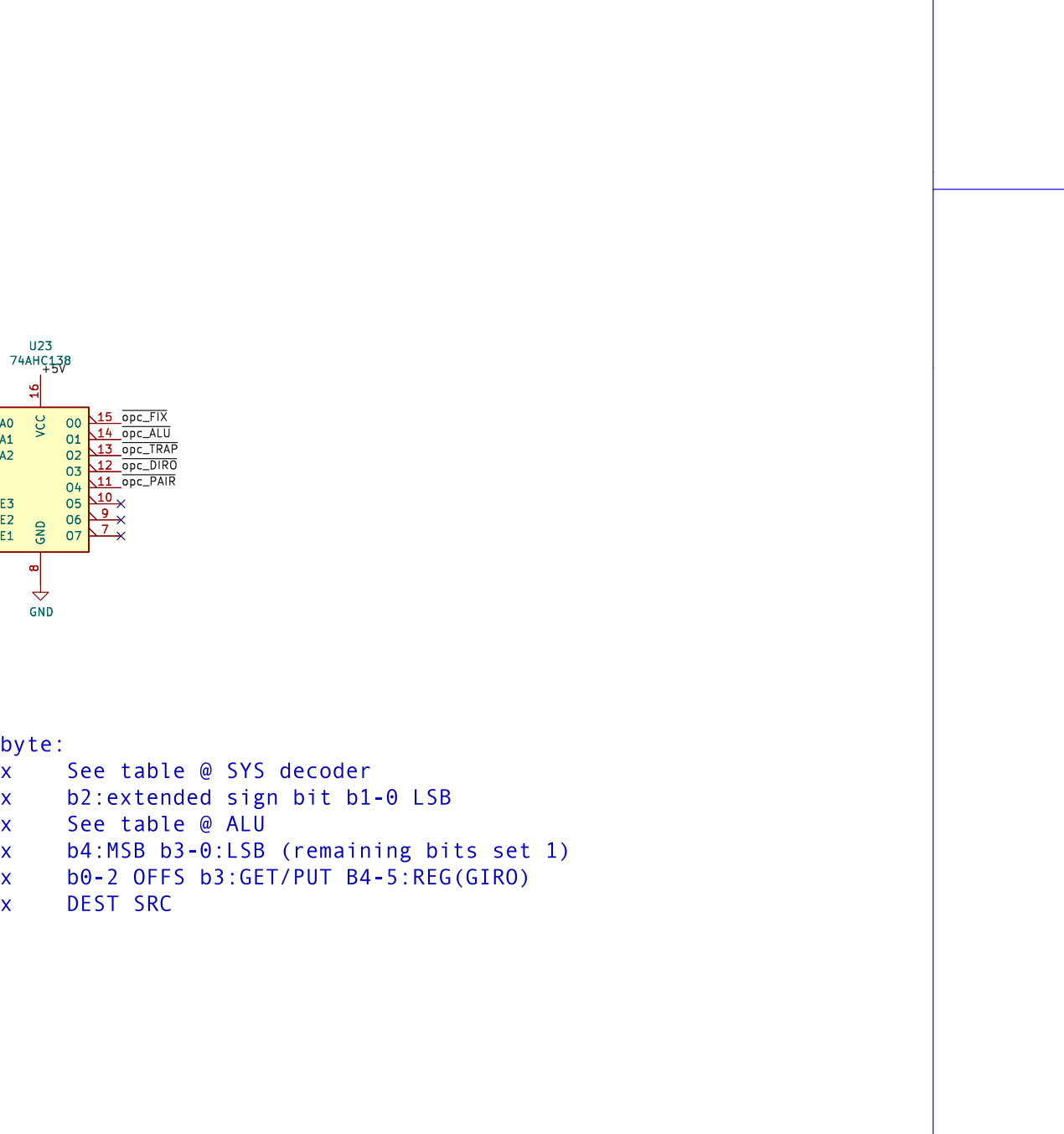
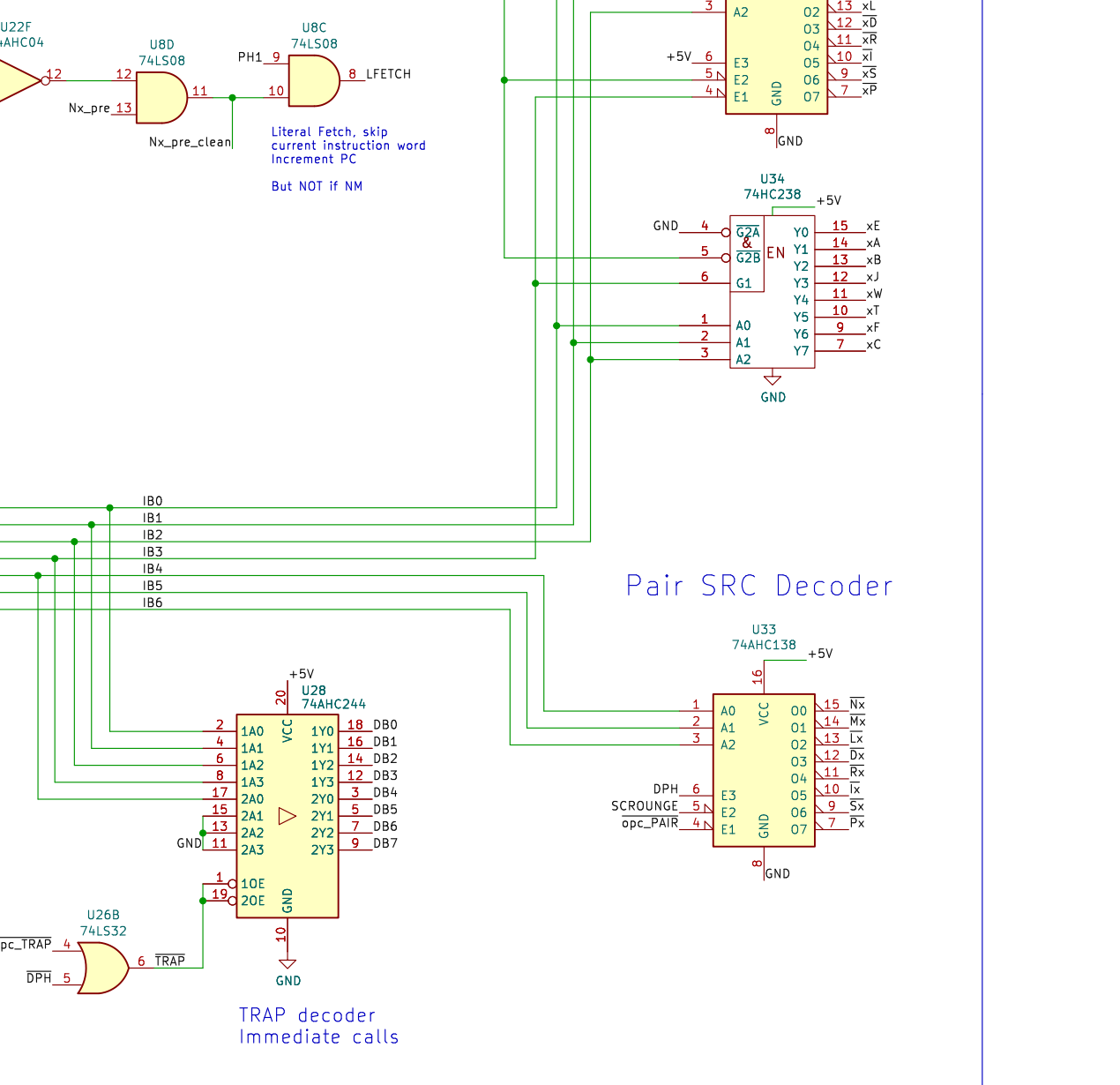
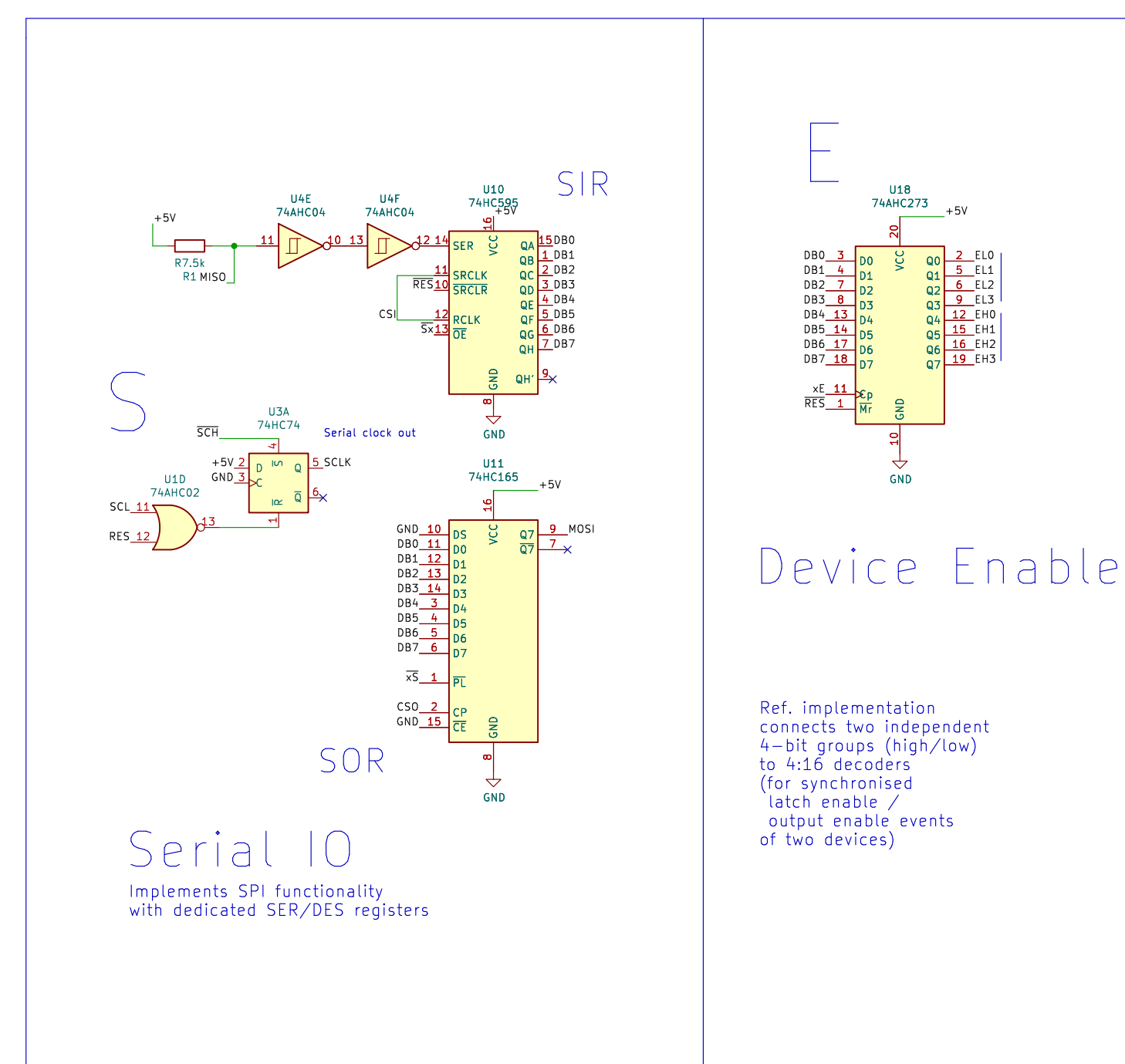
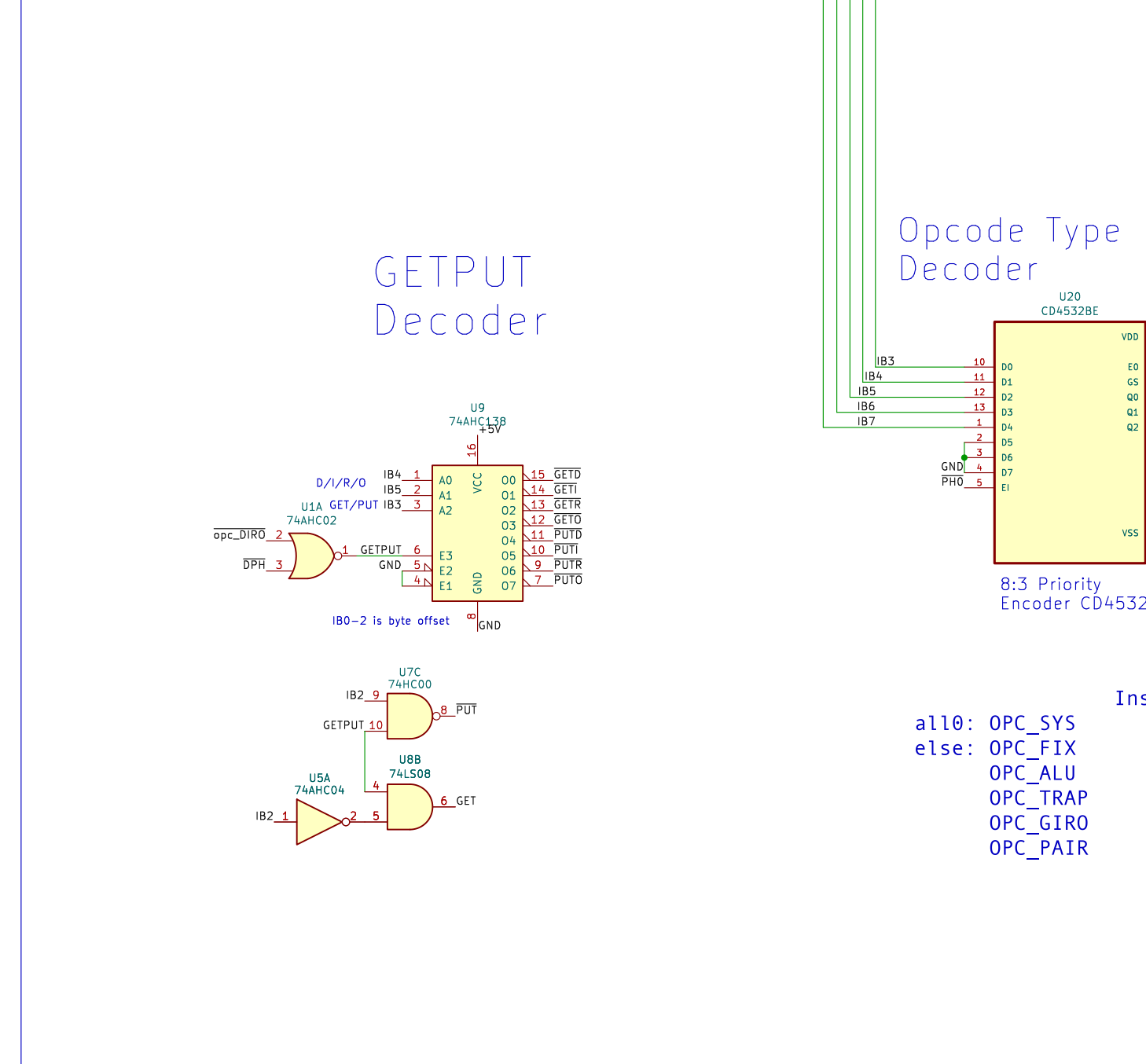
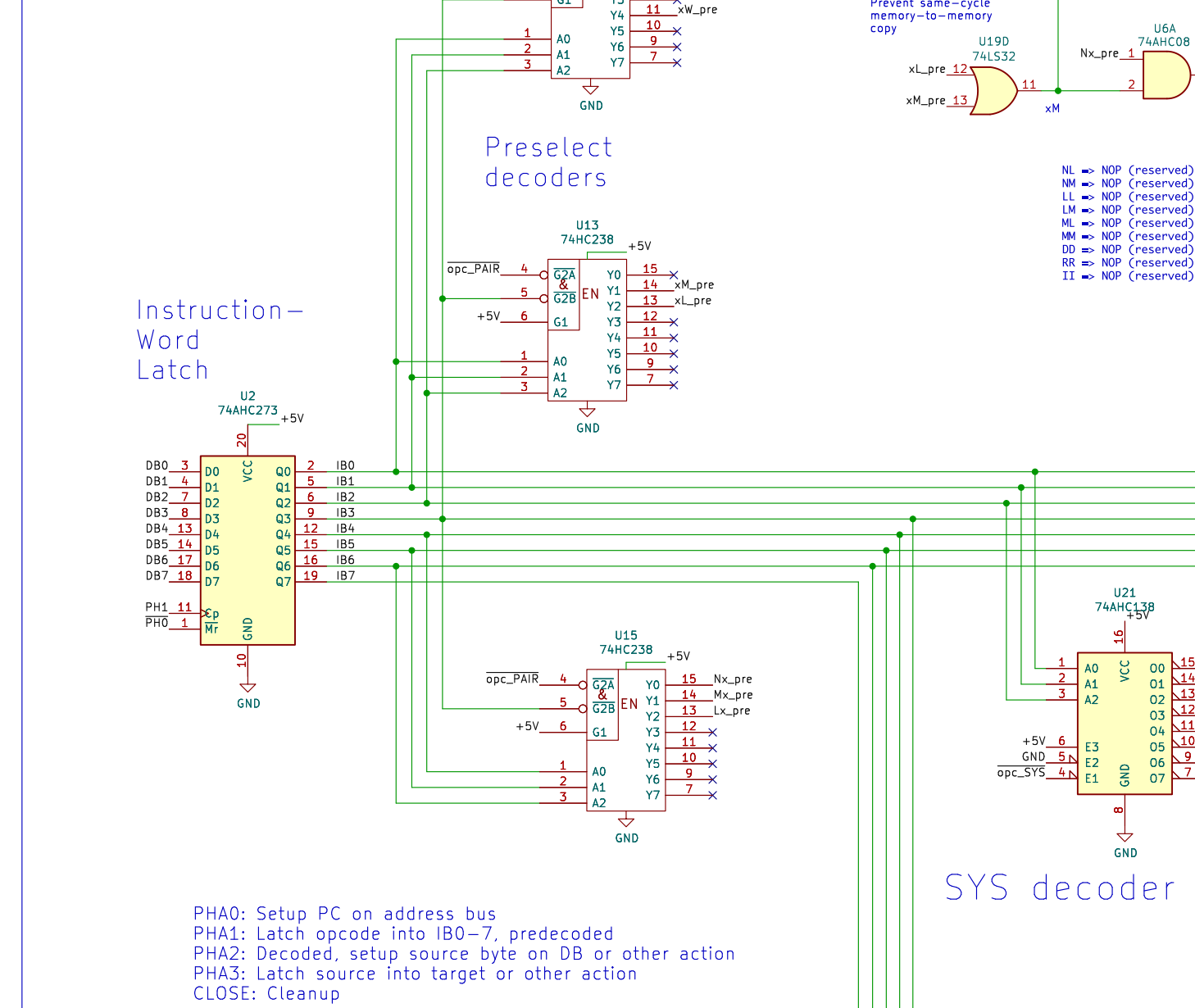
## Rev. Myth/LOX



## Instruction Decoder

Scrounger

Remap 'scrounger' to 66, 68, 69, etc. and impracticable opcodes such as NM



### Implementation Semantics:

CALL/TRAP: L <= PC, Co <= C, decrement L, PC <= 0, C <= DB0-7  
RET: C <= L7, PC <= L, increment L  
COR: C <= D, PC <= L  
OWN: L7 <= Co

x8 does PC-relative in-page branching