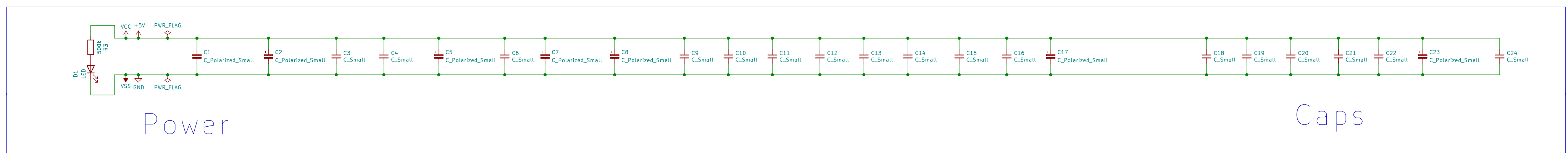


Sonne Microcontroller Reference Schematics rev. Myth



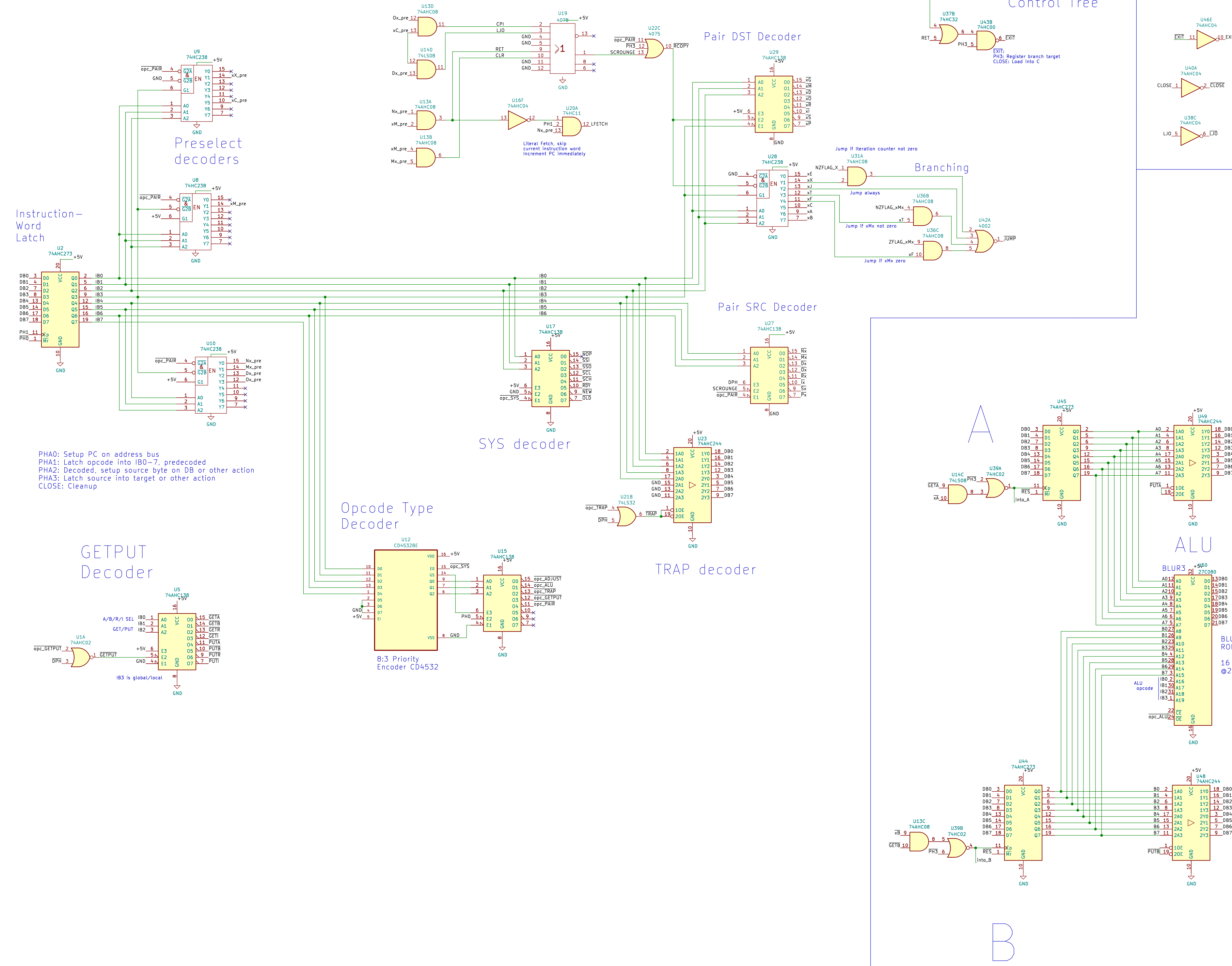
Scrounger

Instruction Decoder

Control Tree

Inverters

O(rgin) Register



R Adjust

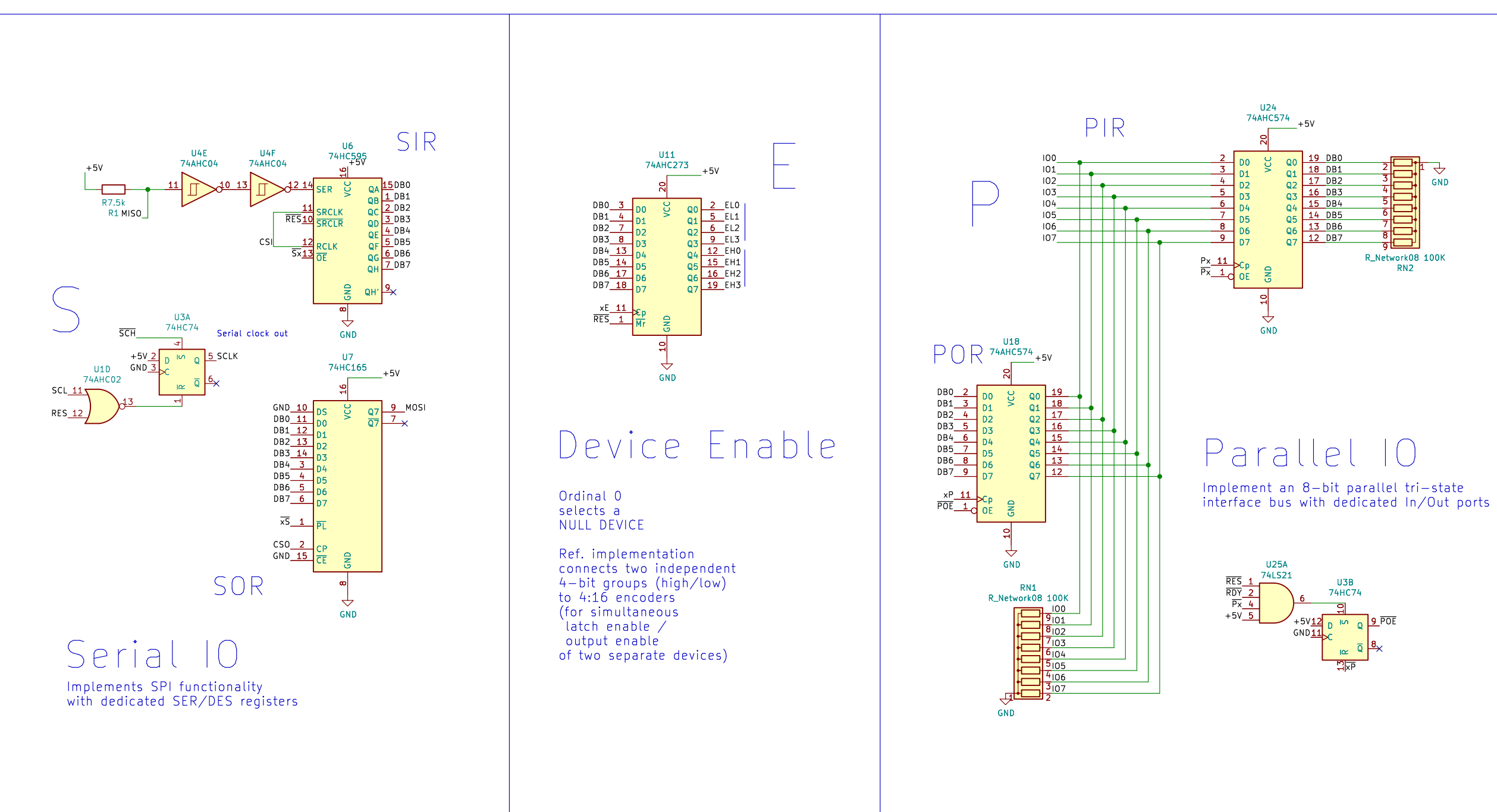
Storage

M

Memory

R(result) Register

High



Glue Logic

X (Iteration Register)

L(ocal) page index Stack Pointer

FRO-7 Bus Frame/Record

D

Spare Units

