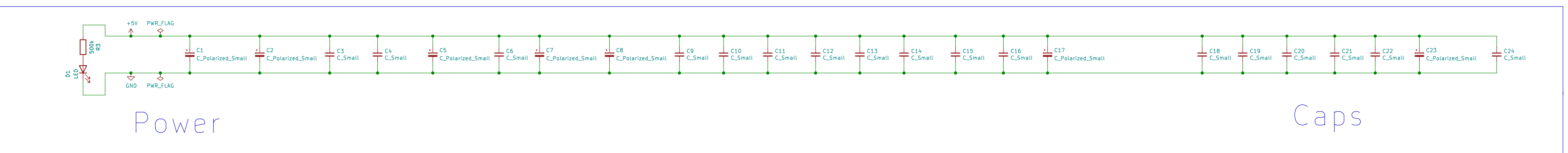
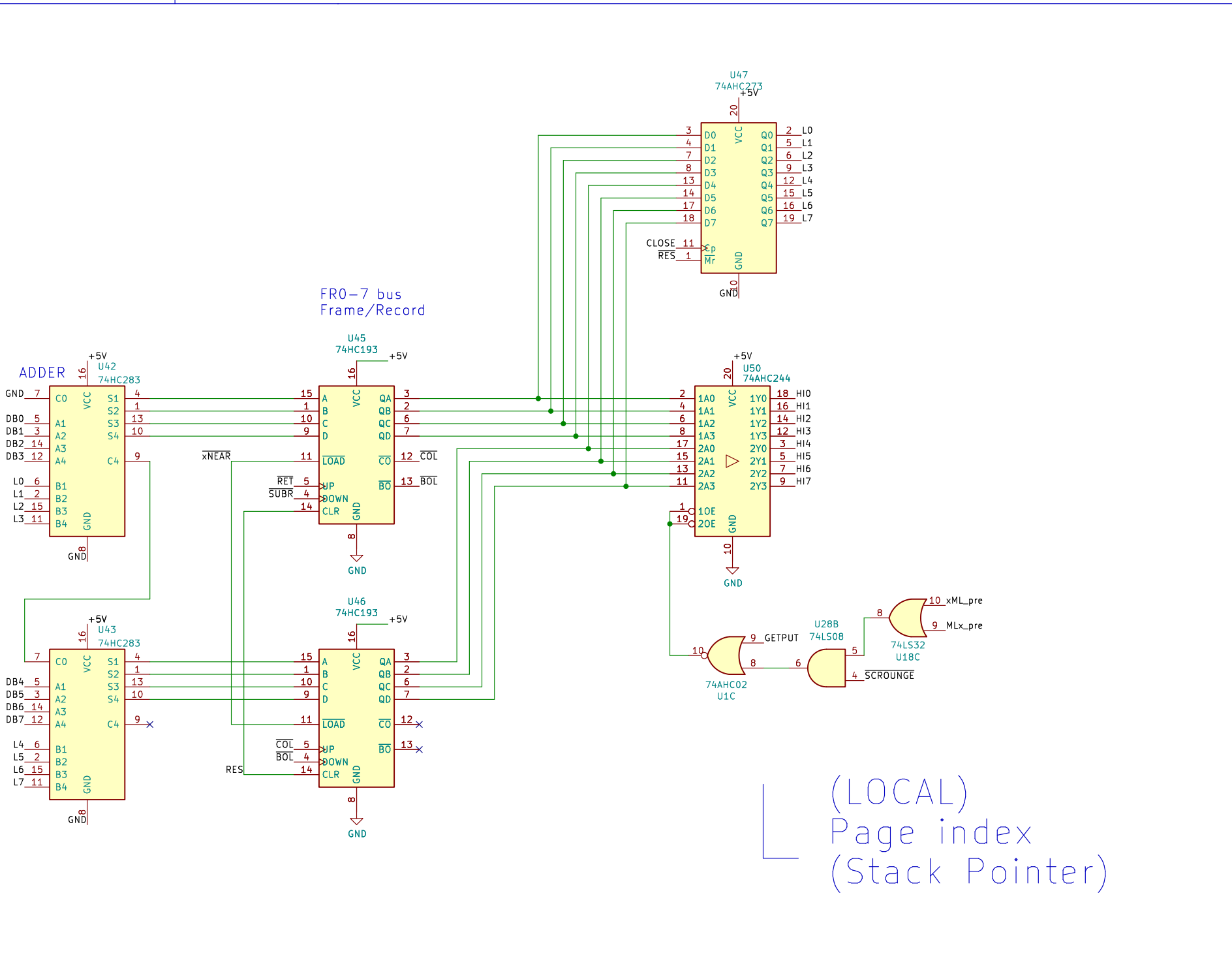
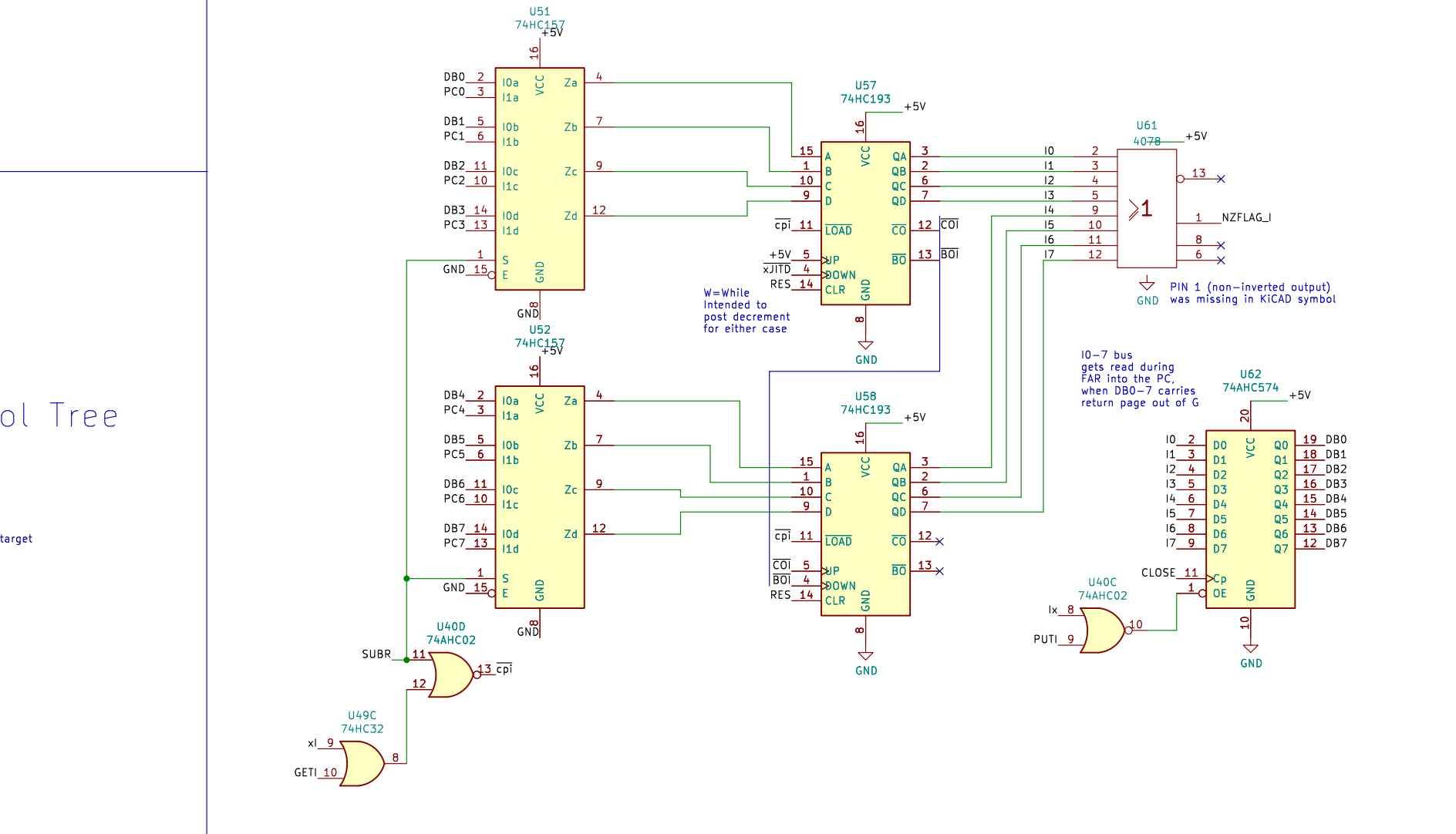


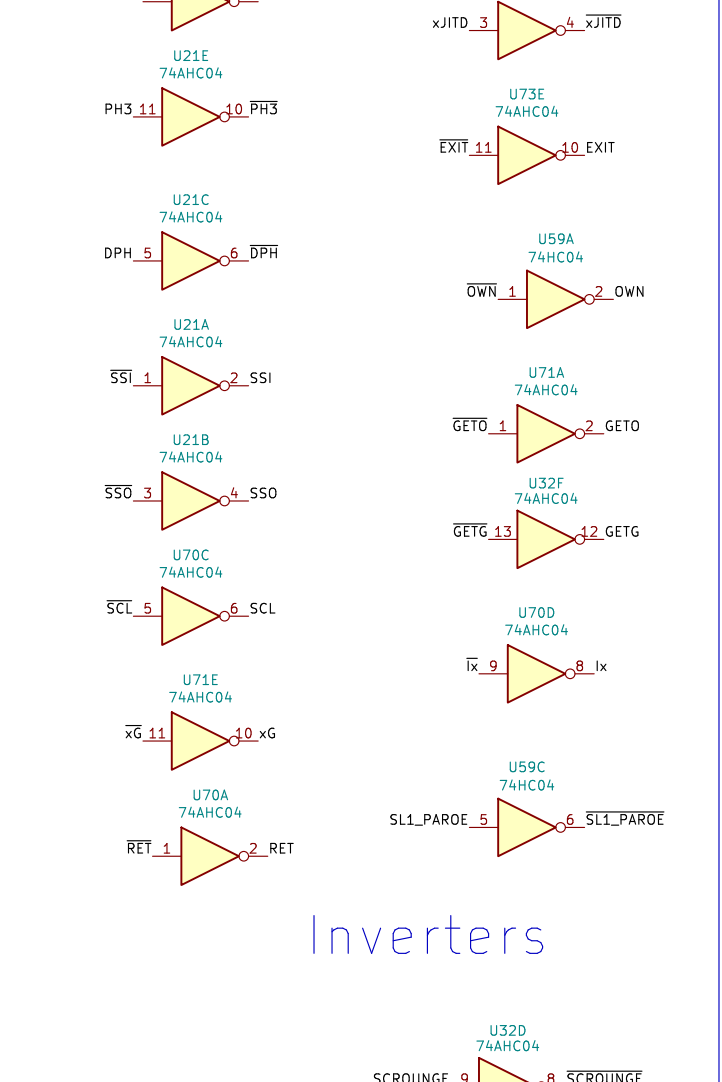
Sonne-8 Microcontroller Reference Schematics Rev. Myth/LOX



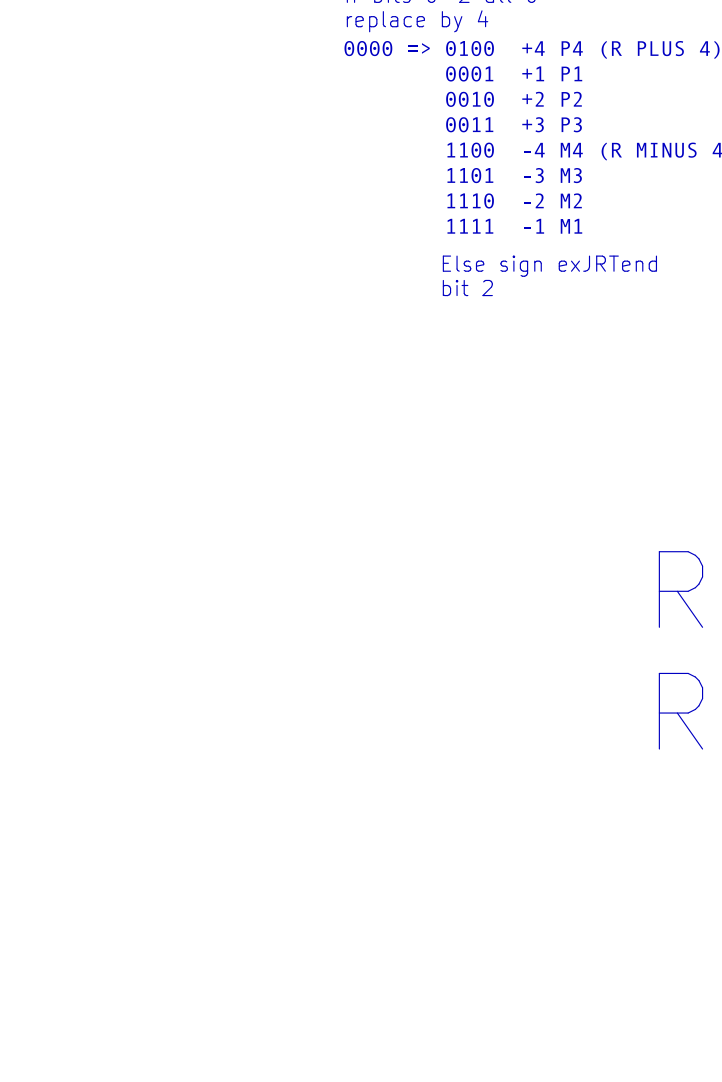
I (INNER Loop Register)



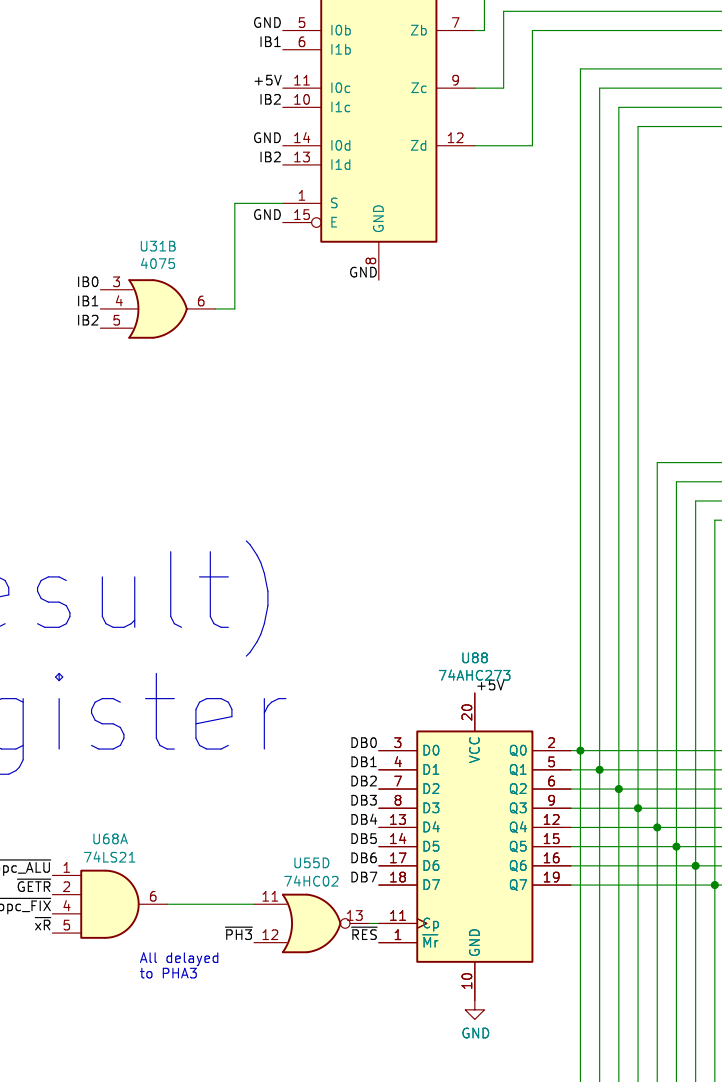
R-Fix



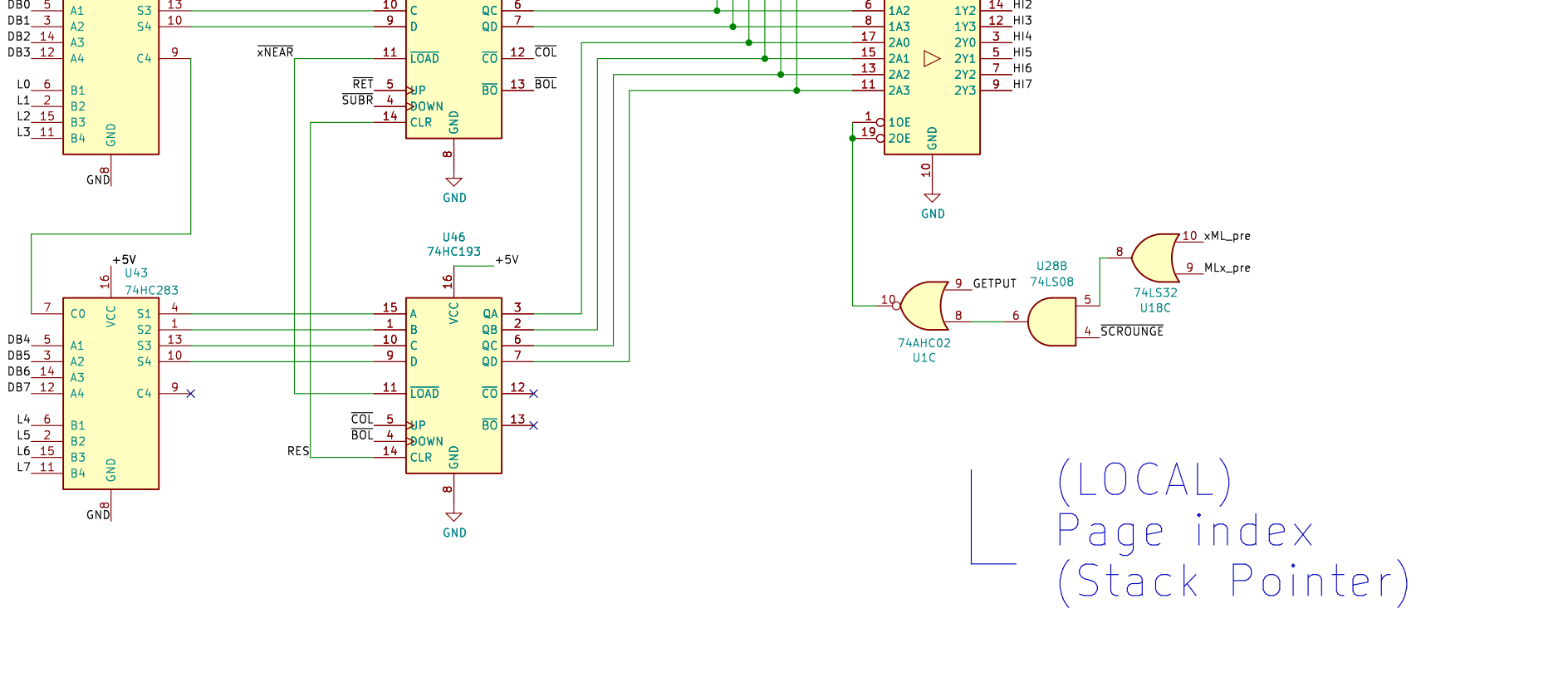
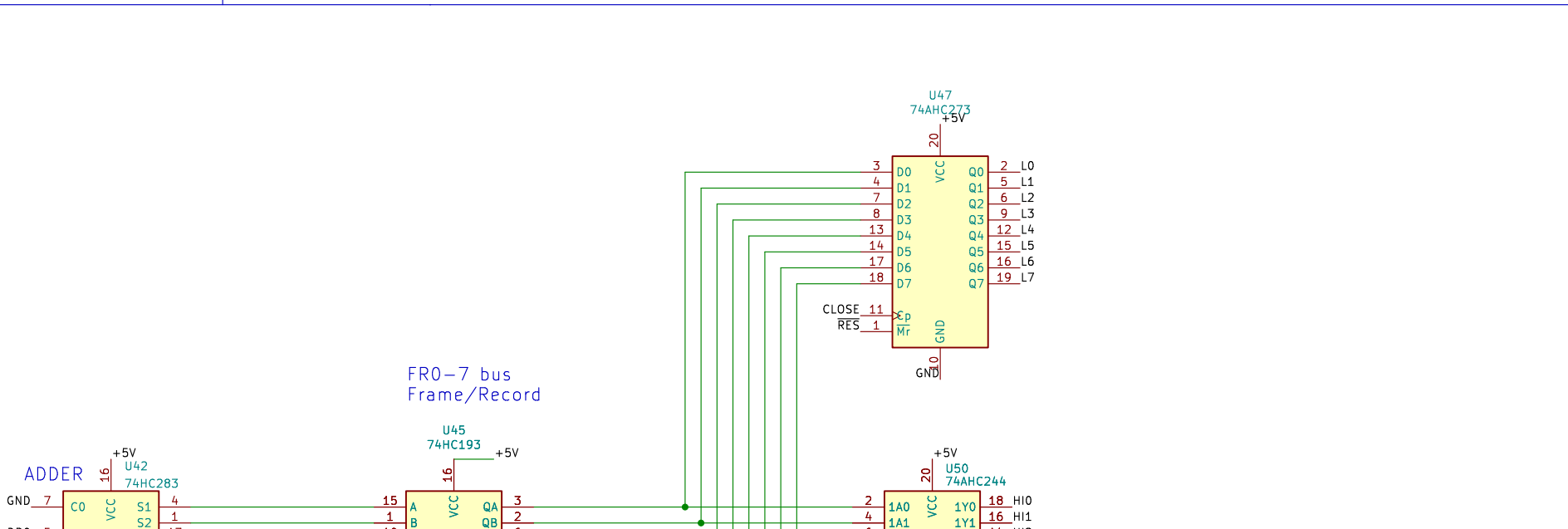
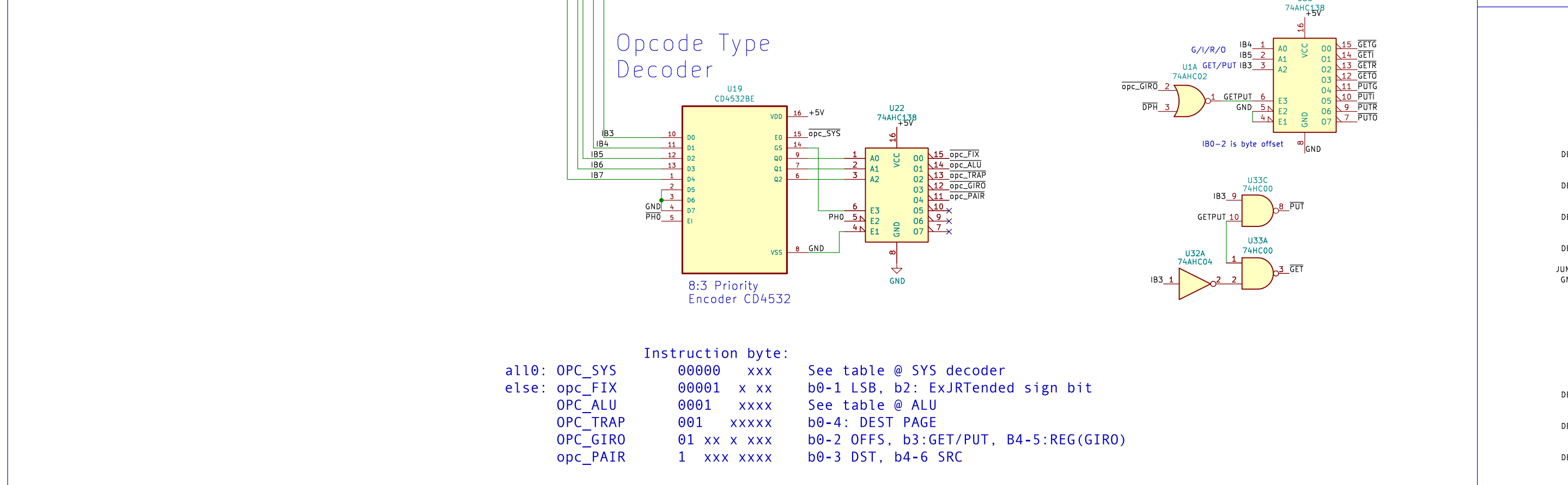
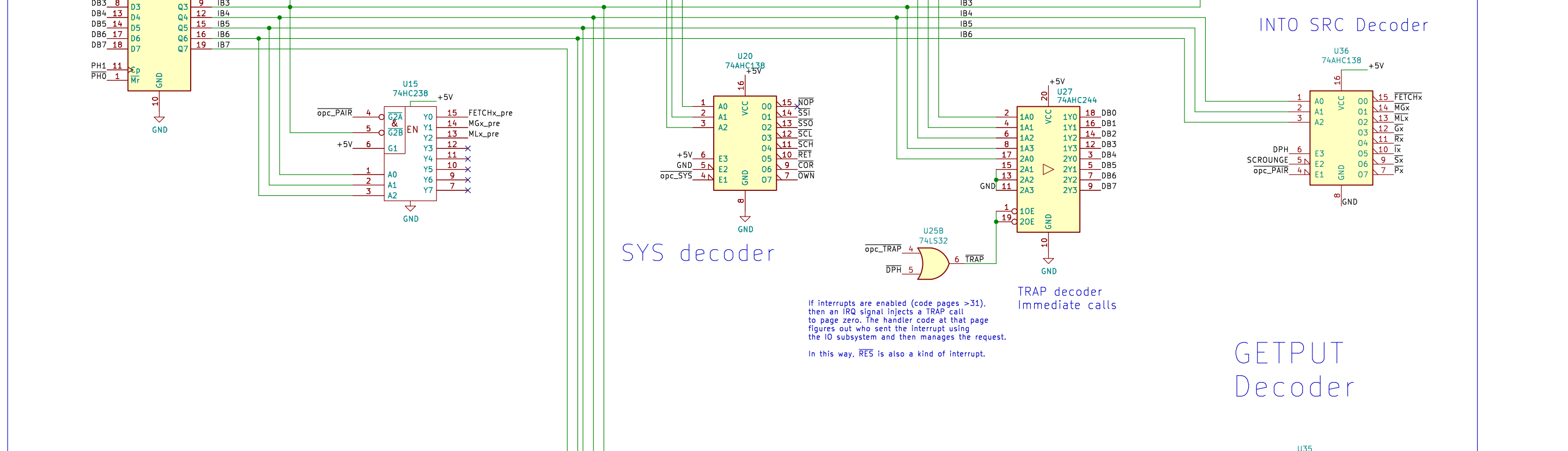
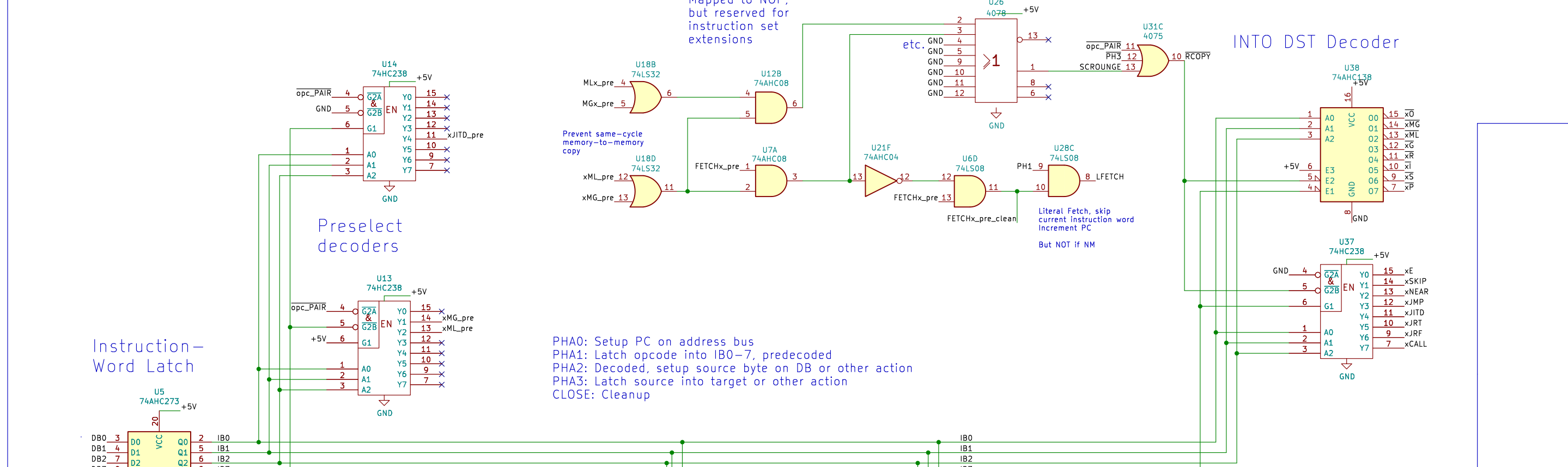
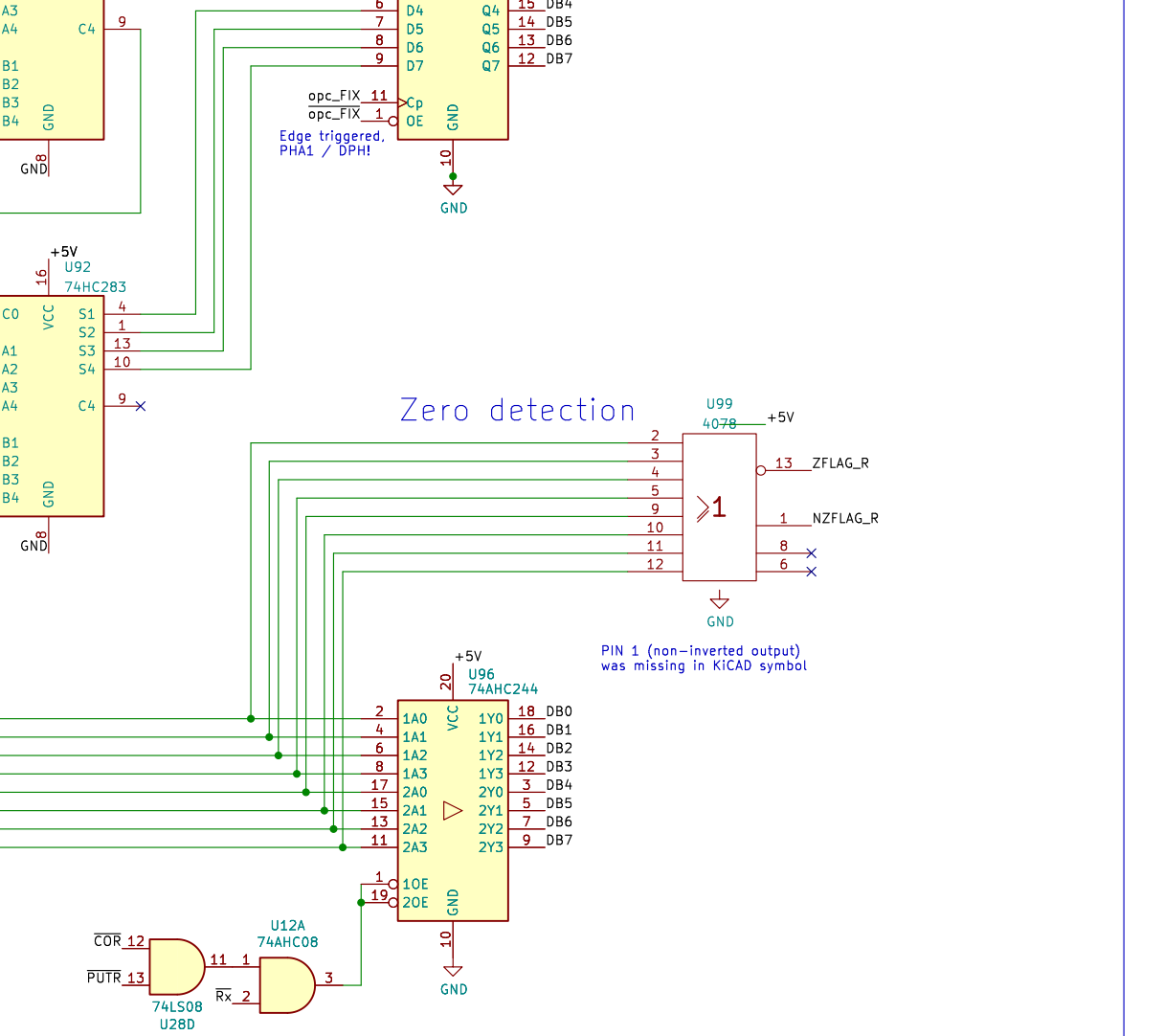
R(result) Register



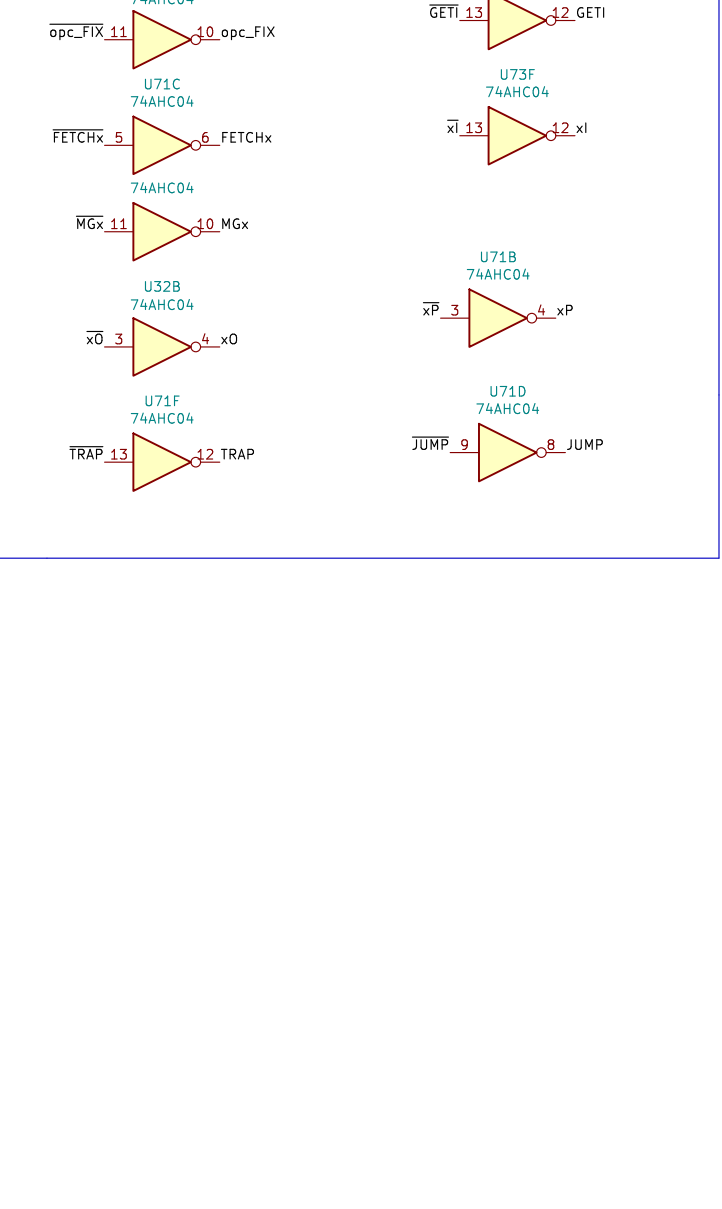
O (OFFSET) Register



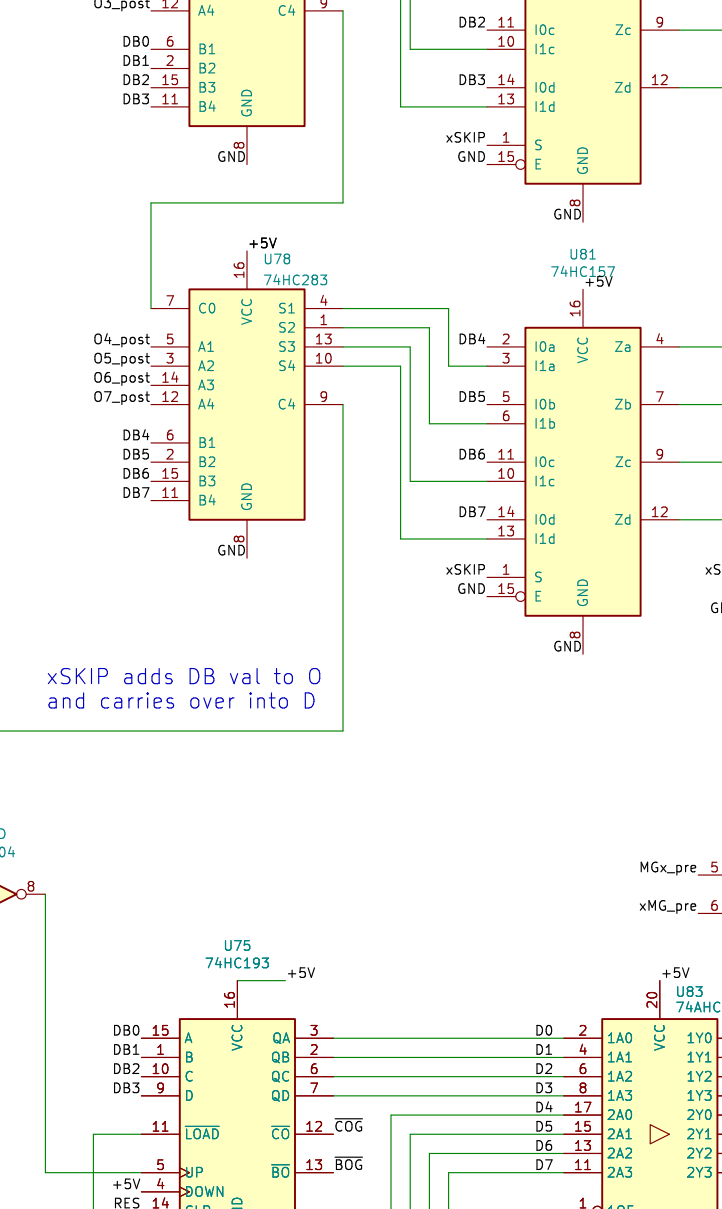
ALU



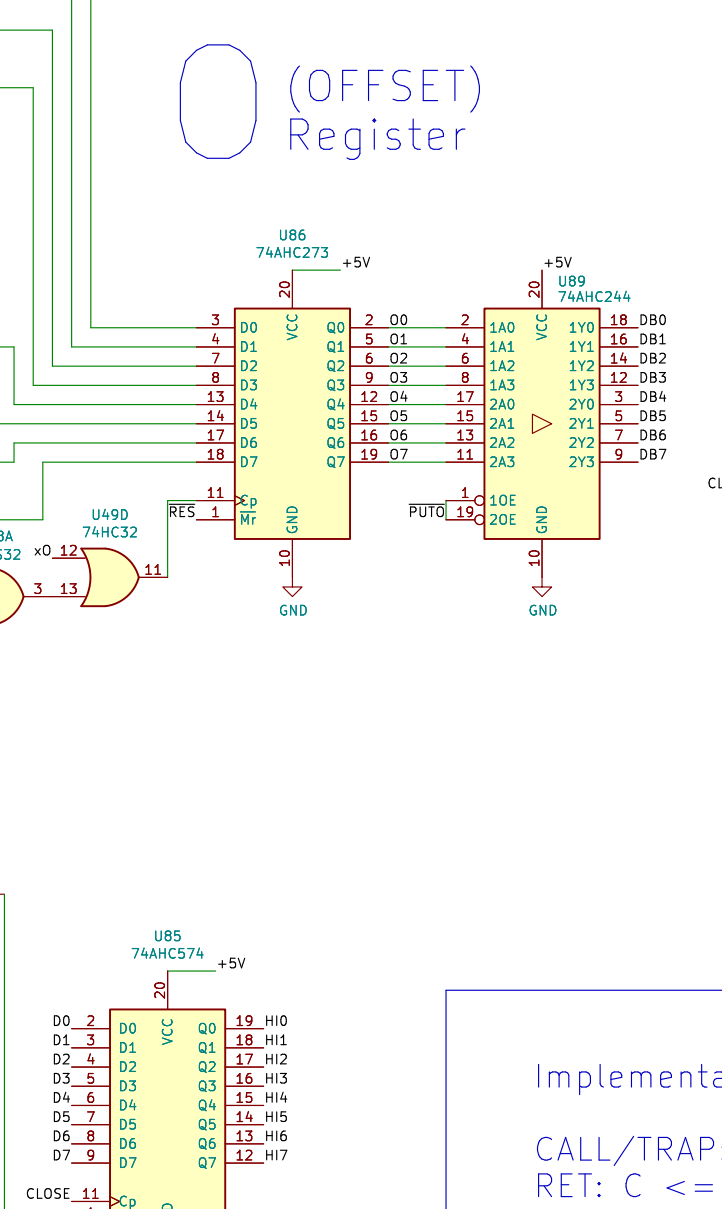
Inverters



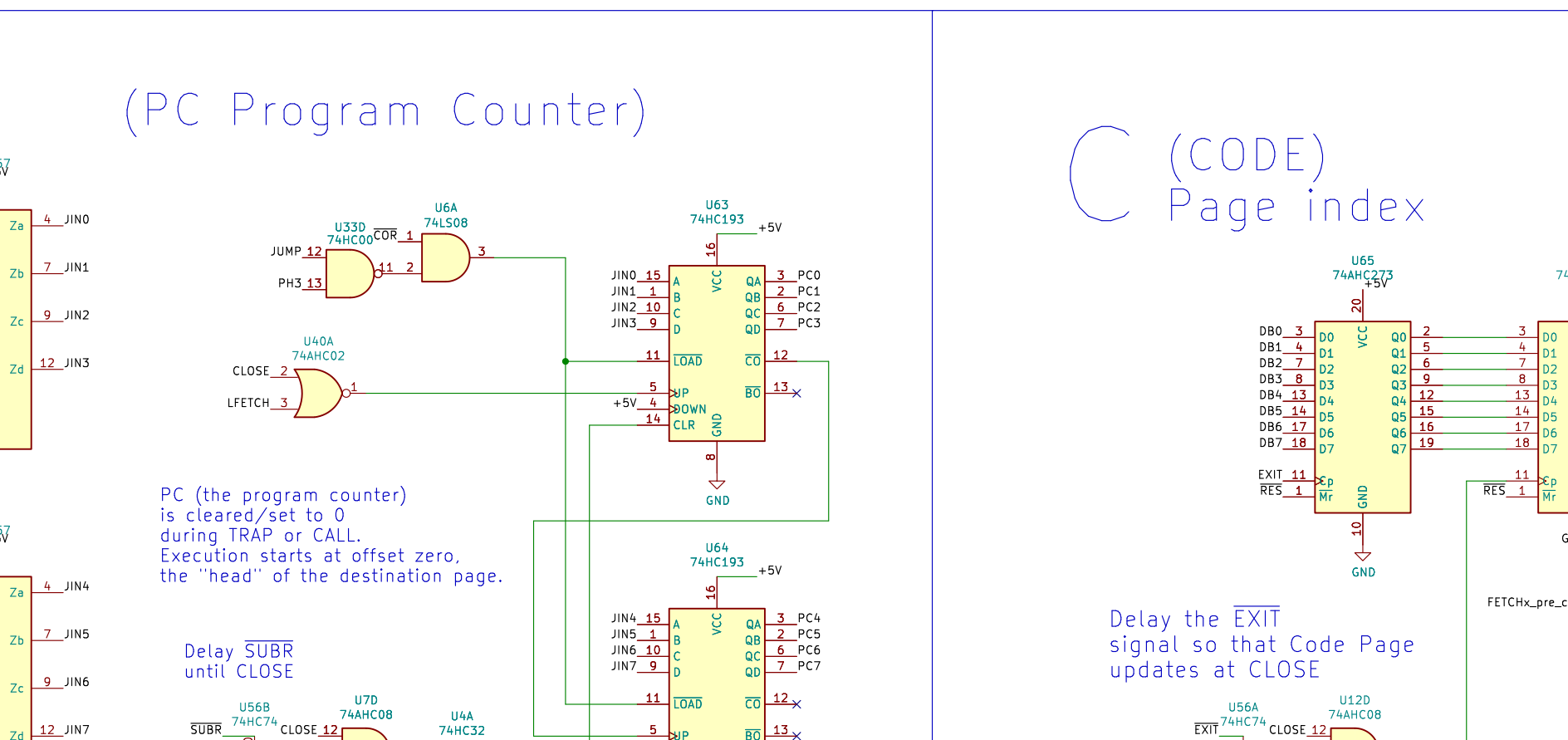
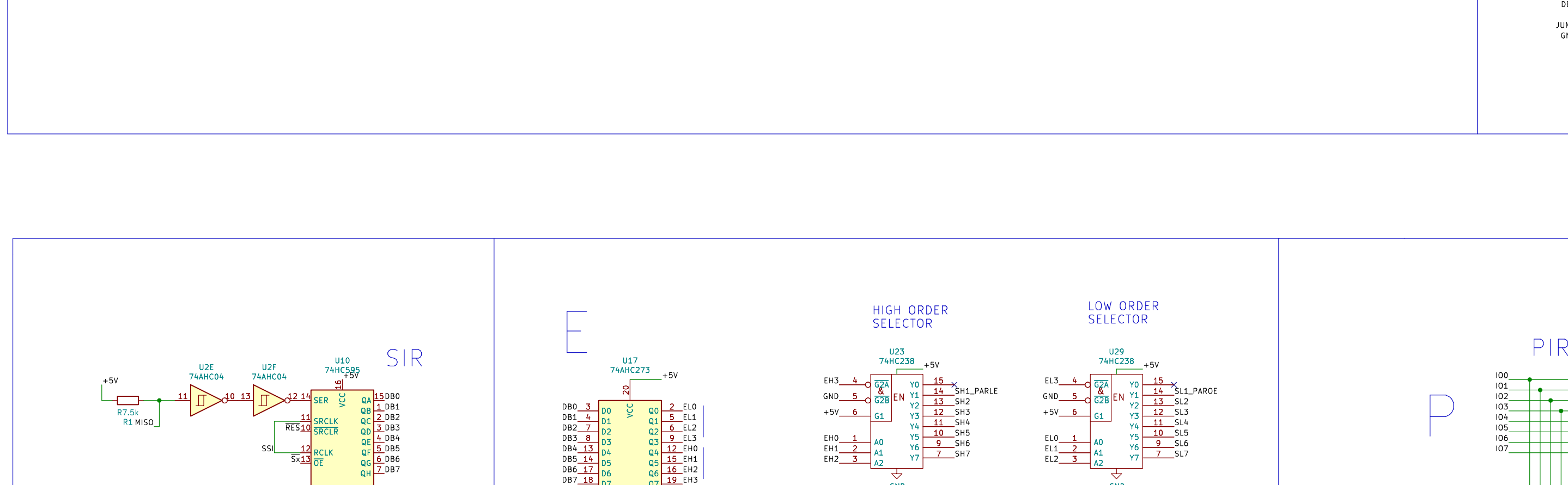
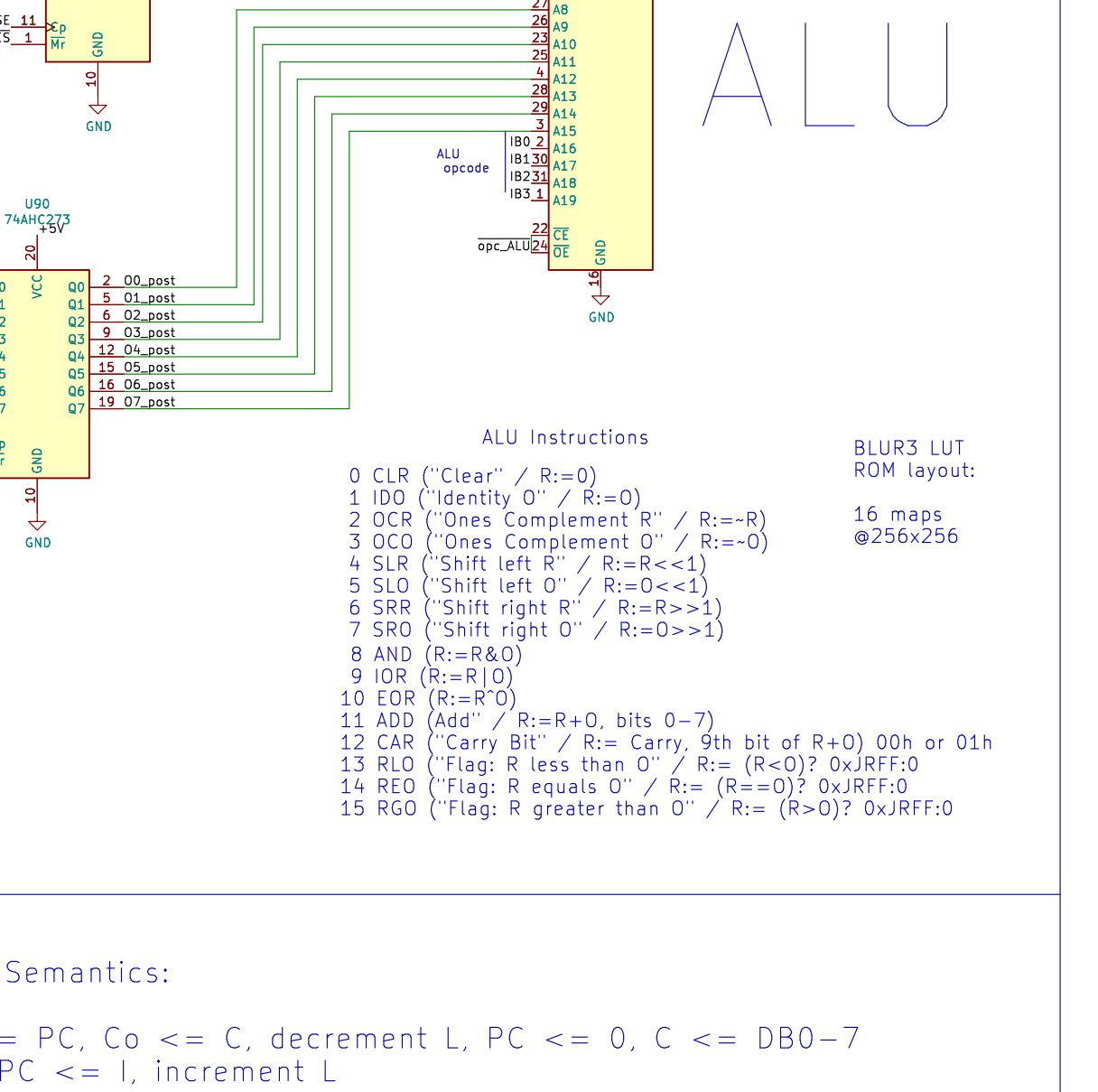
ALU Instructions



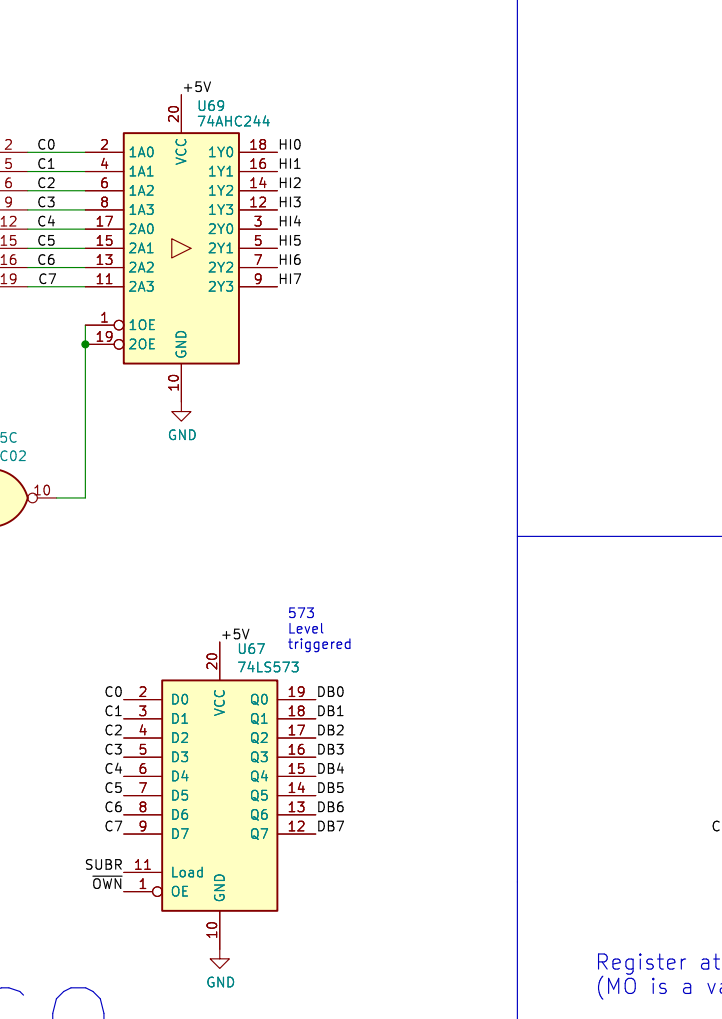
ALU Instructions



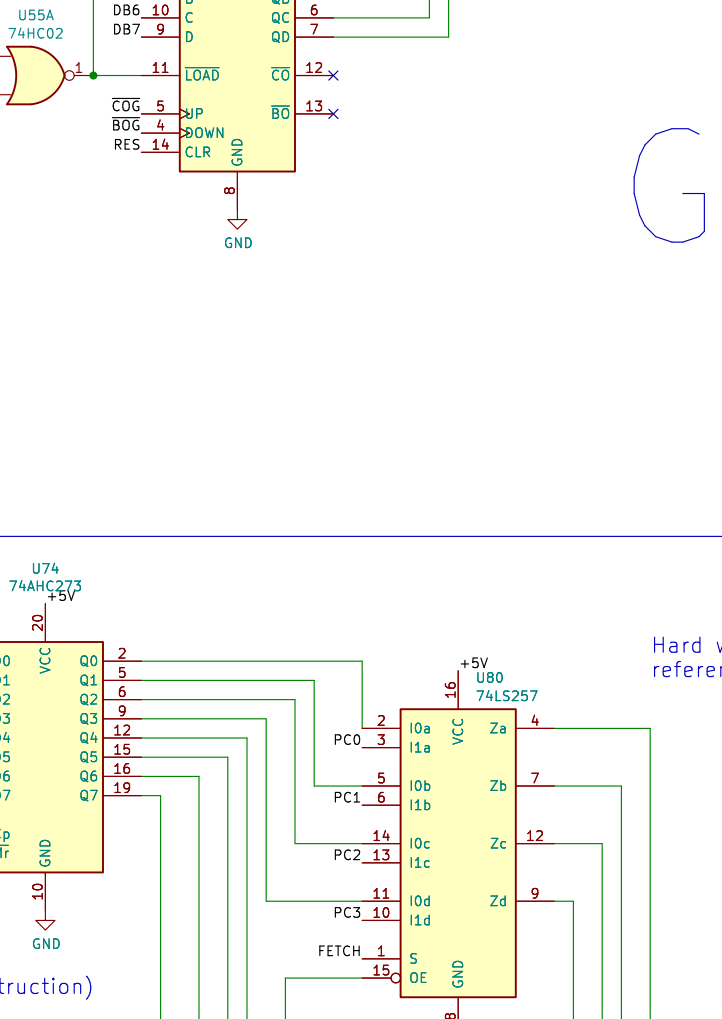
ALU Instructions



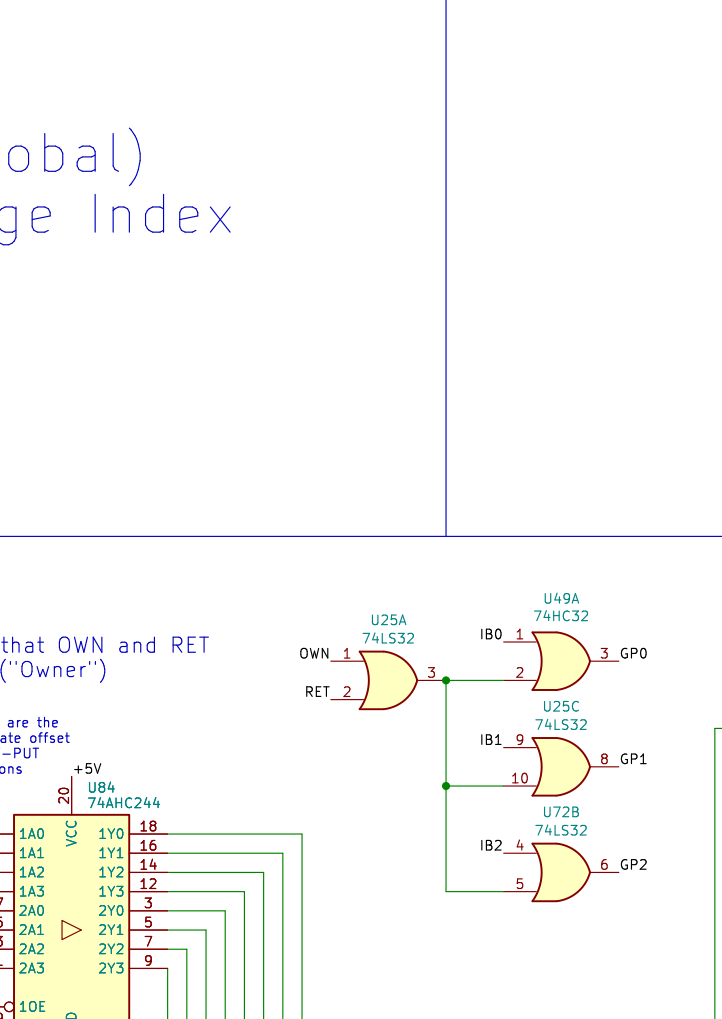
CO (CODE) Page index



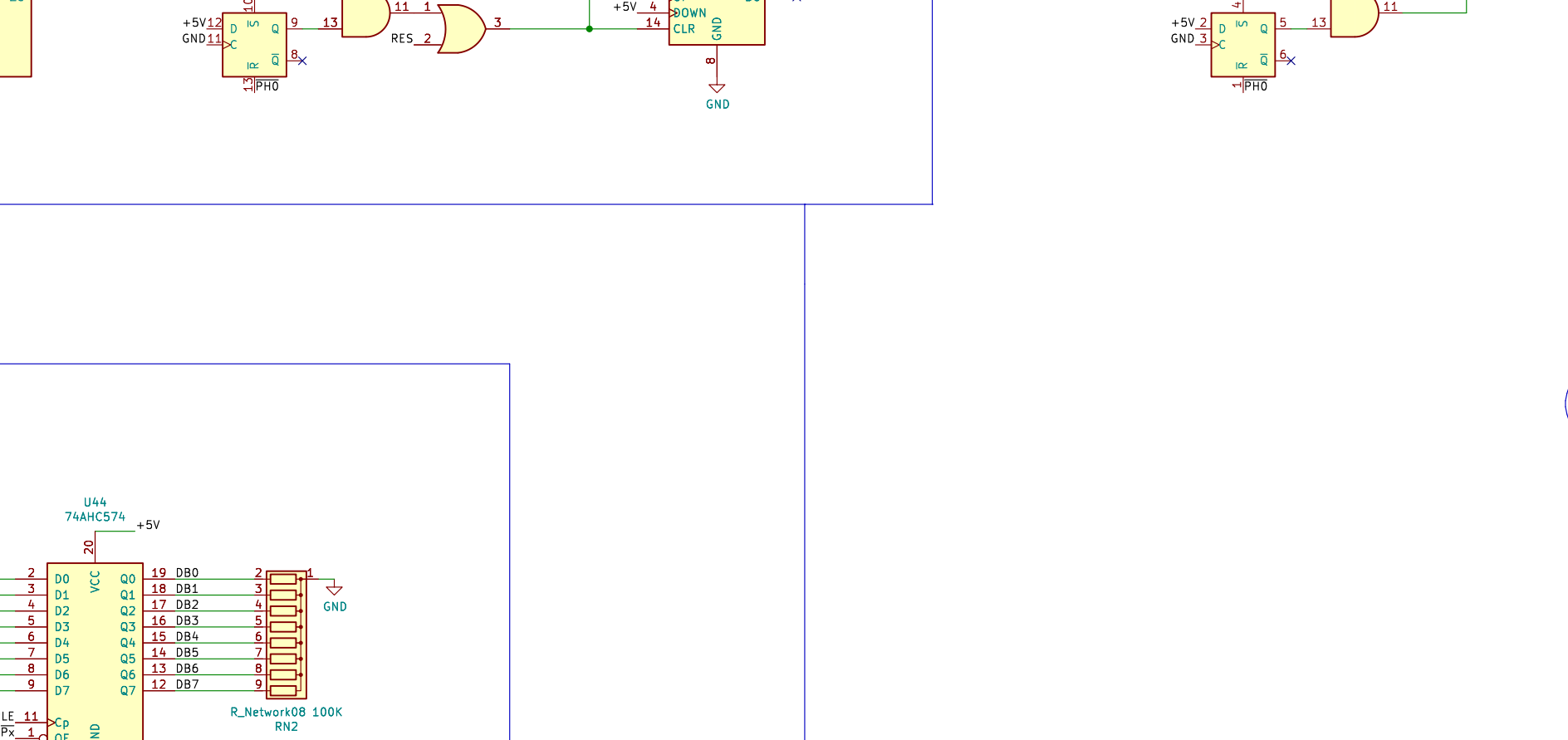
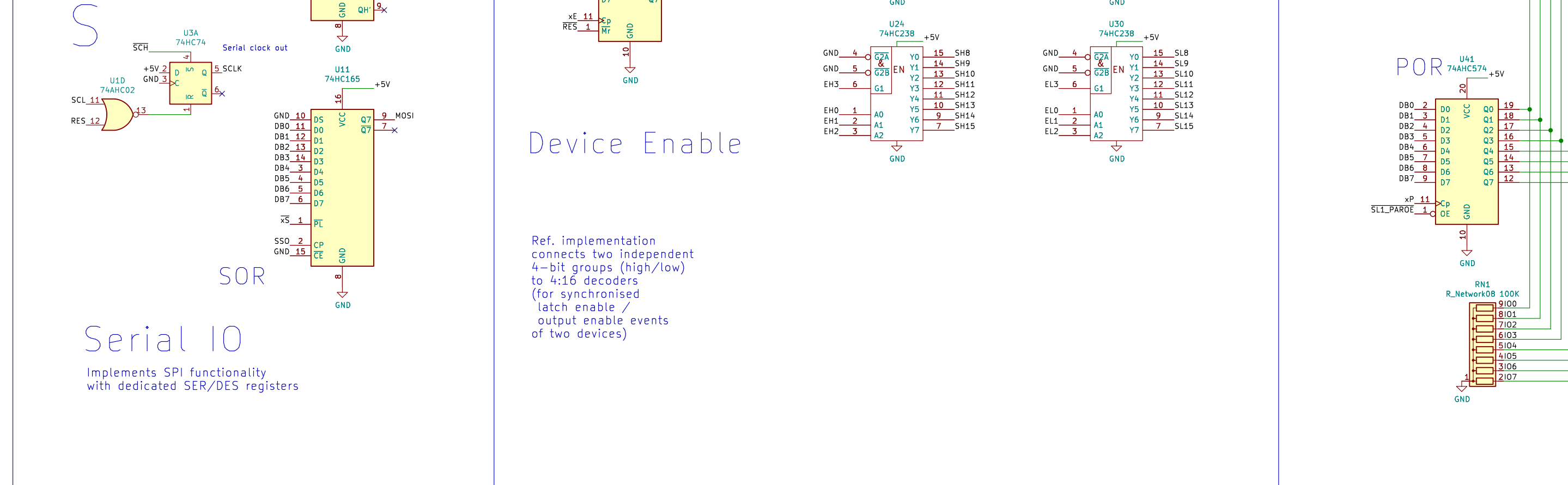
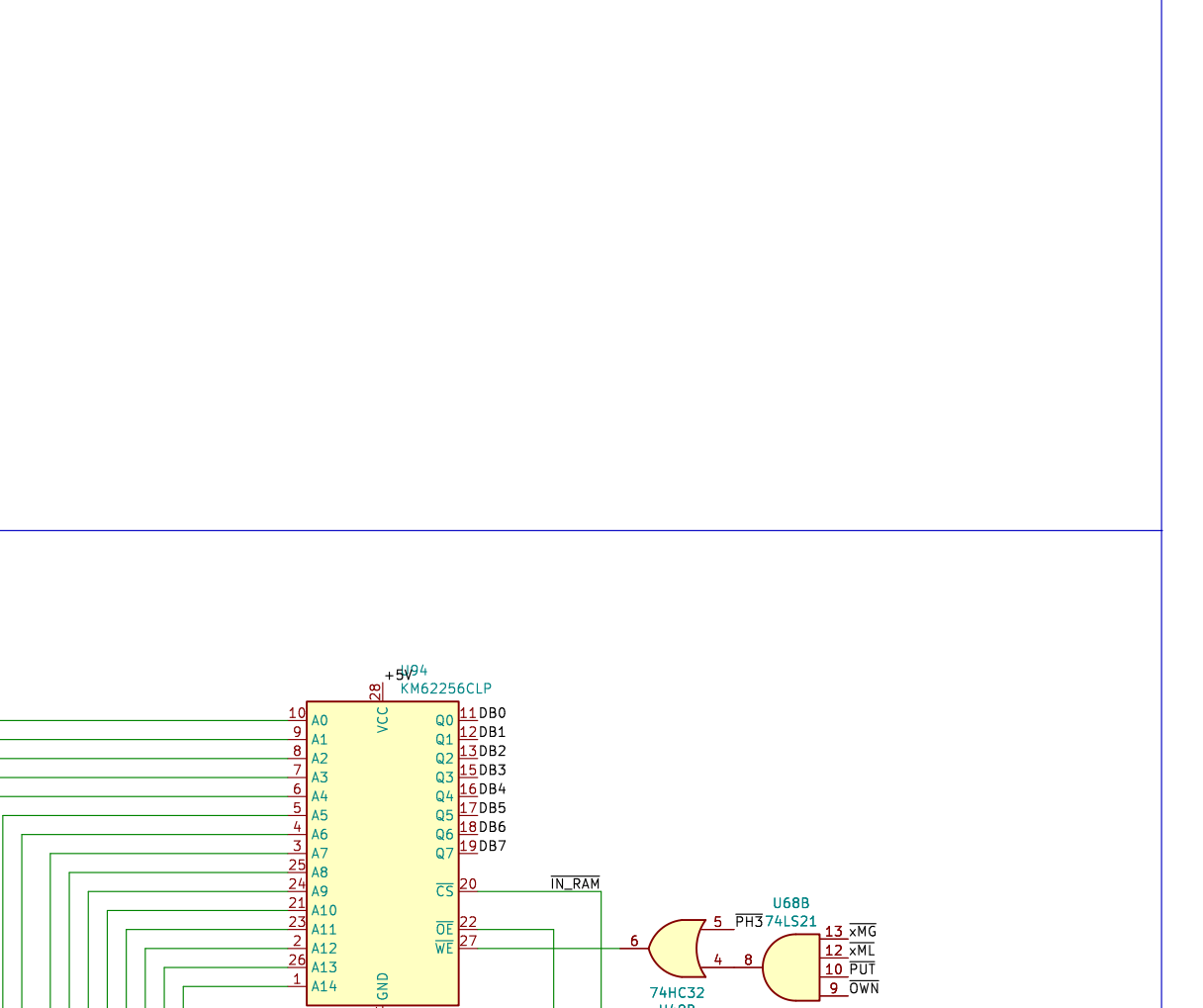
G (Global) Page Index



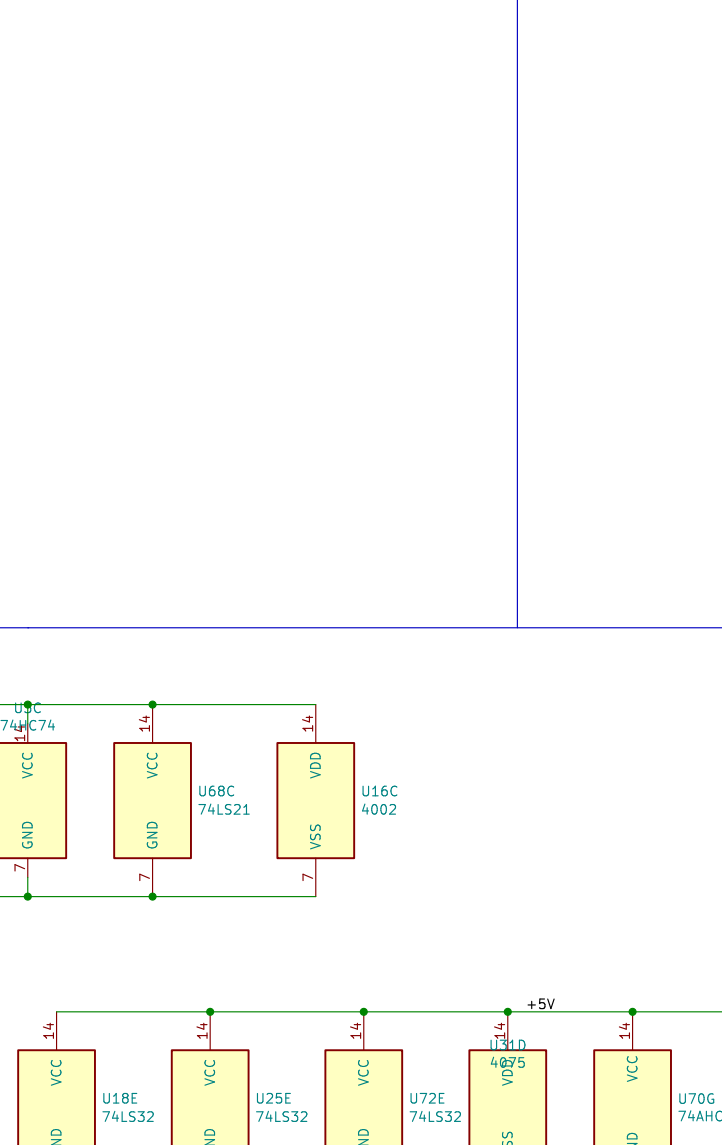
Low



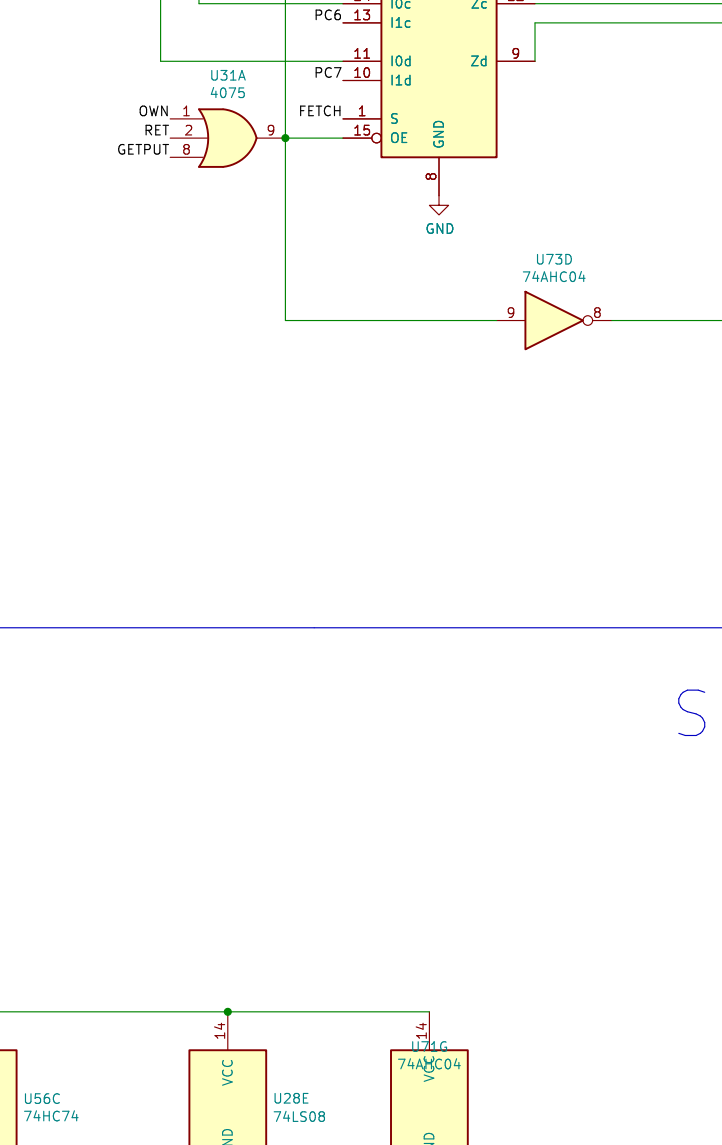
Memory M



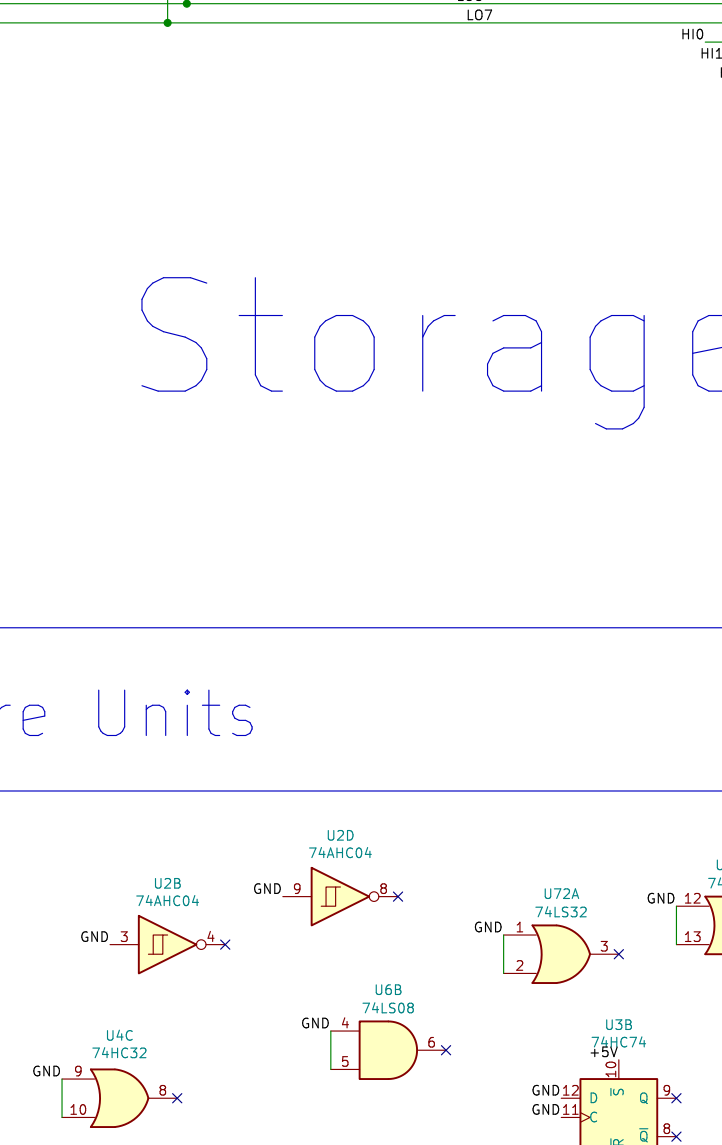
CO (Coroutine Latch)



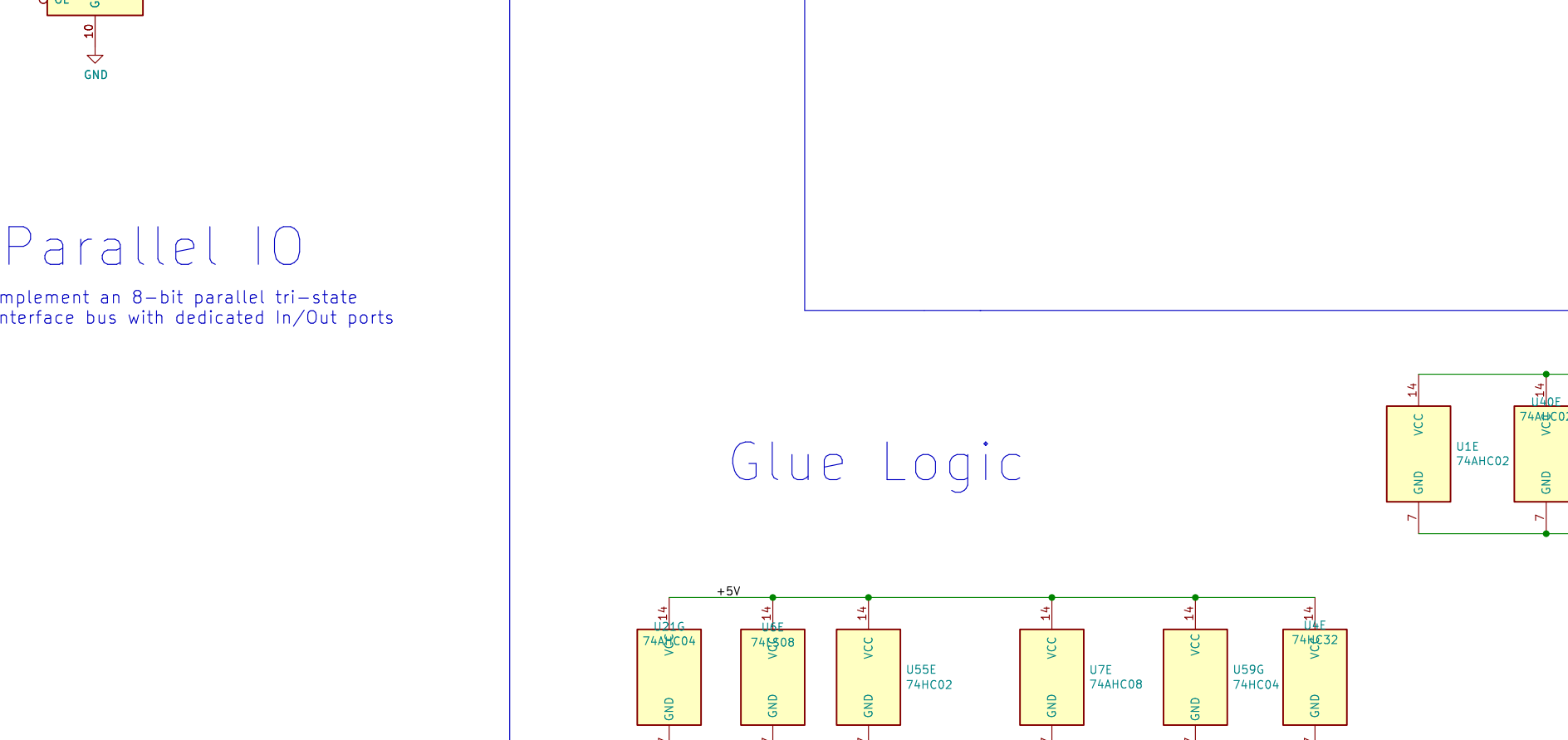
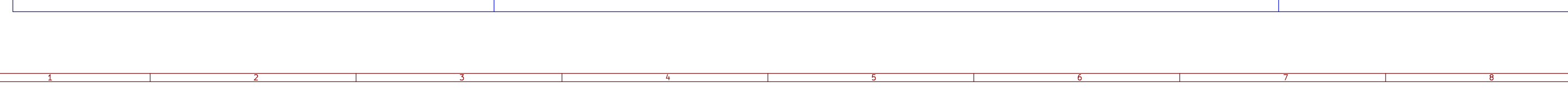
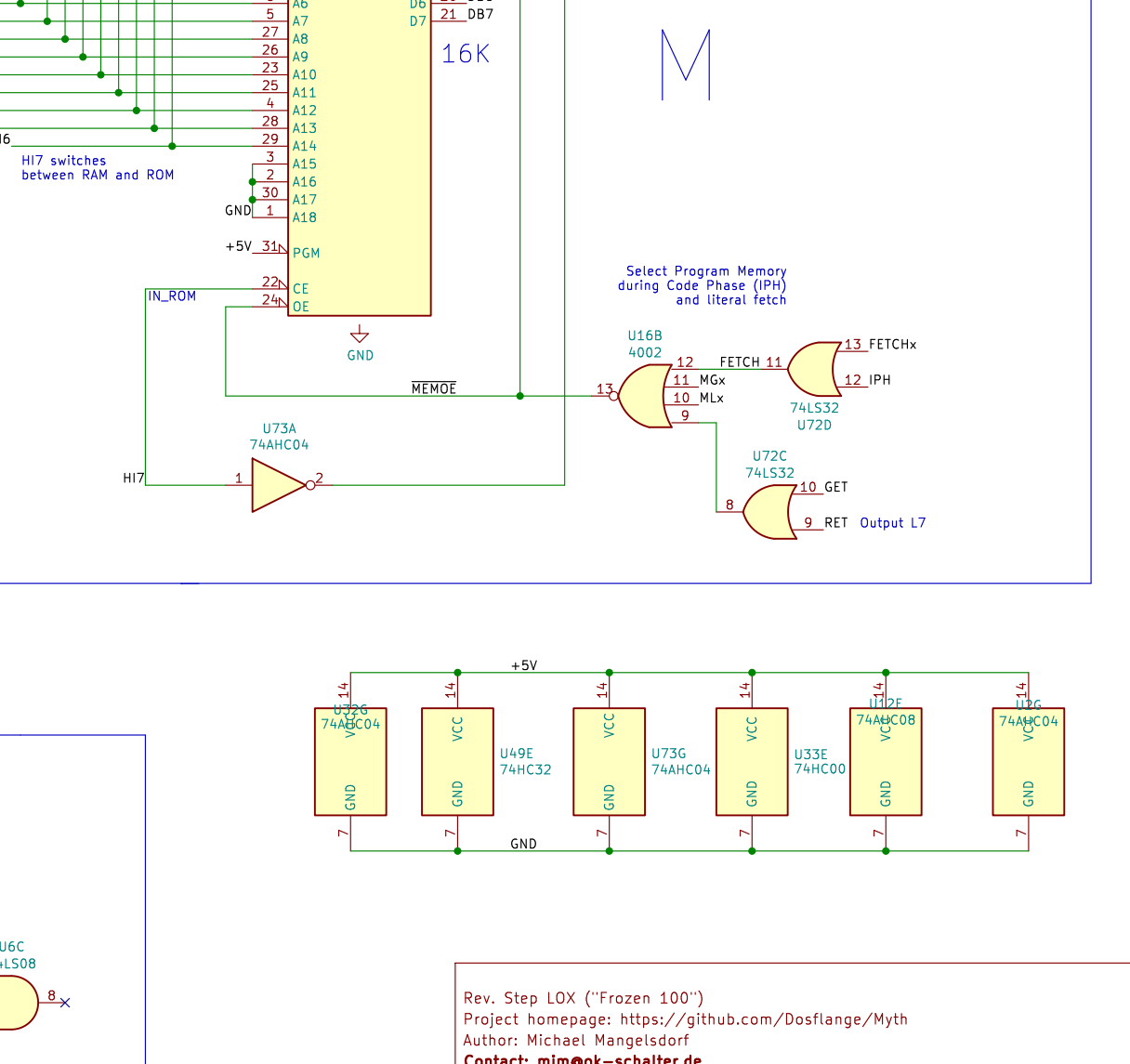
Low



Storage



Memory M



Glue Logic



Spare Units



Storage



Memory M

