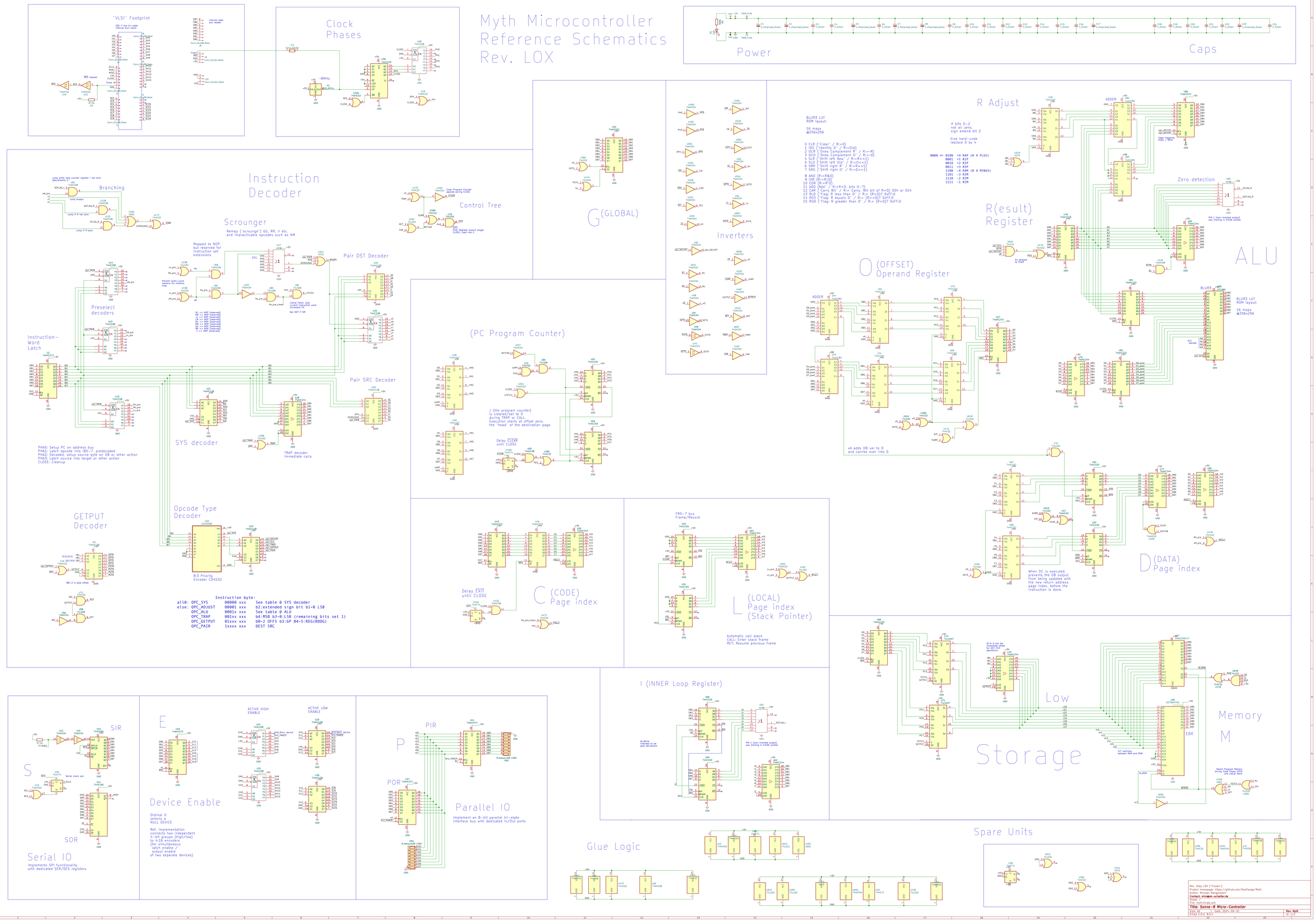
Myth Microcontroller
Reference Schematics
Rev. LOX
"VLSI" Footprint
Clock
Phases
Power
Caps
(GLOBAL)
Inverters
Instruction
Decoder
Branching
Scrounger
Control Tree
Preselect
decoders
Instruction-
Word
Latch
Pair DST Decoder
Pair SRC Decoder
SYS decoder
TRAP decoder
immediate calls
GETPUT
Decoder
Opcode Type
Decoder
(PC Program Counter)
(CODE)
Page index
(LOCAL)
Page index
(Stack Pointer)
R Adjust
R(esult)
Register
(OFFSET)
Operand Register
ALU
(DATA)
Page index
Low
Storage
Memory
M
I (INNER Loop Register)
Serial IO
SIR
SOR
Device Enable
Parallel IO
PIR
POR
Glue Logic
Spare Units