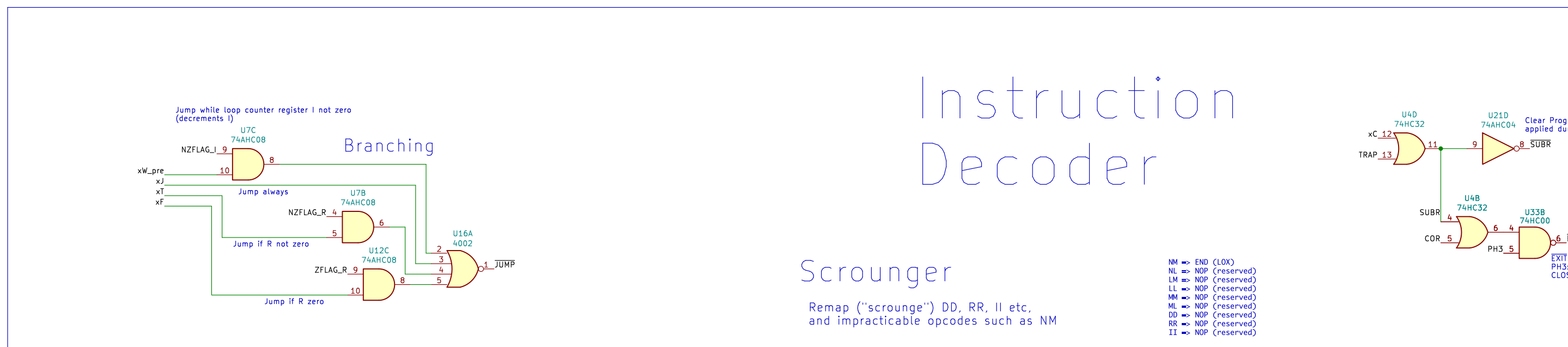
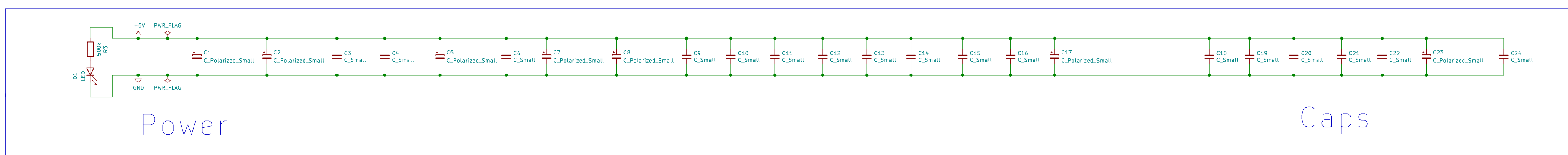
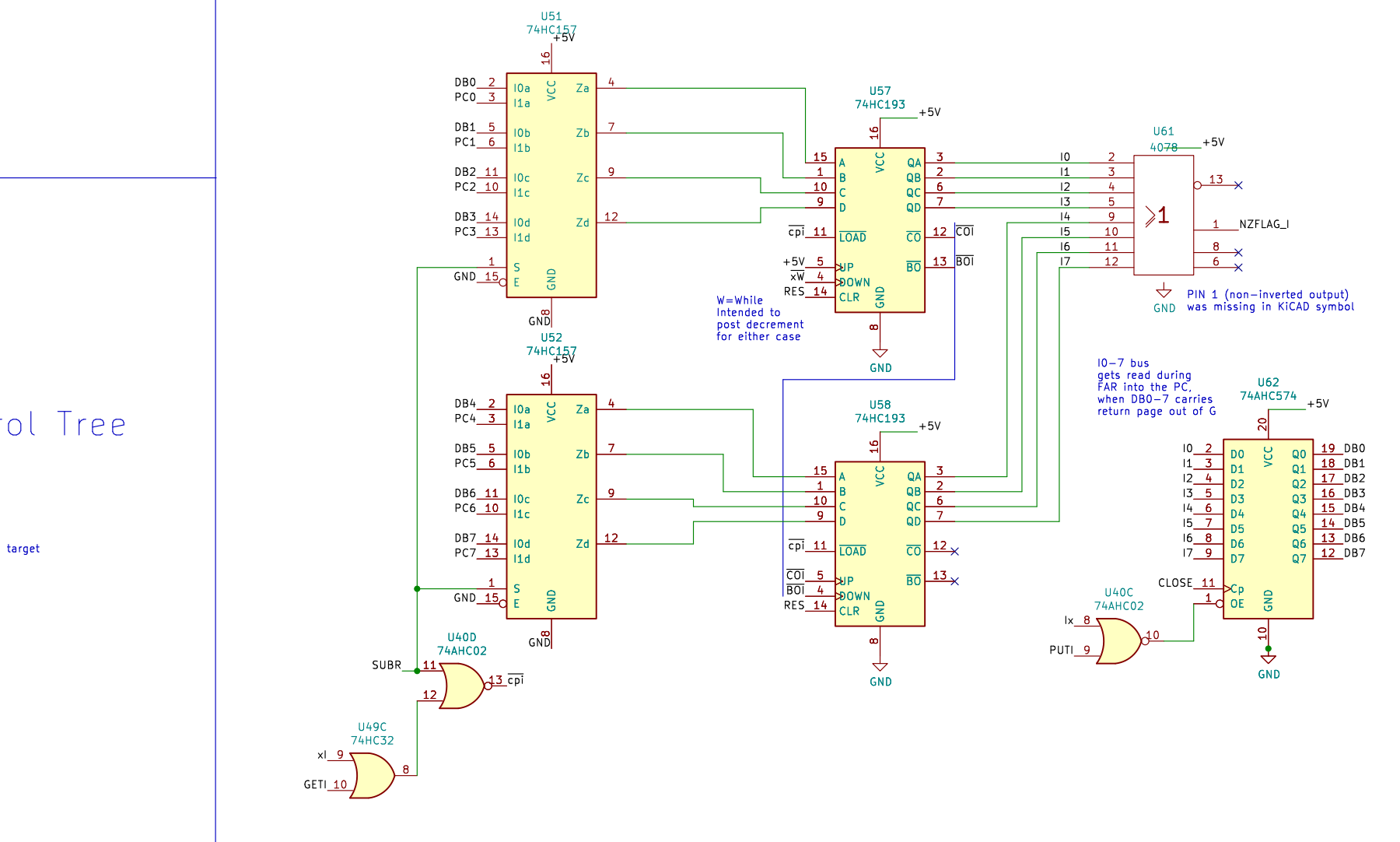


Sonne-8 Microcontroller Reference Schematics

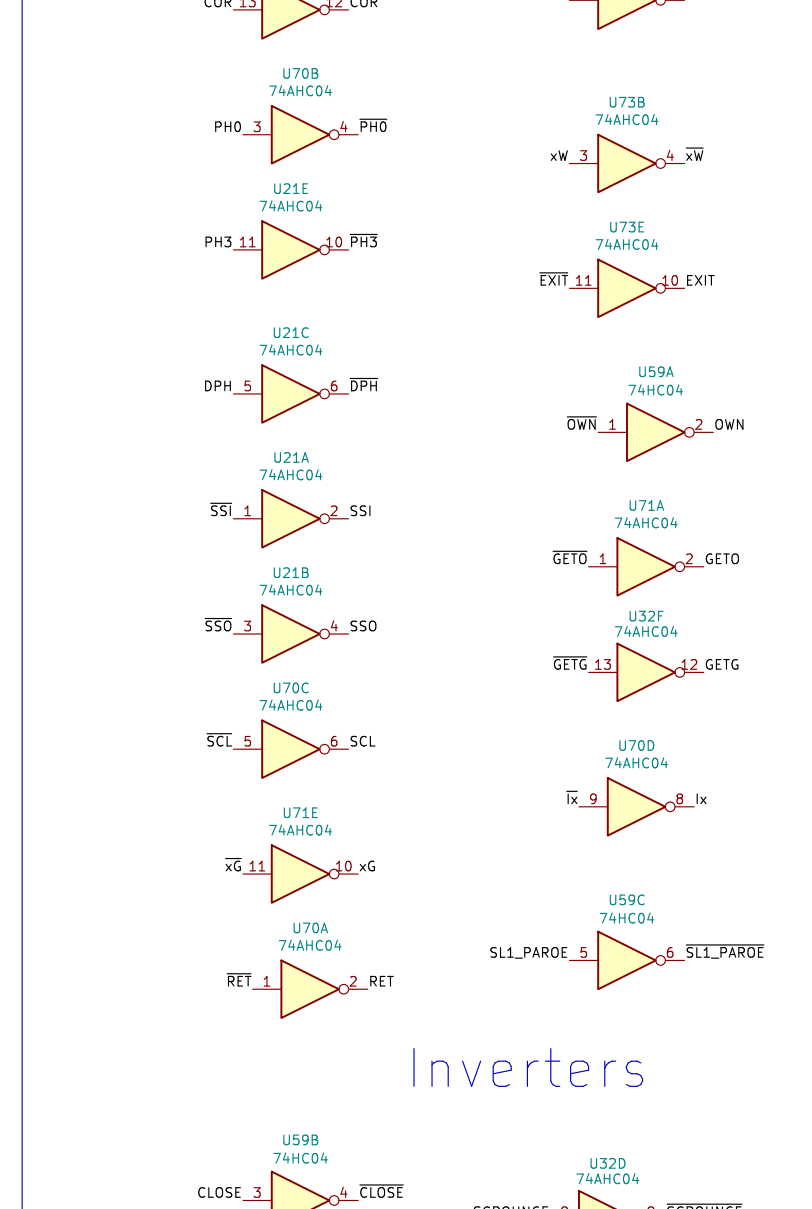
Rev. Myth/LOX



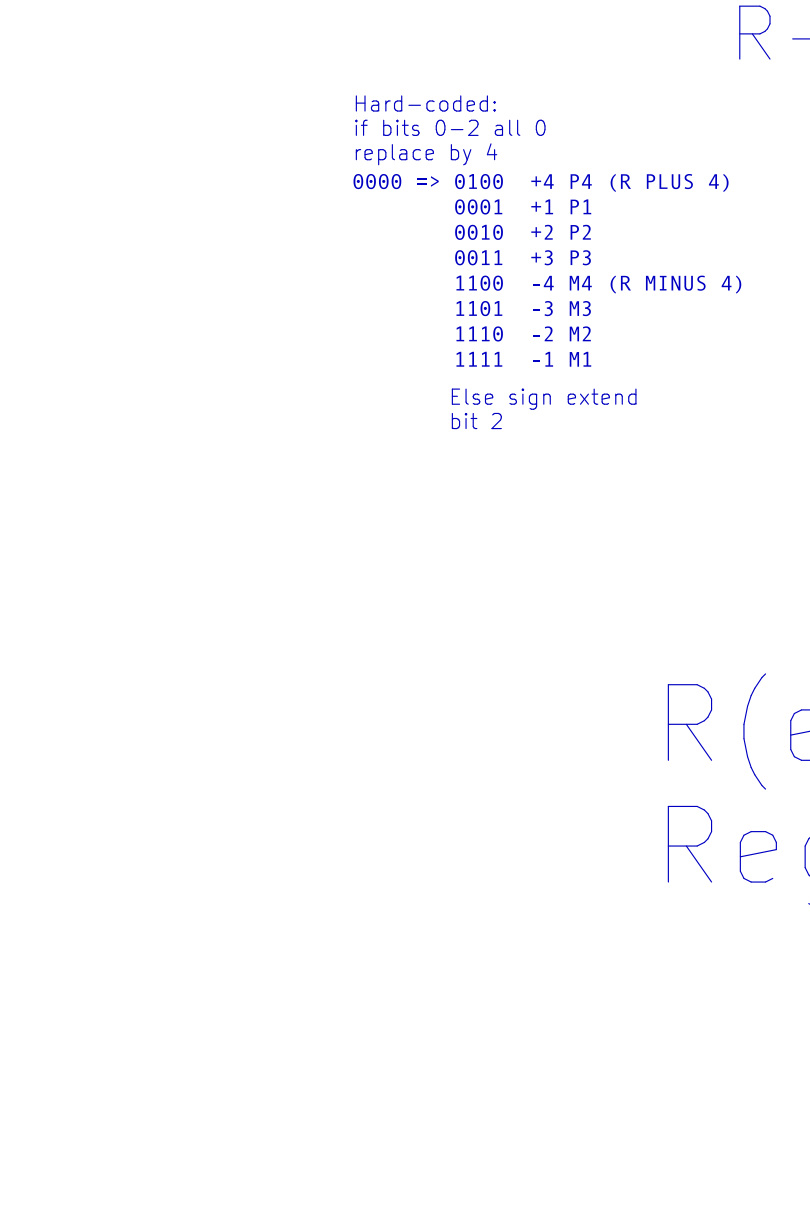
I (INNER Loop Register)



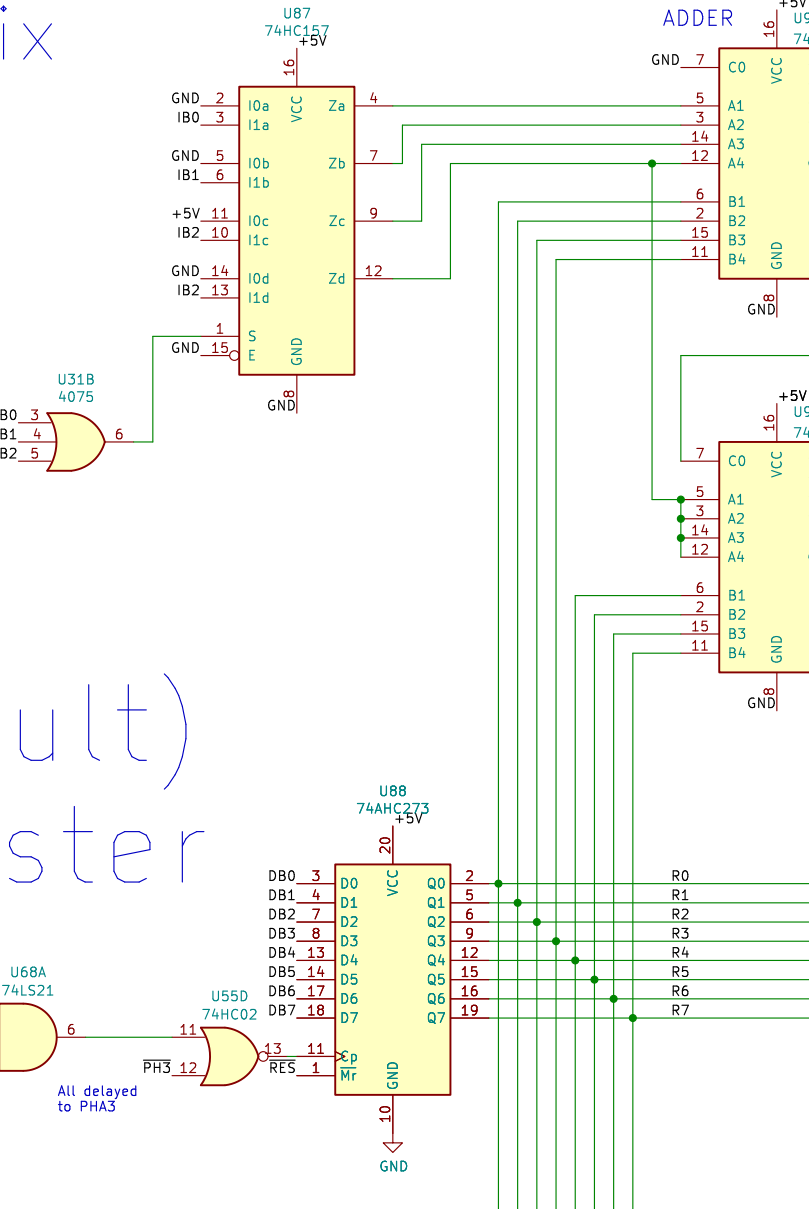
R-Fix



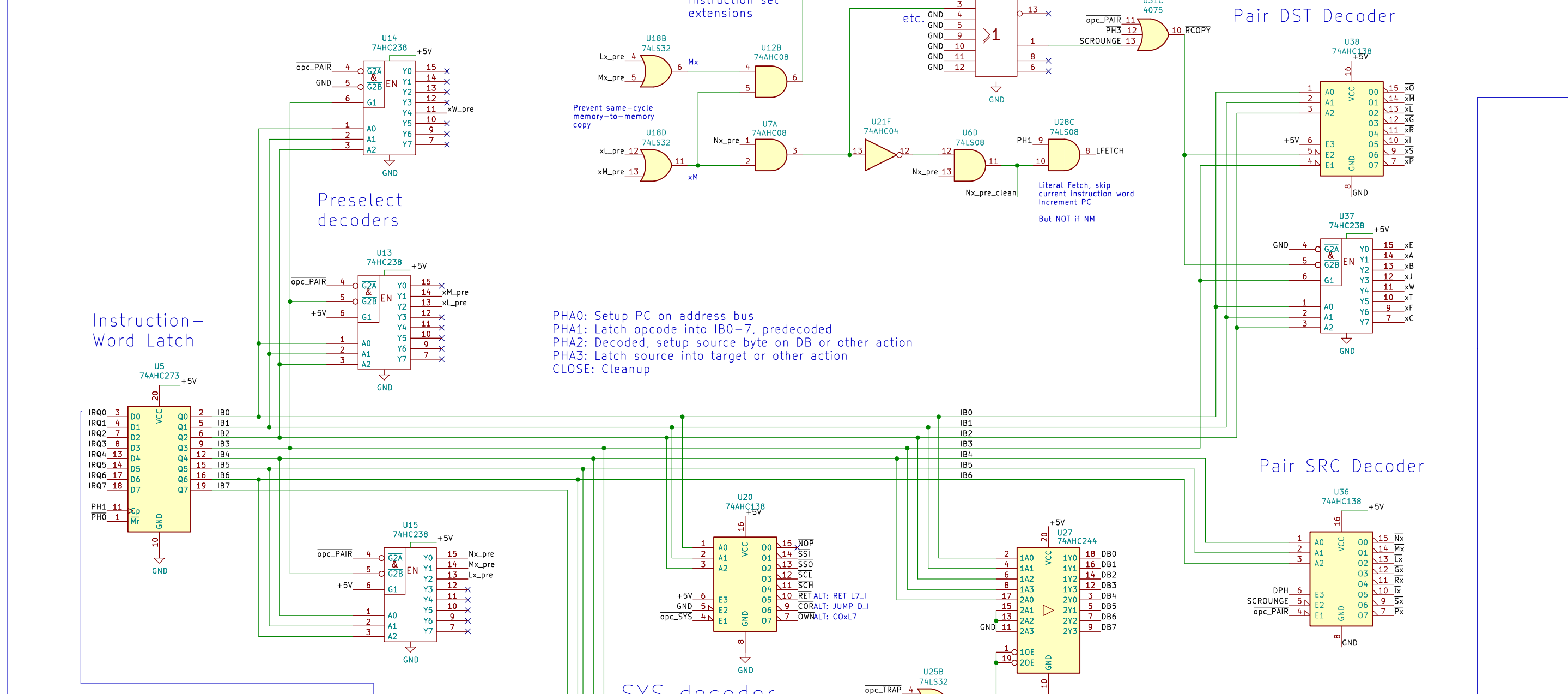
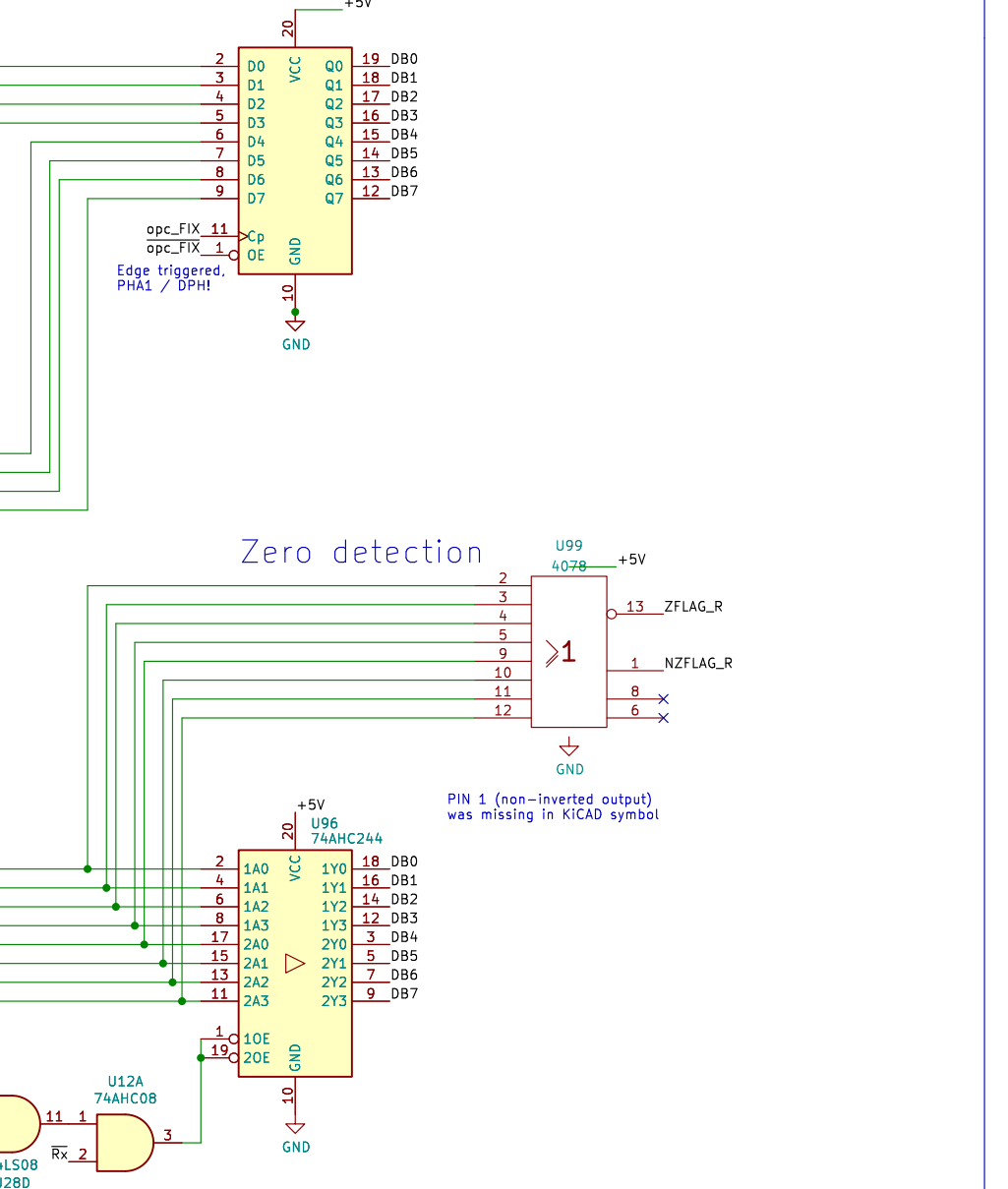
R(result) Register



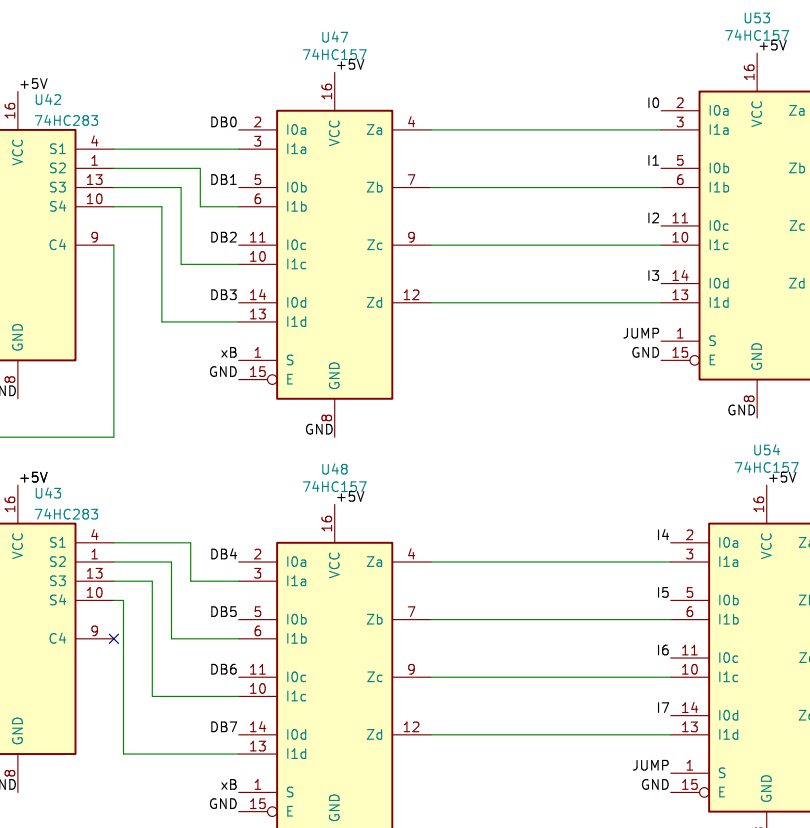
O (OFFSET) Register



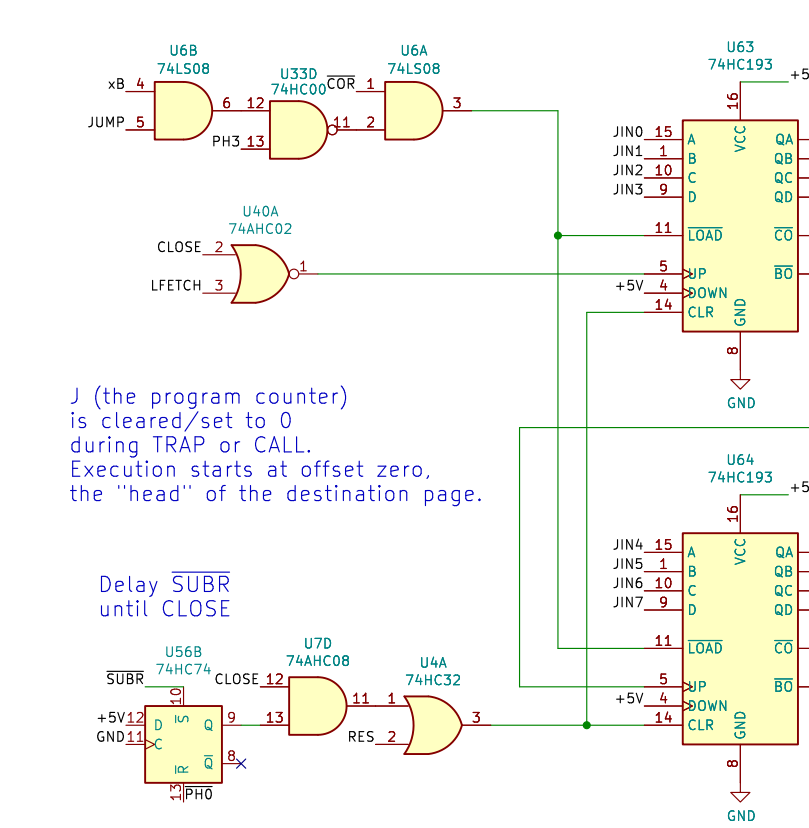
ALU



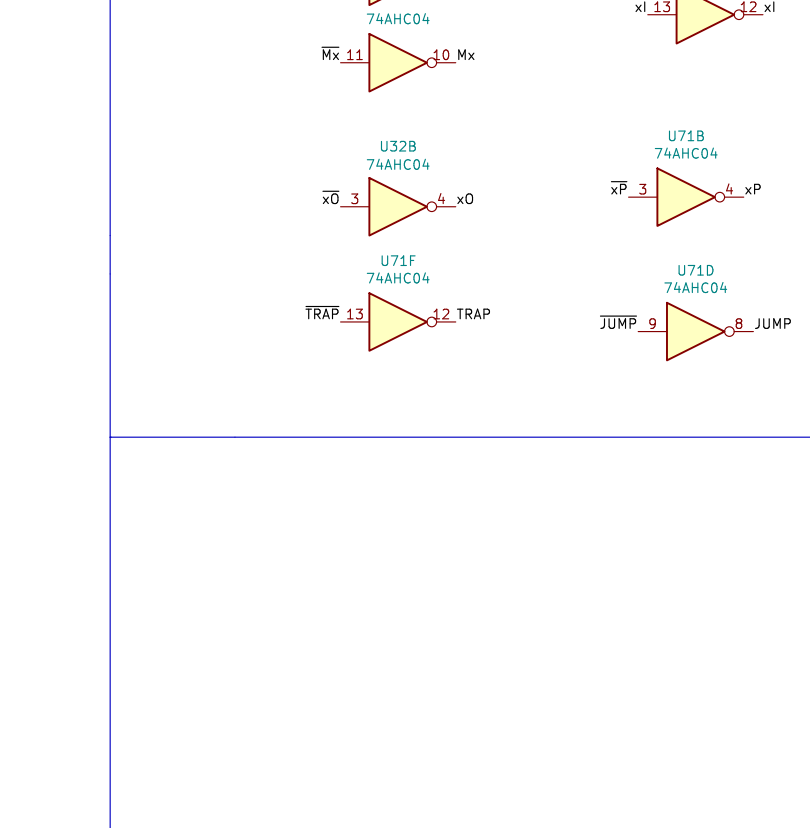
Relative Addressing



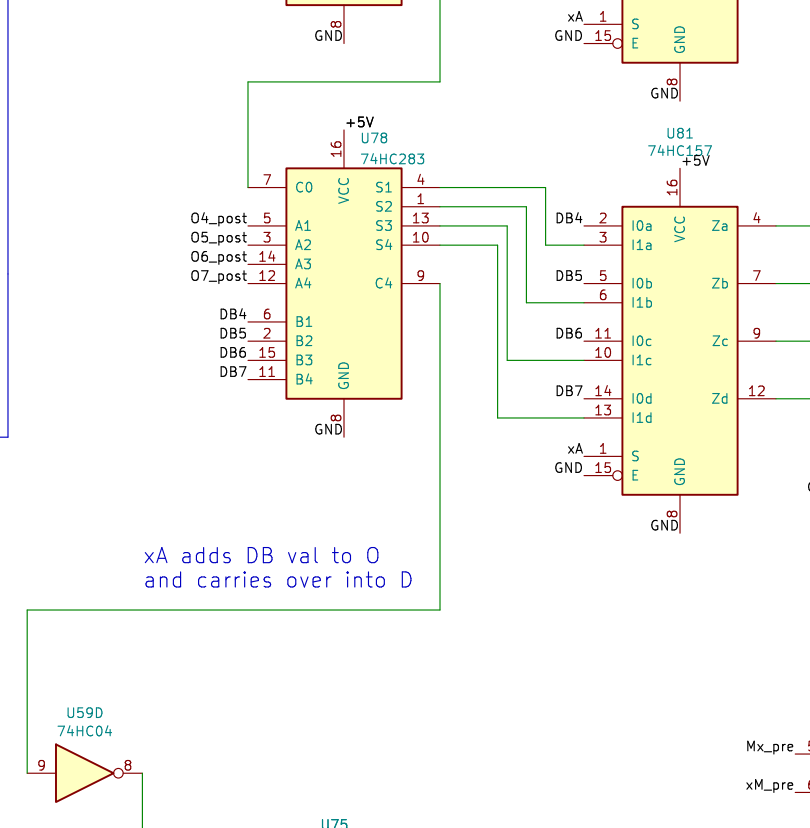
(PC Program Counter)



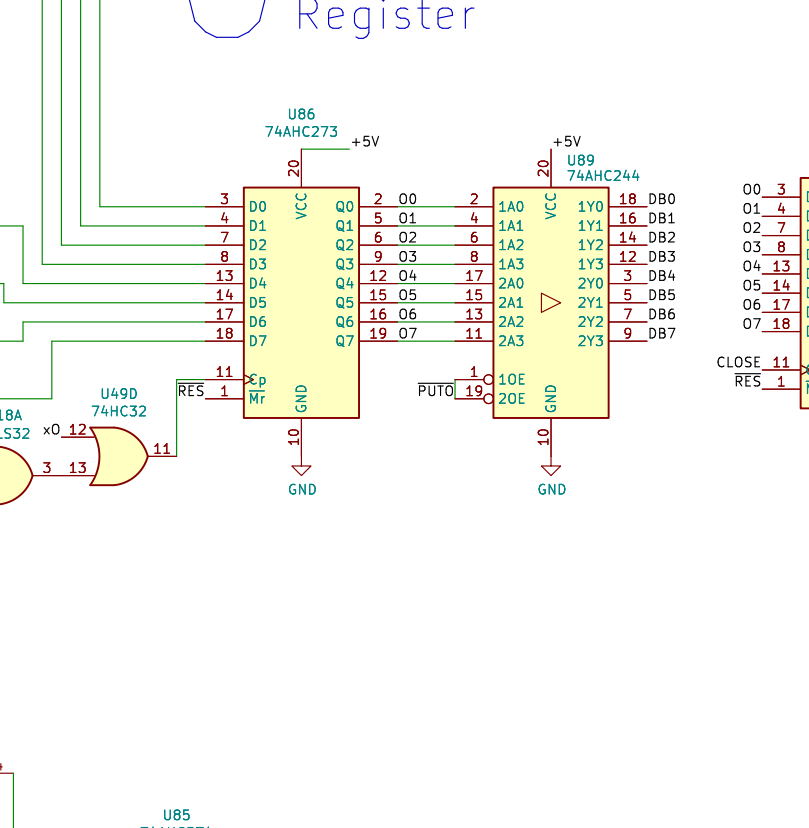
Inverters



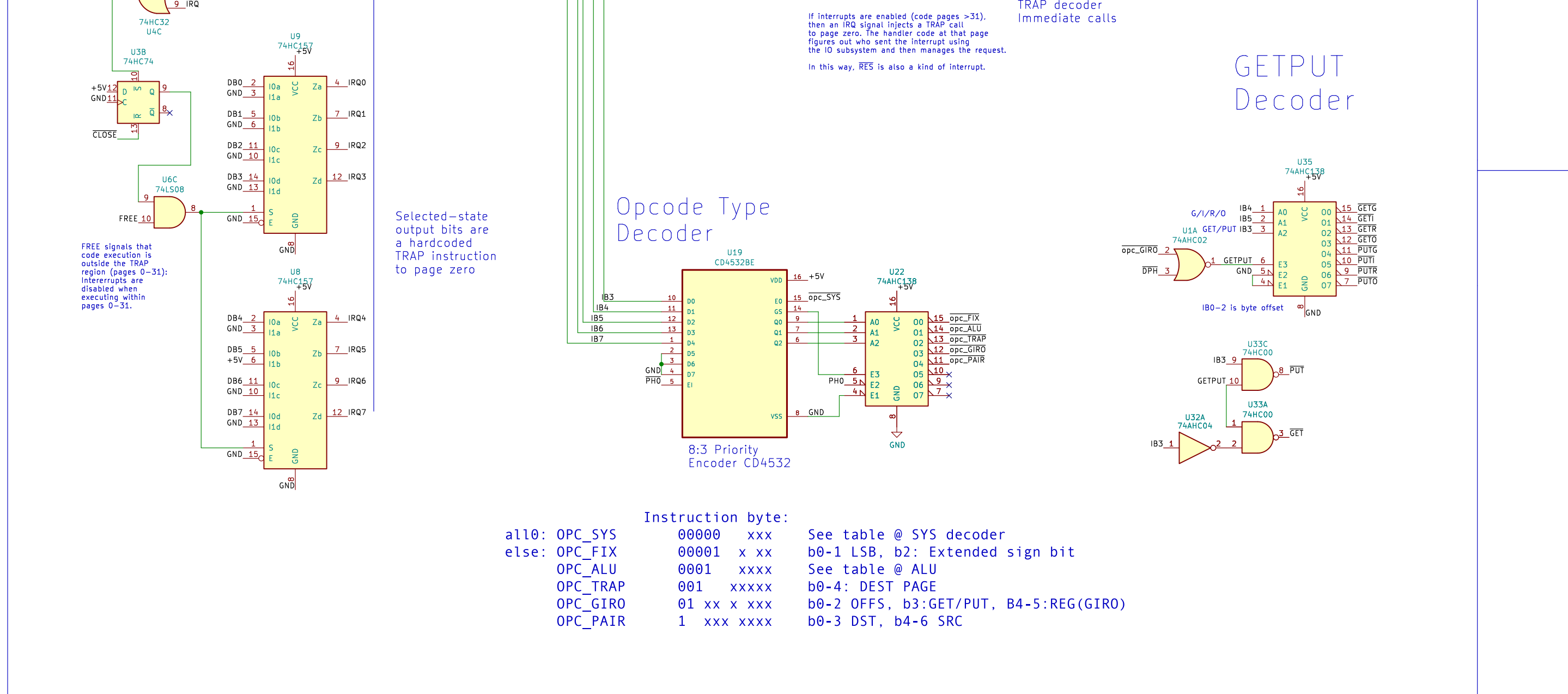
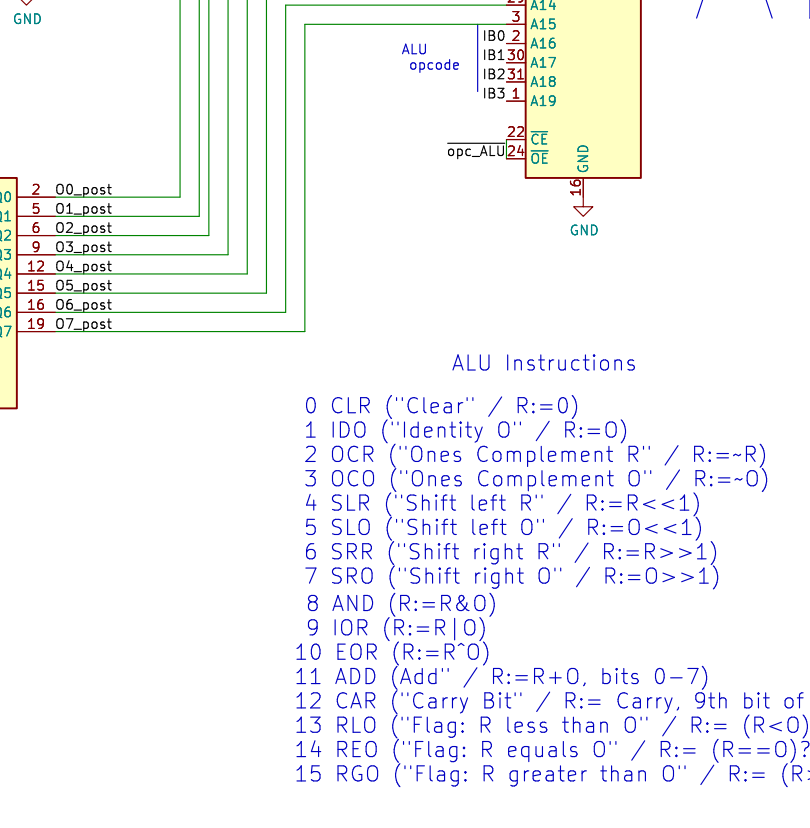
Pair DST Decoder



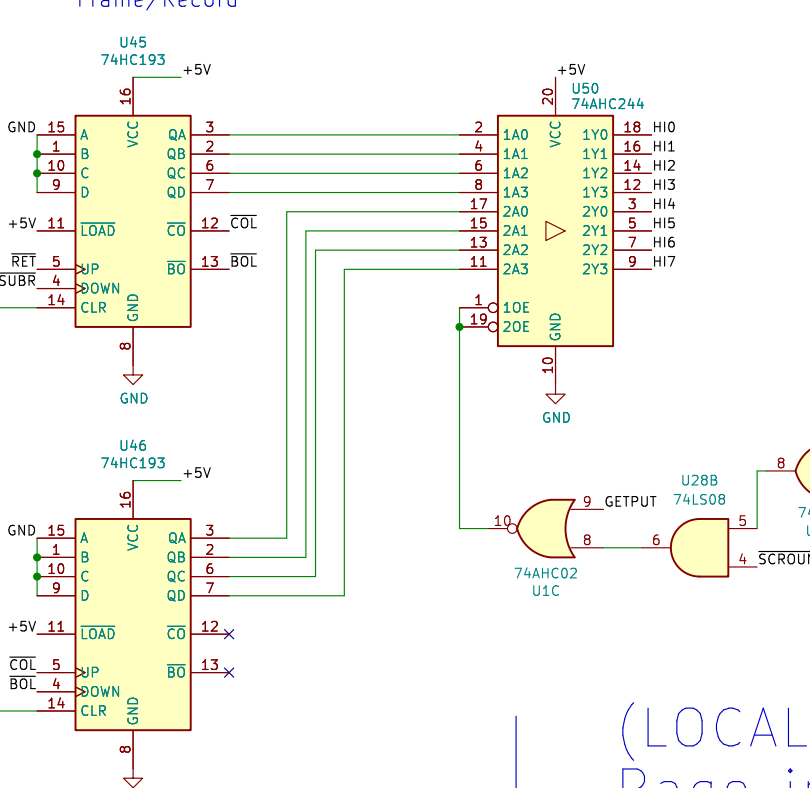
Pair SRC Decoder



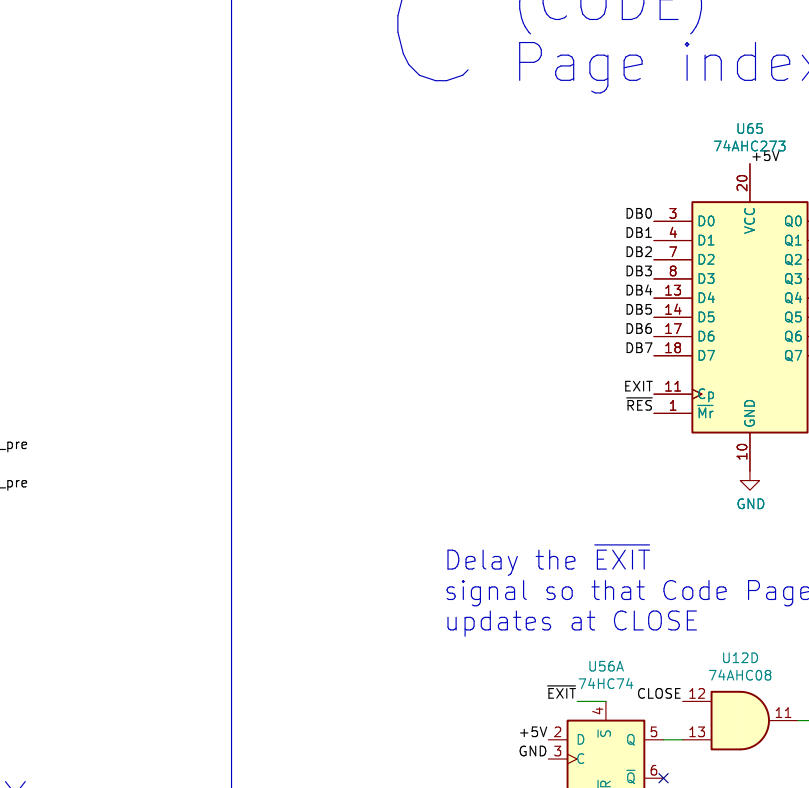
GETPUT Decoder



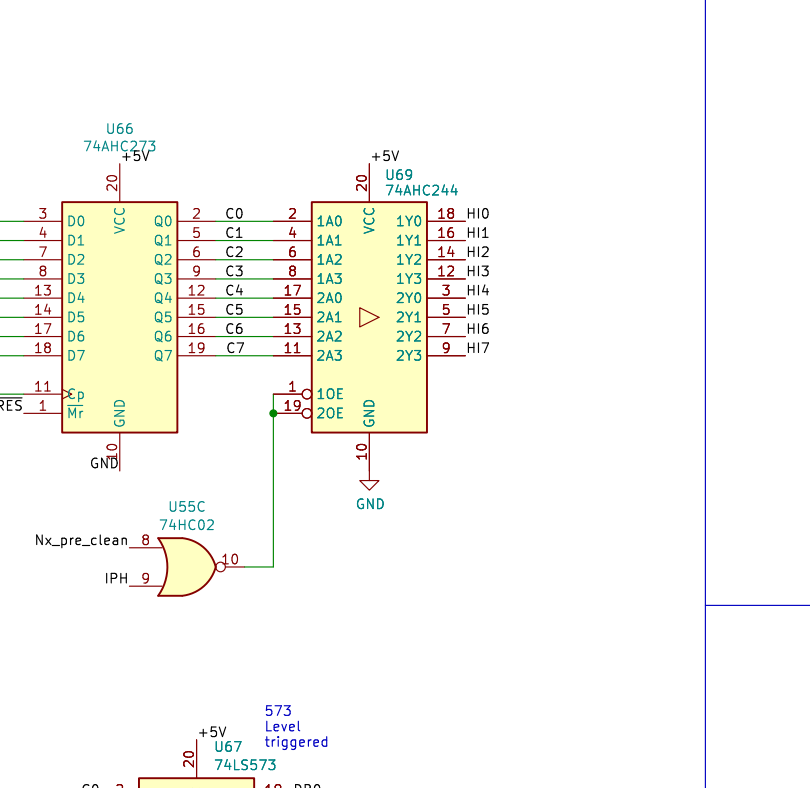
FRD-7 Bus Frame/Record



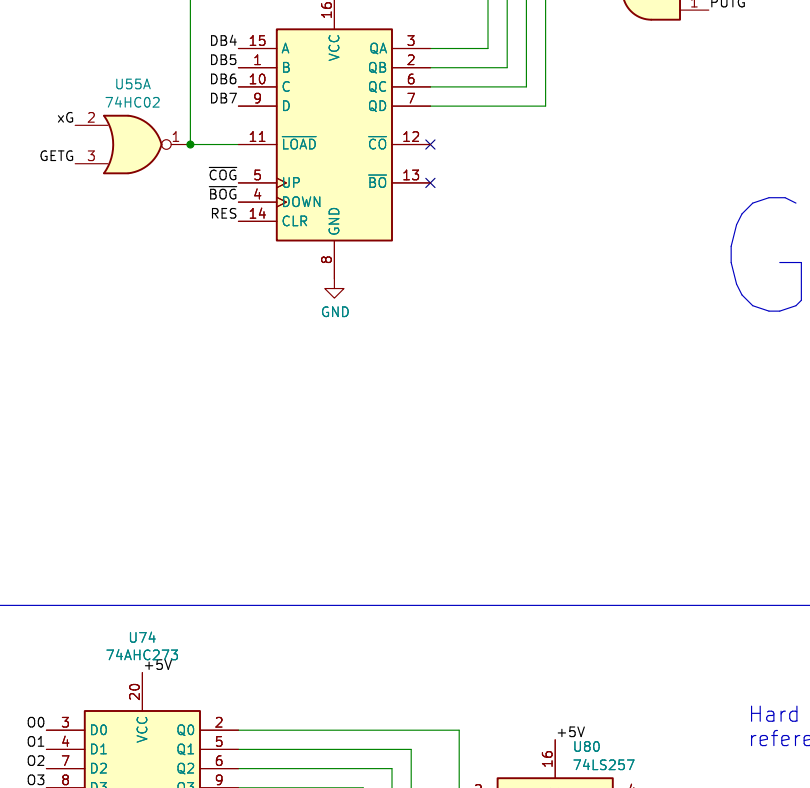
C (CODE) Page index



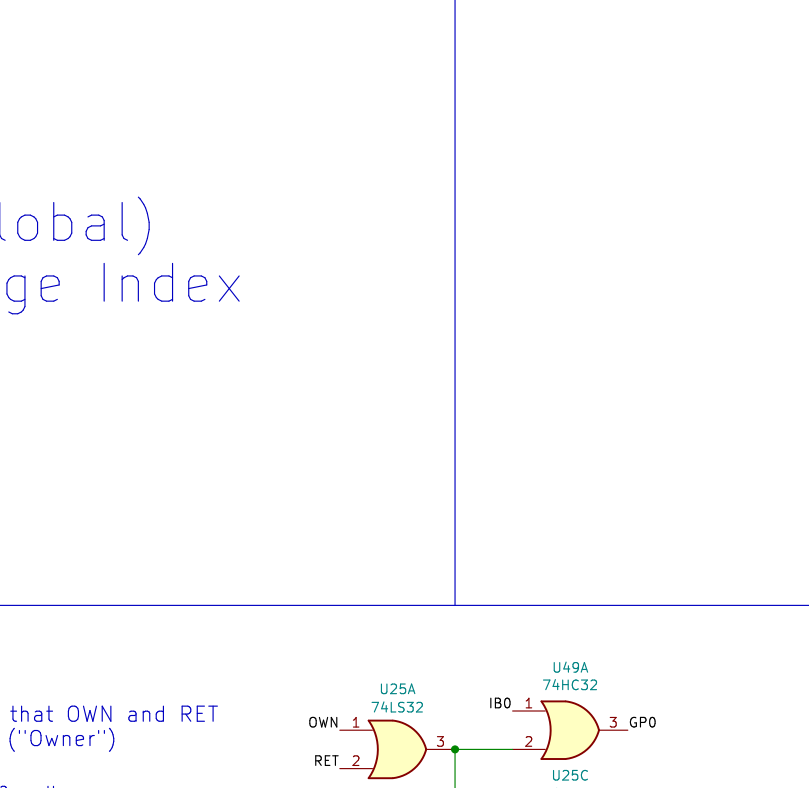
D (DUPE) Register



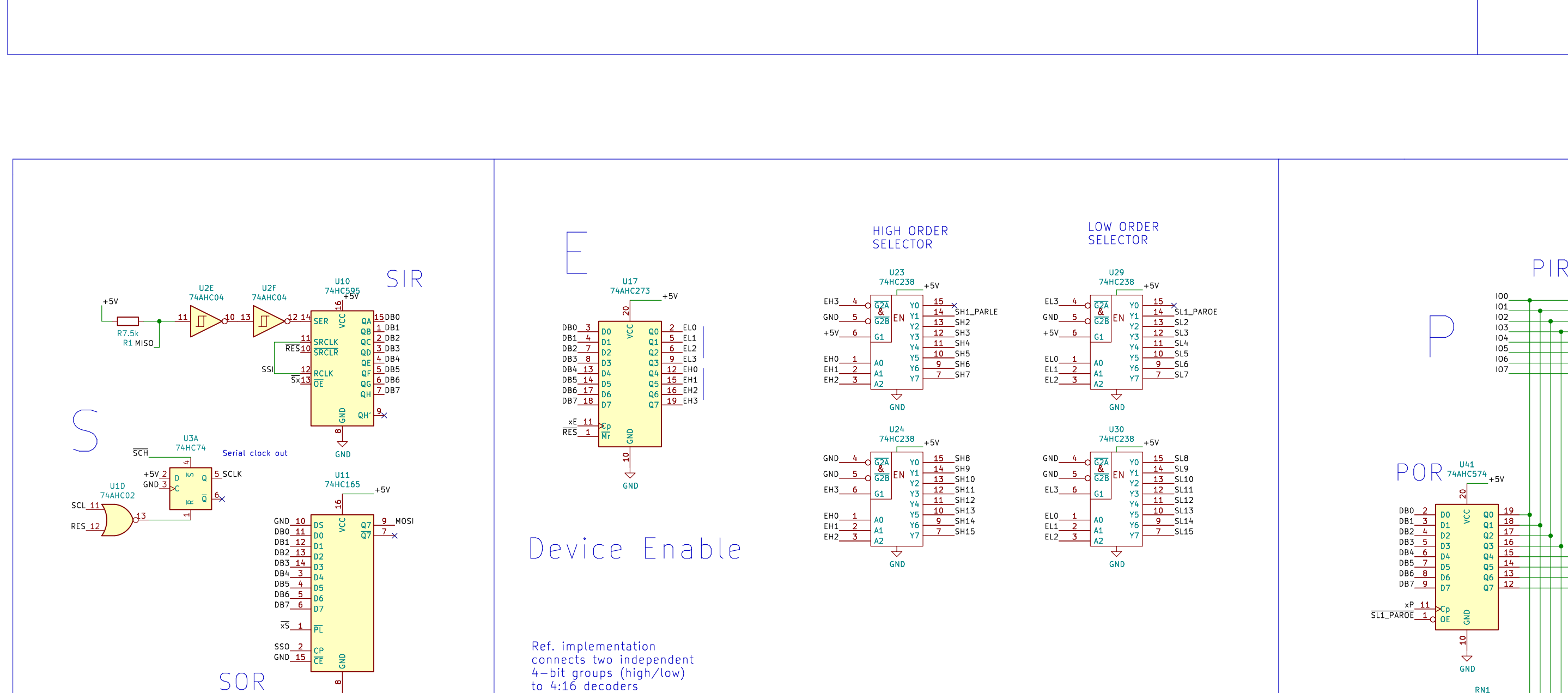
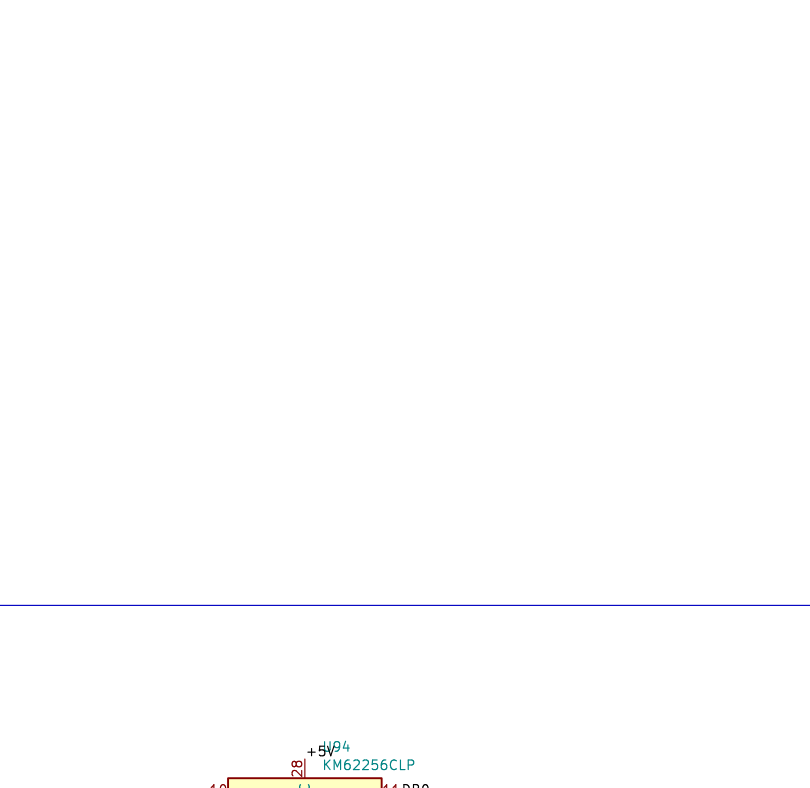
G (Global) Page Index



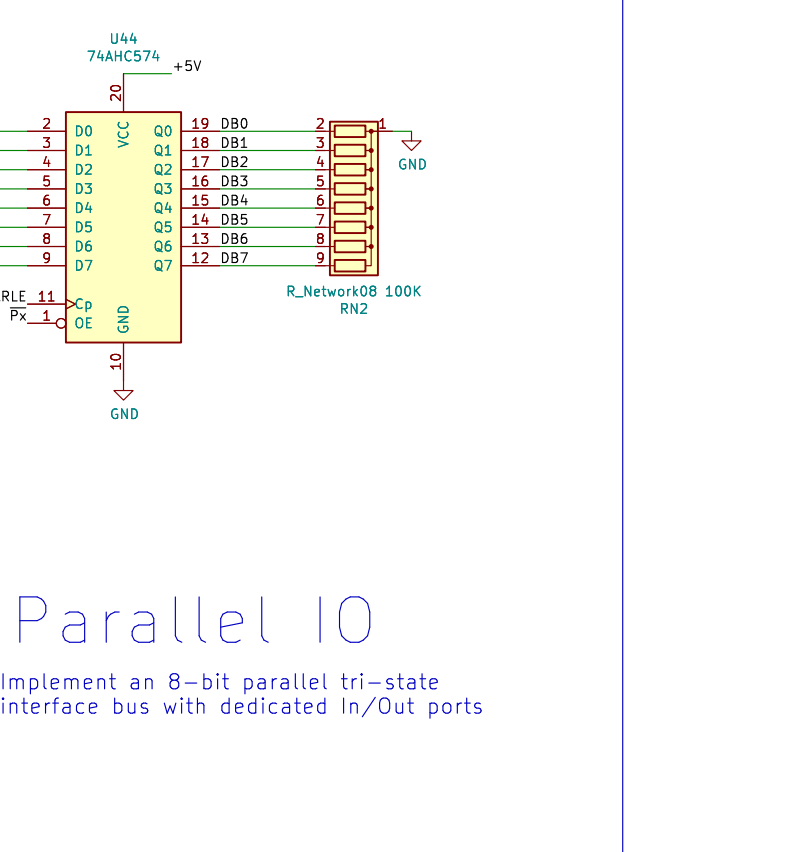
Low



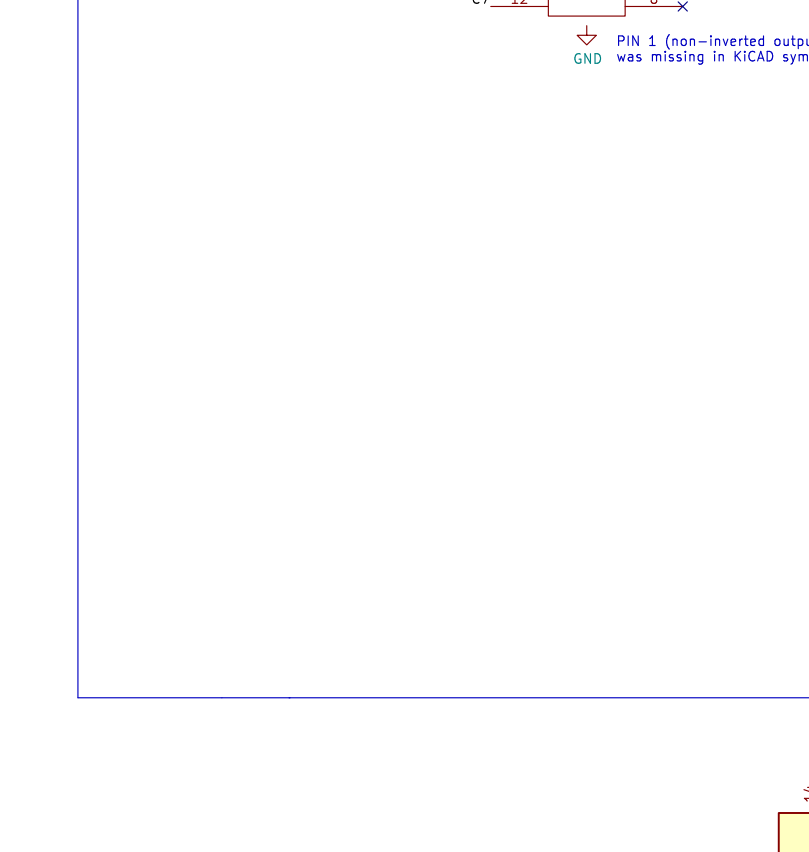
Memory M



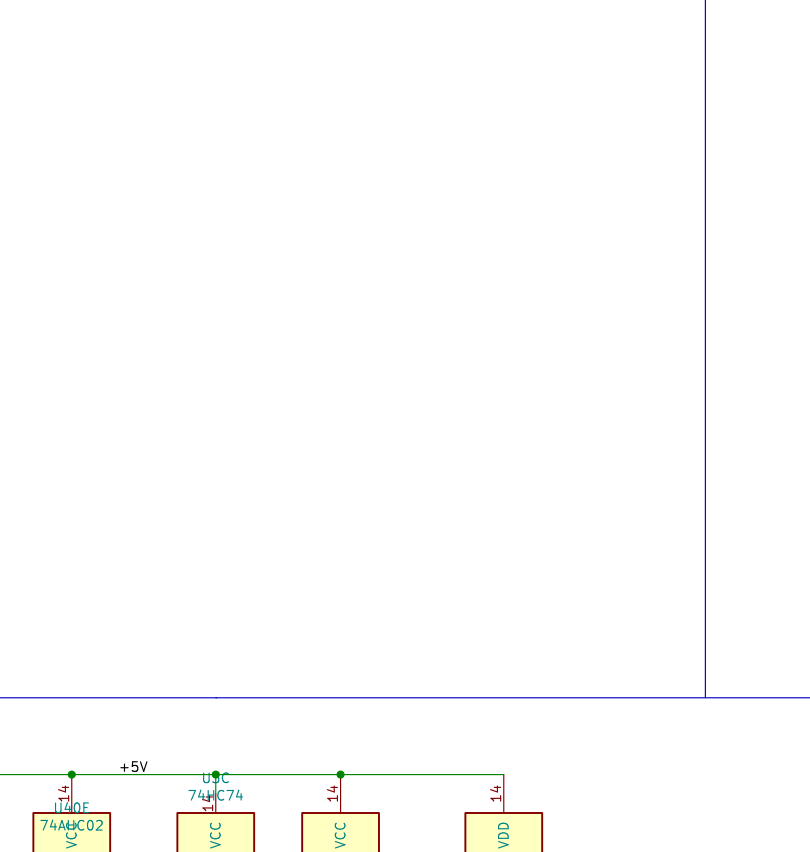
Parallel IO



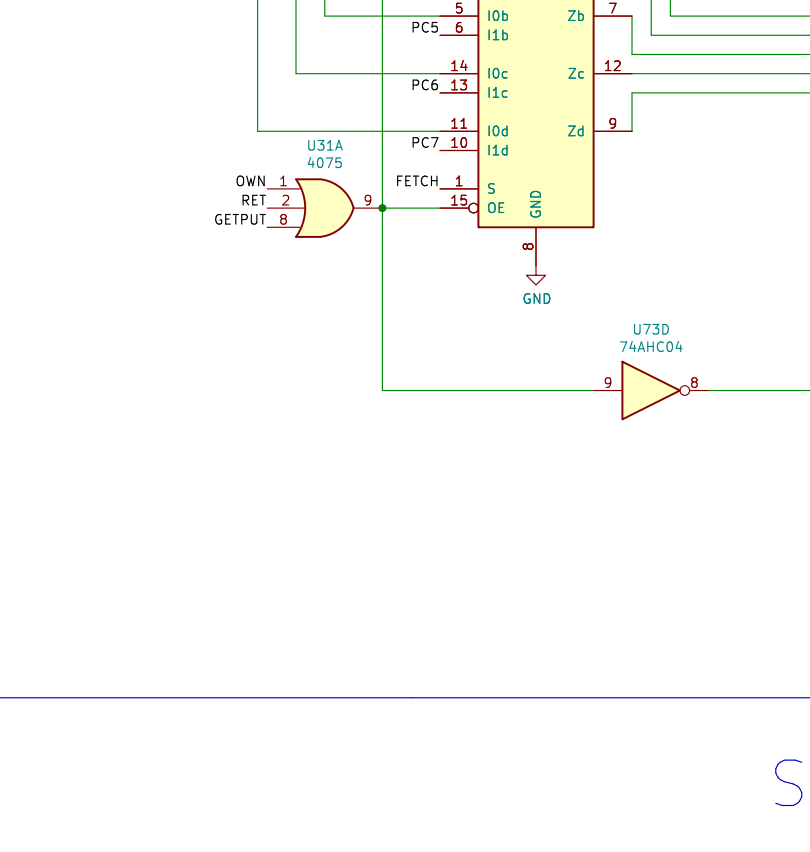
Glue Logic



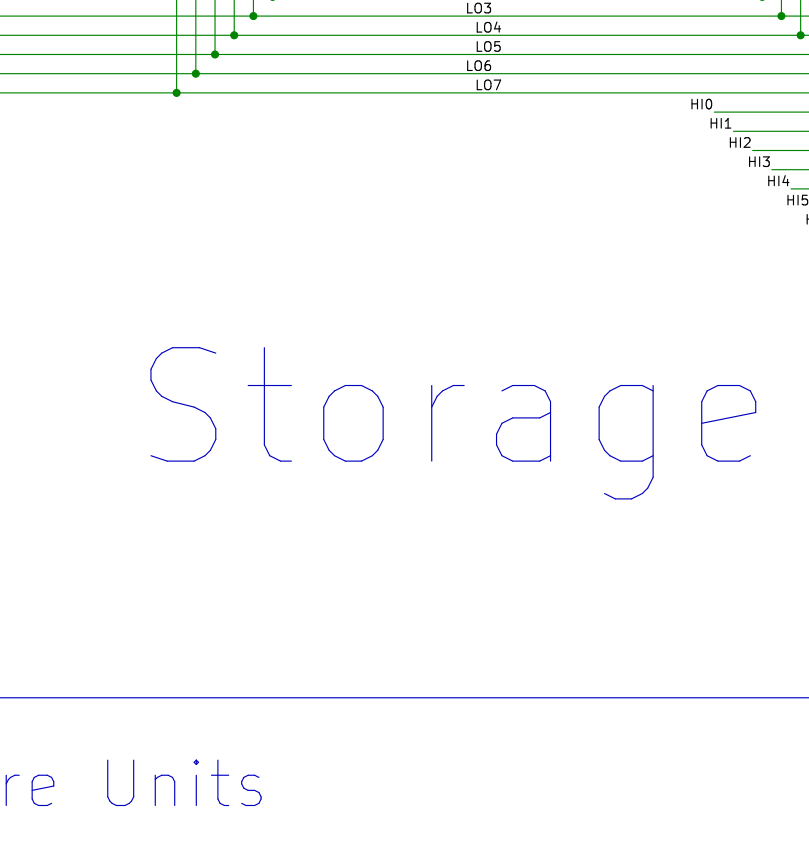
Spare Units



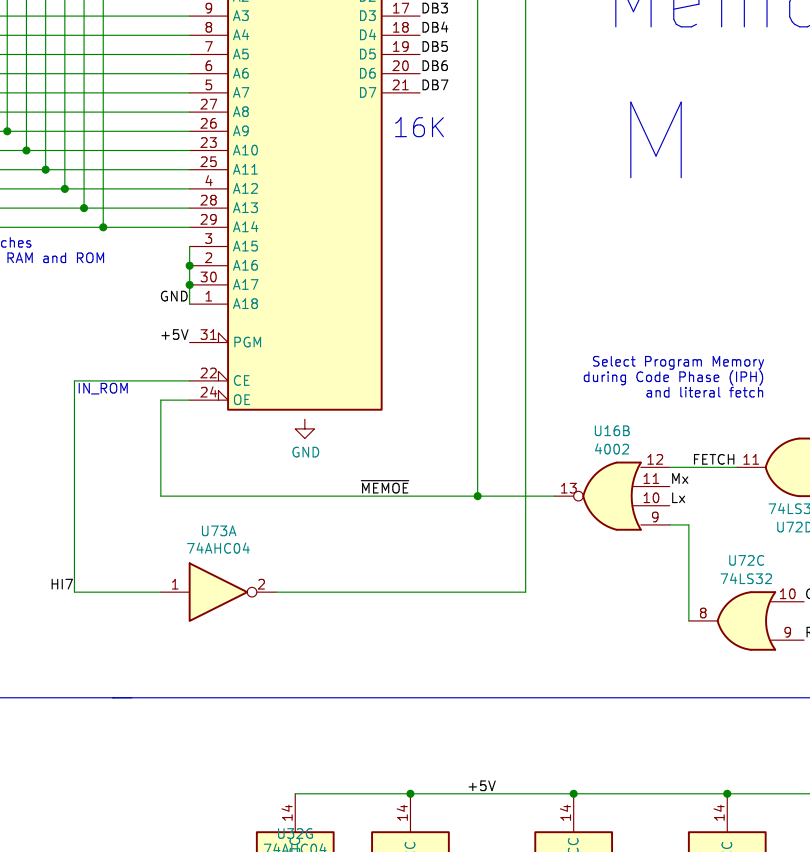
Serial IO



Storage



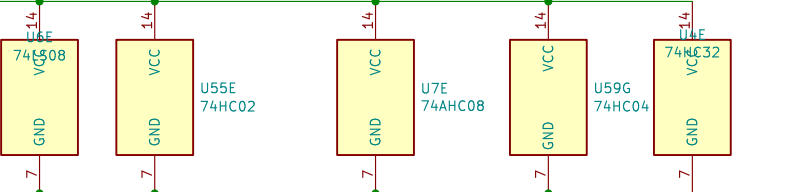
Memory M



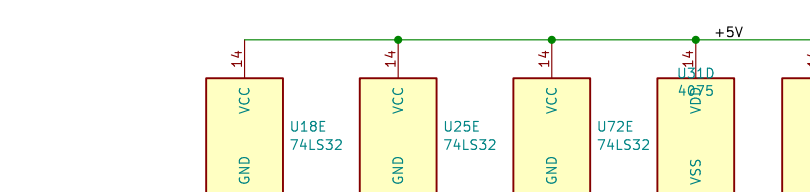
Glue Logic



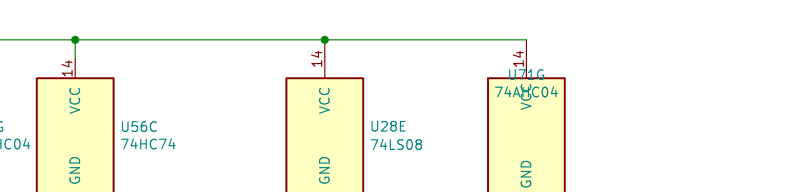
Glue Logic



Glue Logic



Glue Logic



Glue Logic



Glue Logic

