R Adjust

If bits 0-2
not all zero,
sign extend bit 2

Else hard-code
replace 0 by 4

0000 => 0100	+4	P4
0001	+1	P1
0010	+2	P2
0011	+3	P3
1100	-4	M4
1101	-3	M3
1110	-2	M2
1111	-1	M1

U76
74HC157
+5V
16 VCC

GND	2	I0a	Za	4
IB0	3	I1a		
GND	5	I0b	Zb	7
IB1	6	I1b		
+5V	11	I0c	Zc	9
IB2	10	I1c		
GND	14	I0d	Zd	12
IB2	13	I1d		

1 S
GND 15 E
8 GND
GND

U31B
4075

IB0 3
IB1 4
IB2 5
6