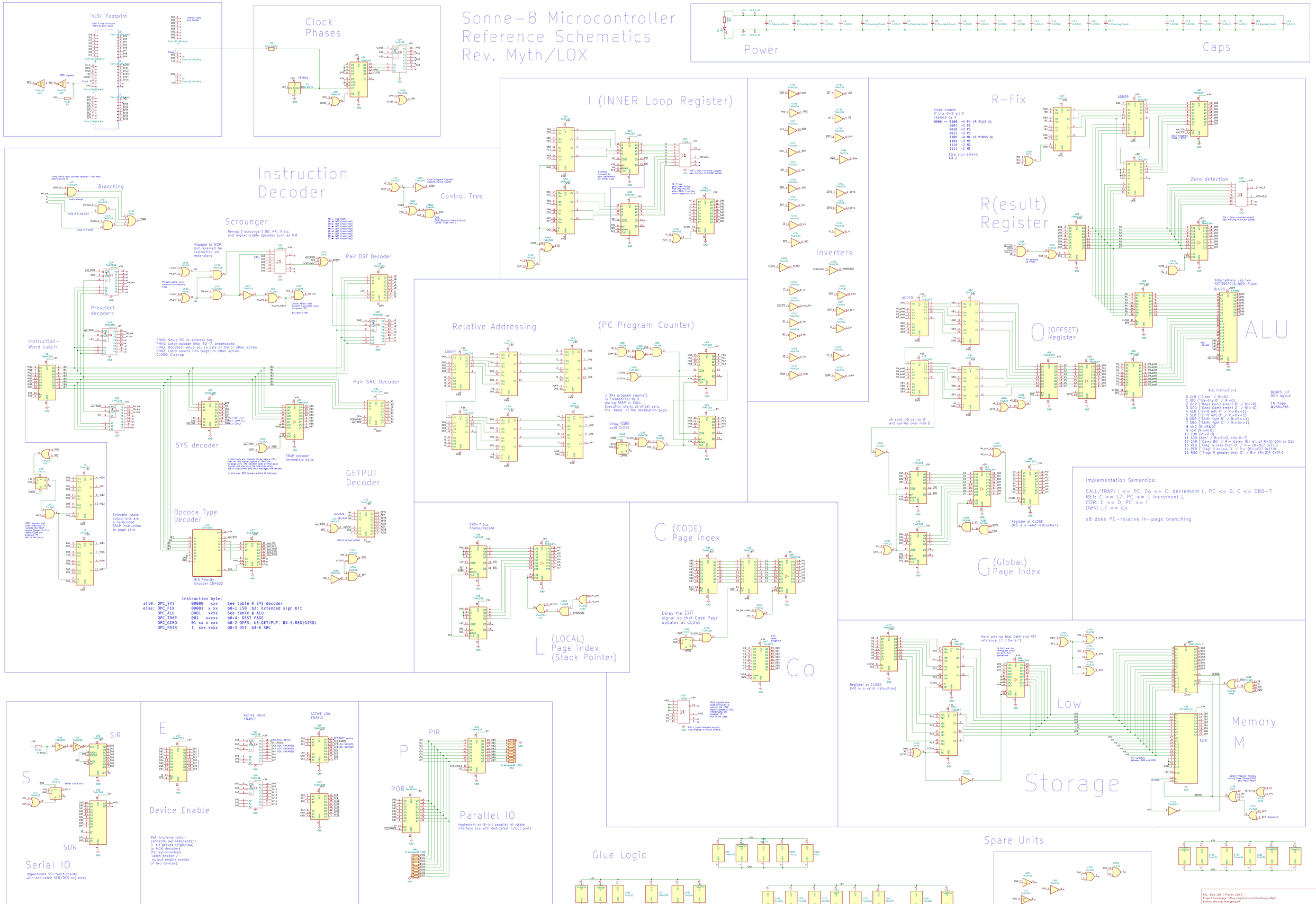
Sonne-8 Microcontroller Reference Schematics Rev. Myth/LOX



Instruction byte:

all0:	OPC_SYS	00000 xxx	See table @ SYS decoder
else:	OPC_FIX	00001 x xx	b0-1 LSB, b2: Extended sign bit
	OPC_ALU	0001 xxxx	See table @ ALU
	OPC_TRAP	001 xxxxx	b0-4: DEST PAGE
	OPC_GIRO	01 xx x xxx	b0-2 OFFS, b3:GET/PUT, B4-5:REG(GIRO)
	OPC_PAIR	1 xxx xxxx	b0-3 DST, b4-6 SRC

Implementation Semantics:

CALL/TRAP: I <= PC, Co <= C, decrement L, PC <= 0, C <= DB0-7
RET: C <= L7, PC <= I, increment L
COR: C <= D, PC <= I
OWN: L7 <= Co

xB does PC-relative in-page branching

Rev. Step LOX ("Frozen 100")
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File: myth.kicad_sch

Title: Sonne-8 Micro-Controller

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