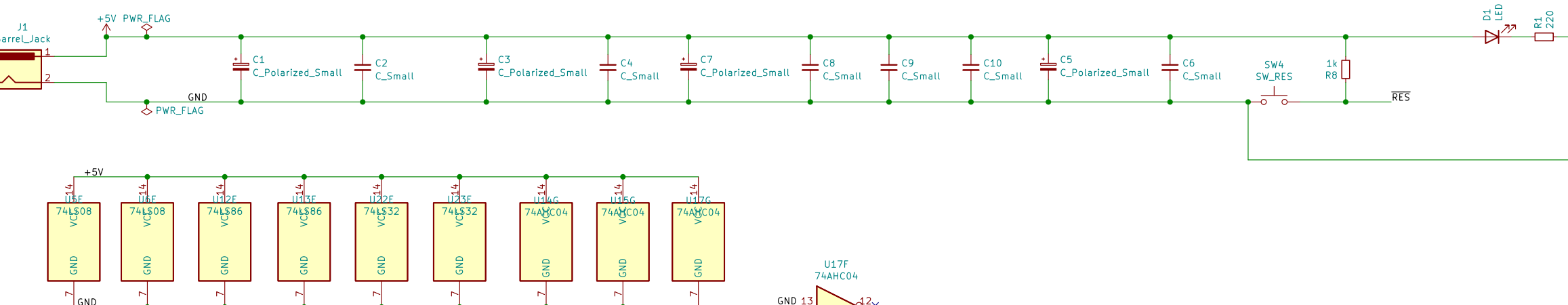


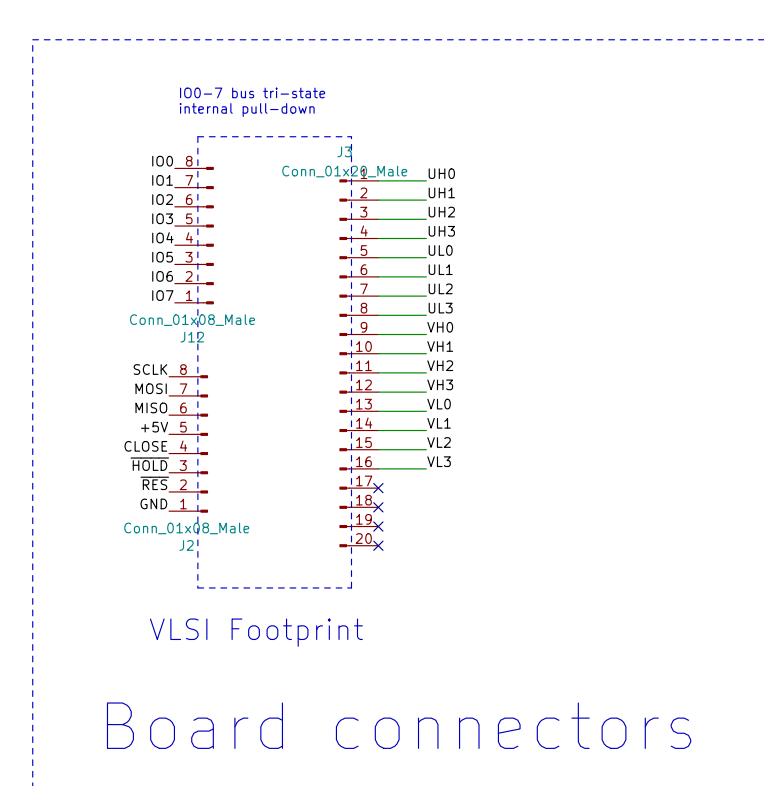
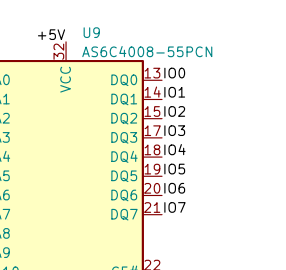
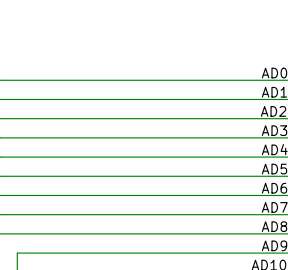
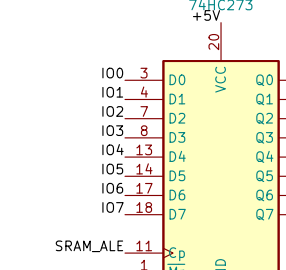
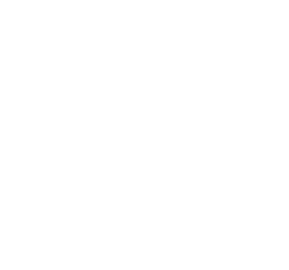
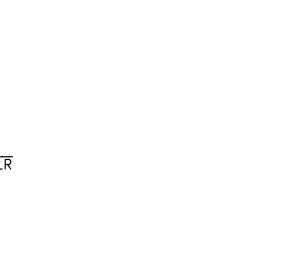
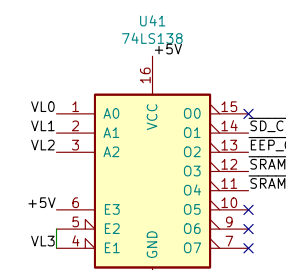
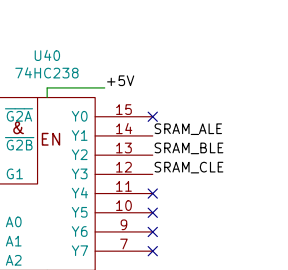
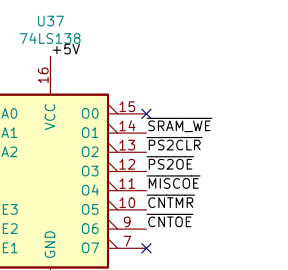
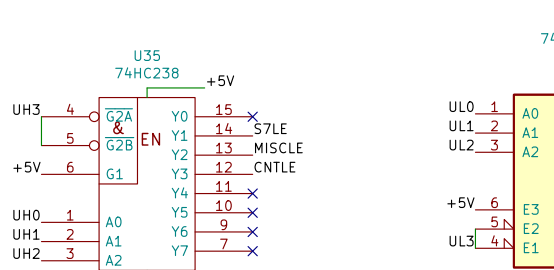
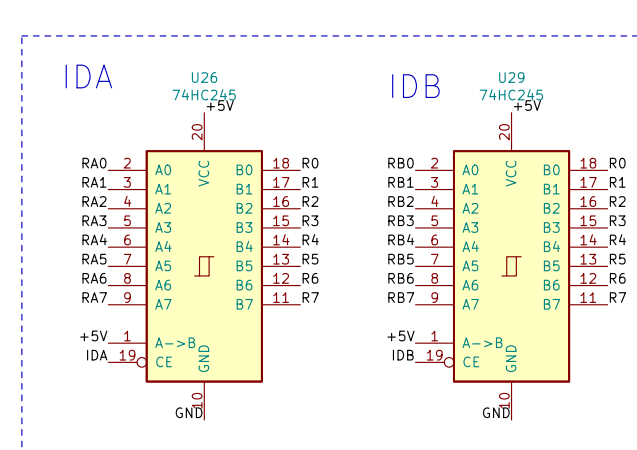
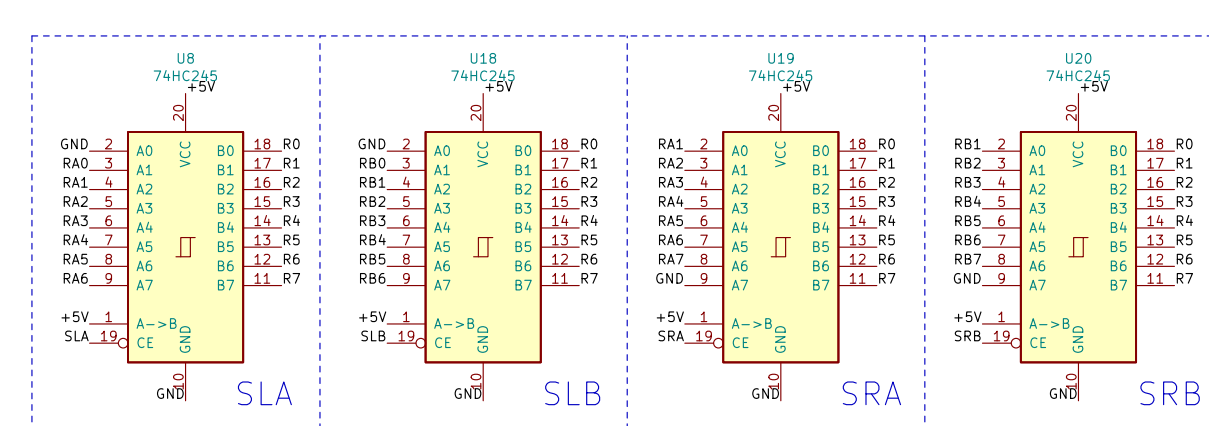
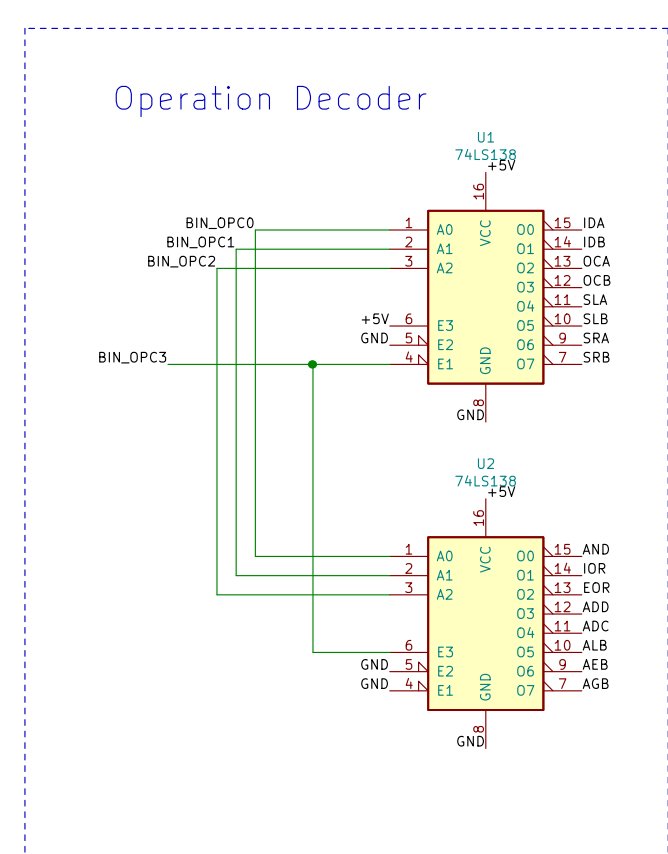
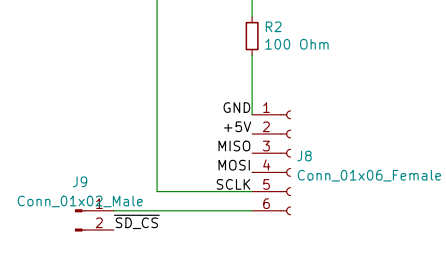
BLUR3  
ROM image:  
16 maps  
@256x256

0 IDQ (Identity Q' / OP=Q)  
1 OA (Identity A' / OP=A)  
2 OQ' (Does Complement Q' / OP=Q)  
3 OA' (Does Complement A' / OP=A)  
4 SQA (Shift left A' / OP=A<<1)  
5 SQA' (Shift left A' / OP=A<<1)  
6 SQA' (Shift right symmetric A' / OP=A<>1)  
7 SQA' (Shift right symmetric A' / OP=A<>1)  
8 AND (OP=A&A)  
9 OR (OP=A|A)  
10 EOR (OP=A^A)  
11 AND (OP=A&A, bits 0-7)  
12 CNY (Carry flag / OP=Carry, 8th bit of Q-A) 00s or 01s  
13 SQA (Times 0 less than P' / OP=(Q-A)>0) 00s or 01s  
14 DEA (Times 0 equals A' / OP=(Q-A)>0) 00s or 01s  
15 SQA (Times 0 greater than A' / OP=(Q-A)>0) 00s or 01s

C=A use CLA+1 etc.



Resistor flux reads errors from SPI EEPROM.  
Whenever MISO went low, SCLK would spike up again.



Socket Placeholder  
for Board Connector Only

