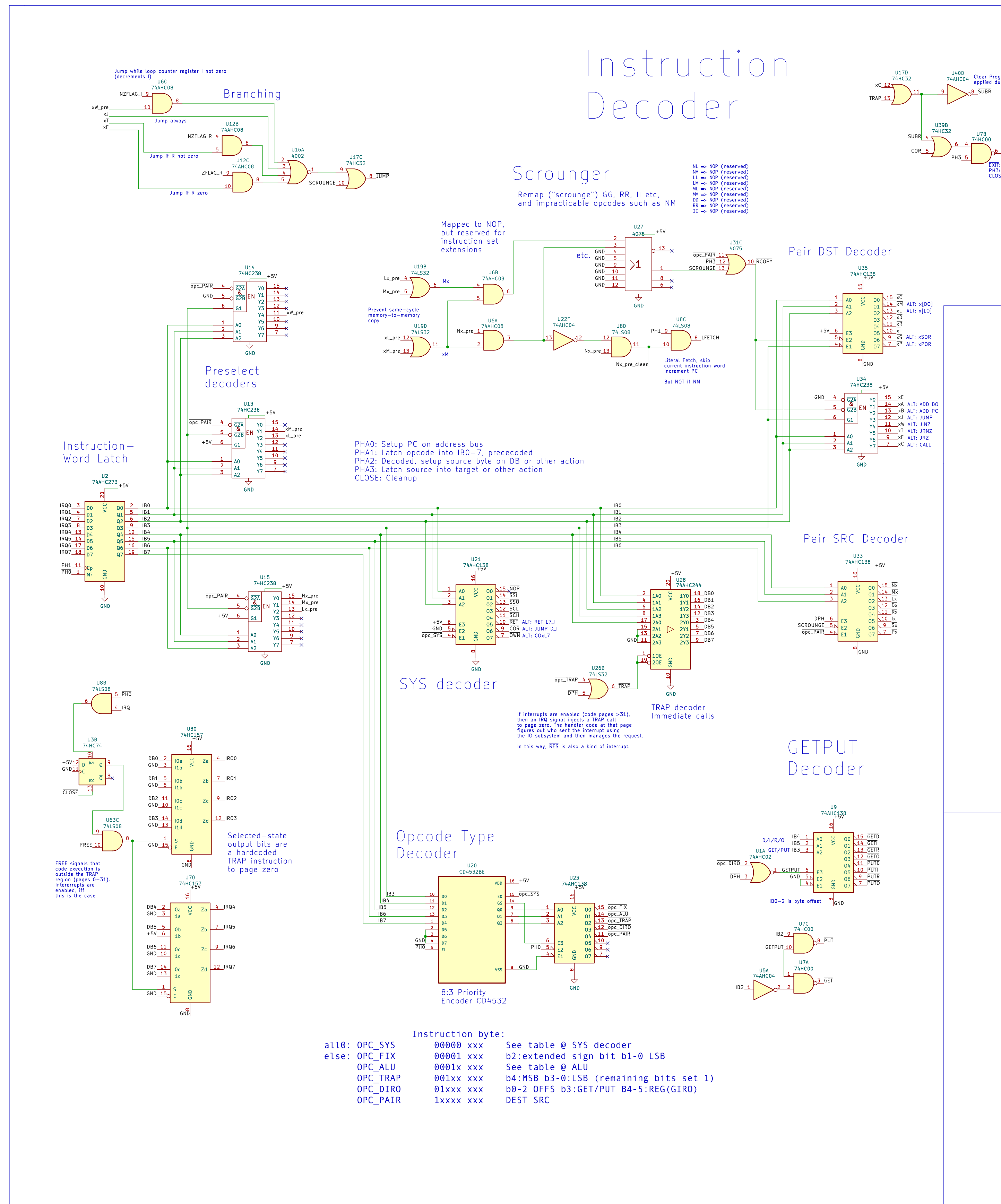
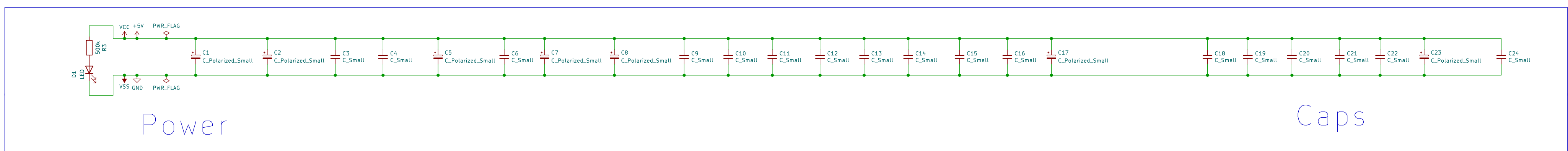
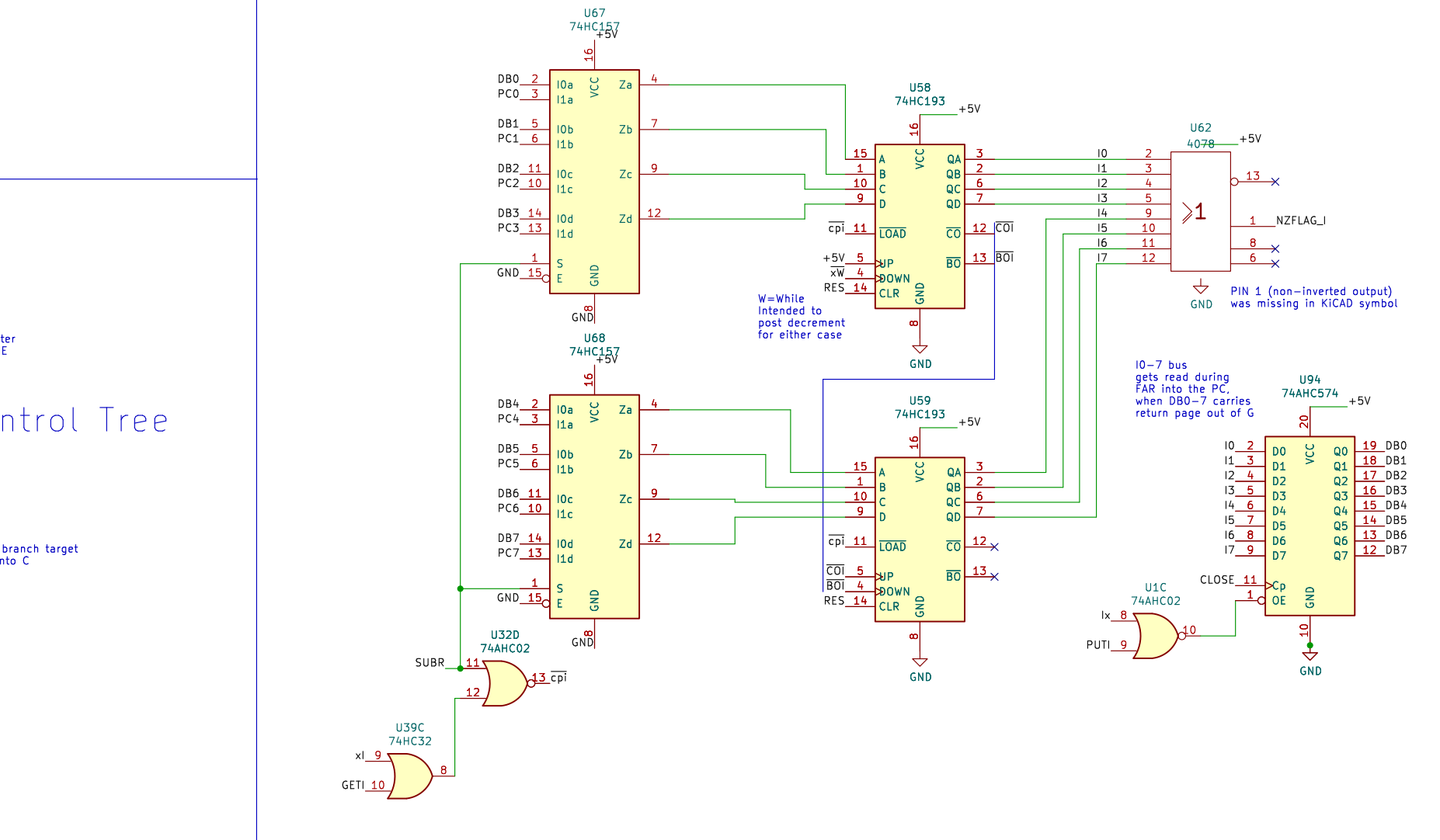


Sonne-8 Microcontroller Reference Schematics

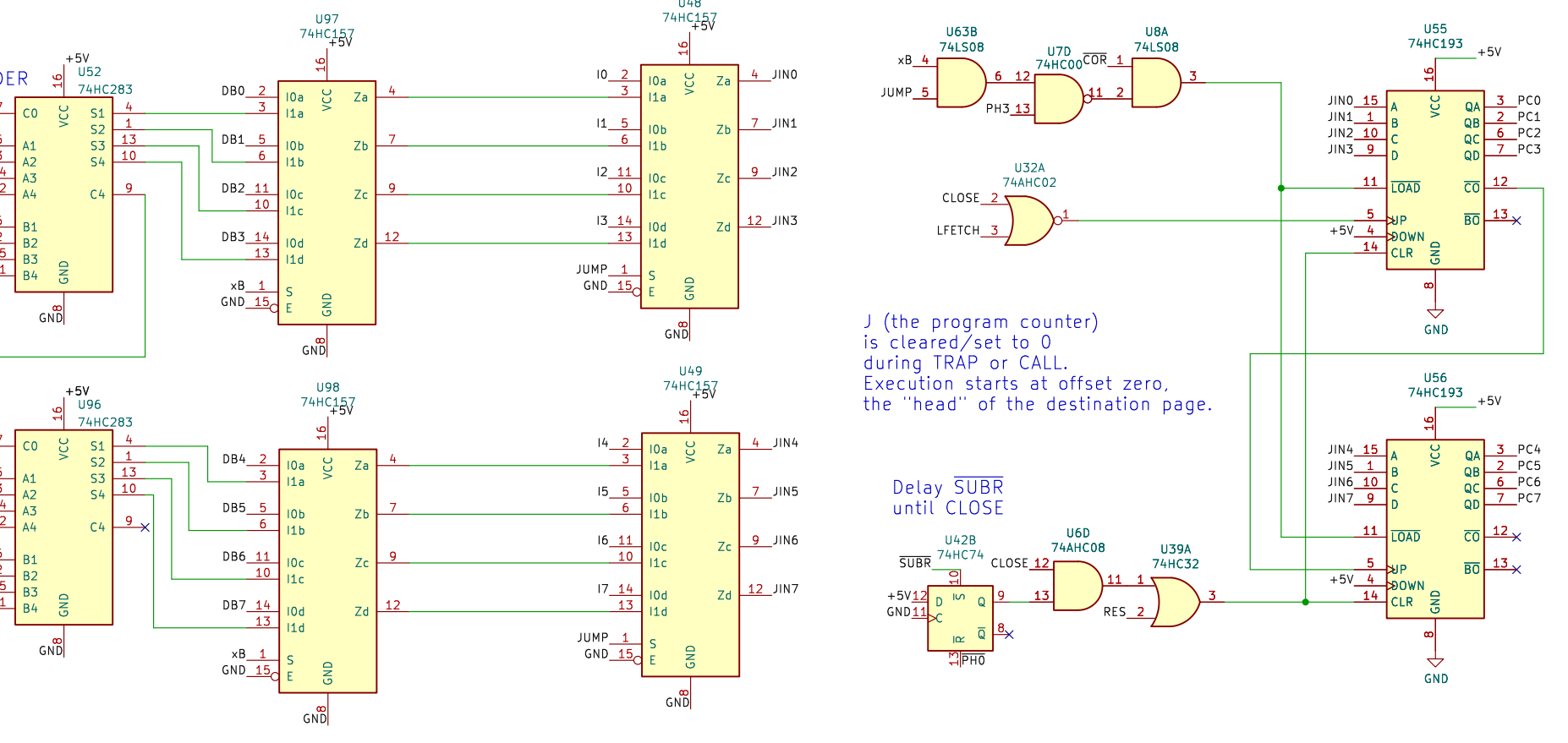
Rev. Myth/LOX



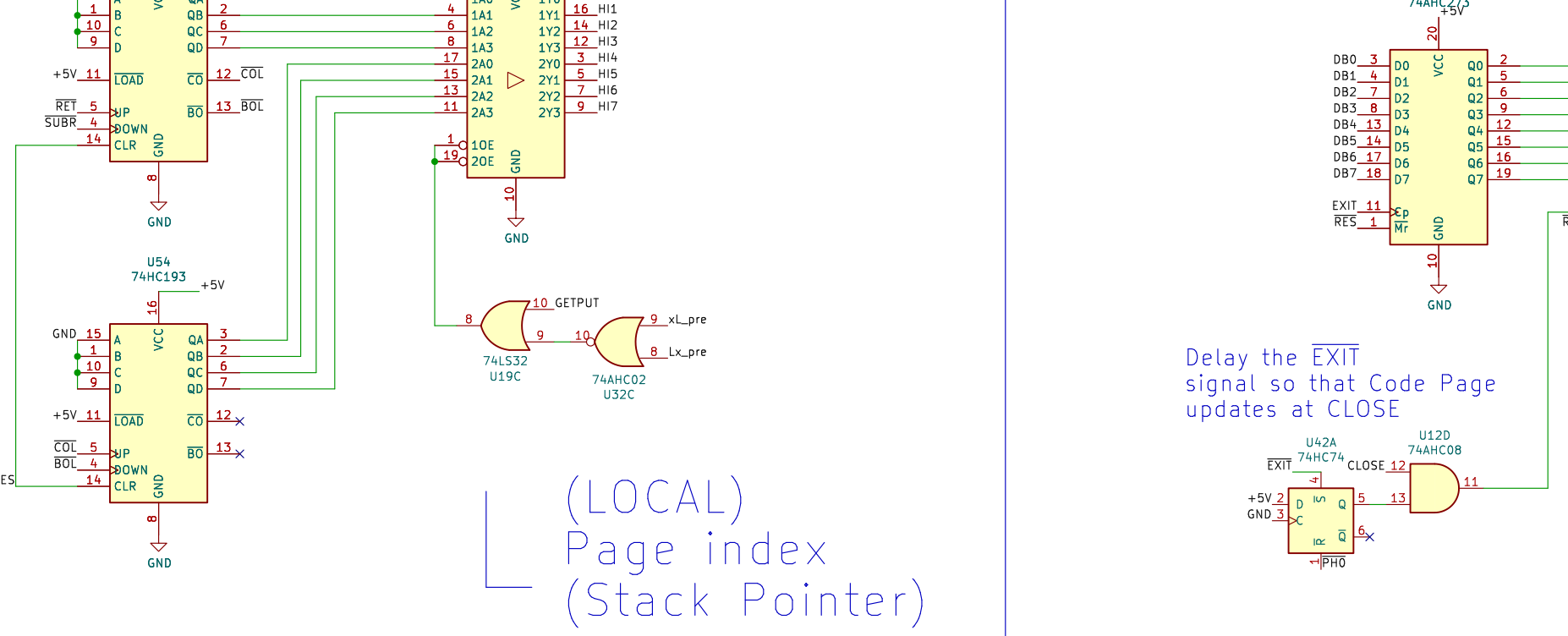
I (INNER Loop Register)



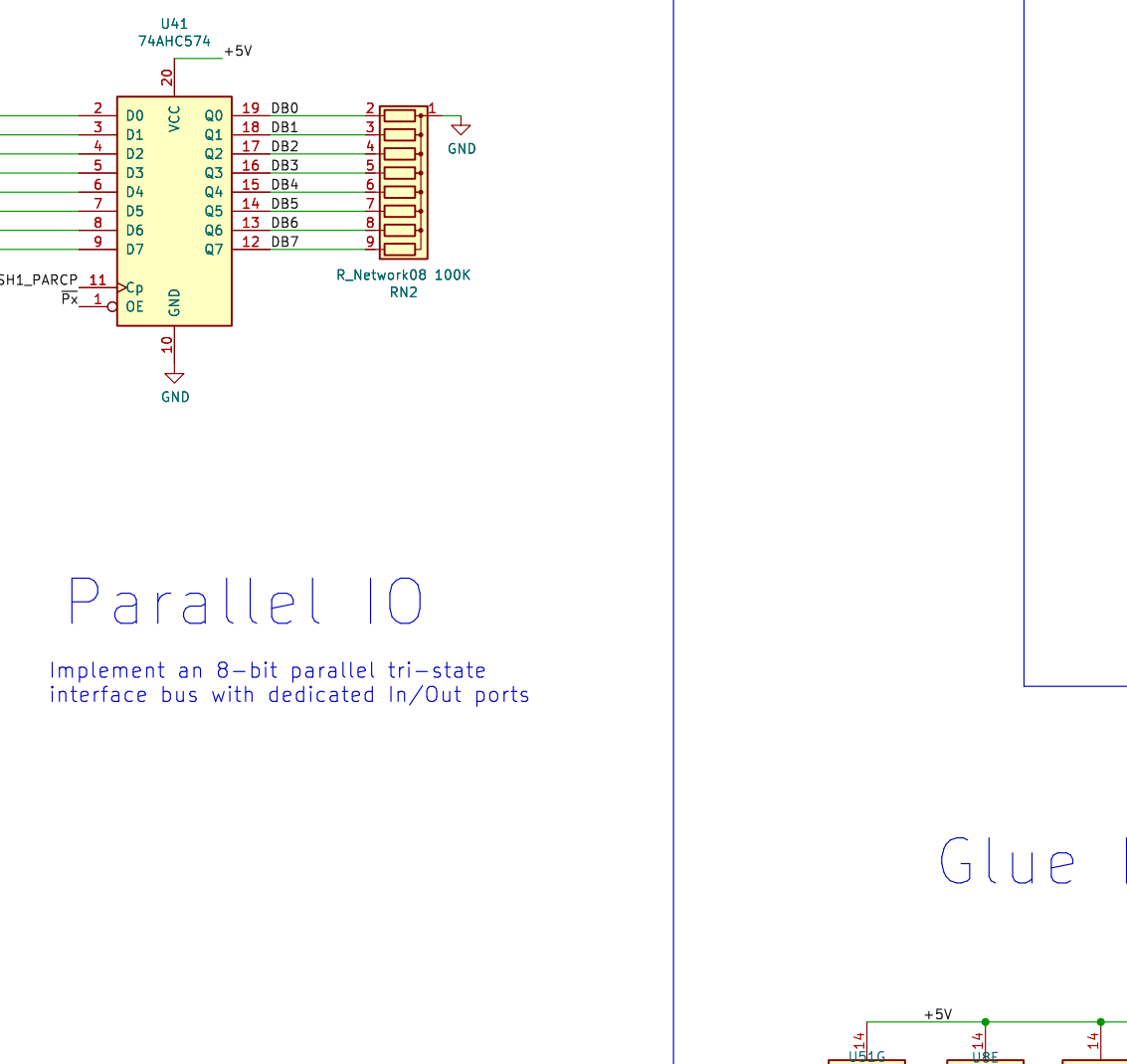
Relative Addressing (PC Program Counter)



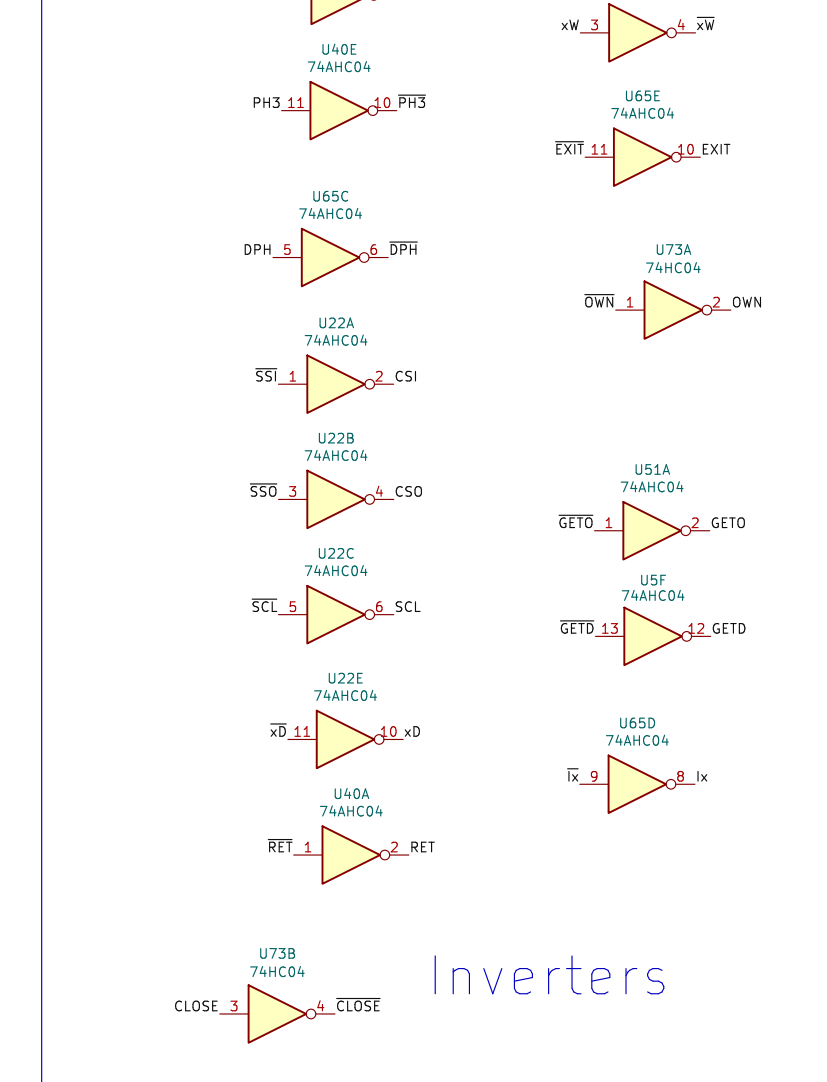
C (CODE) Page index



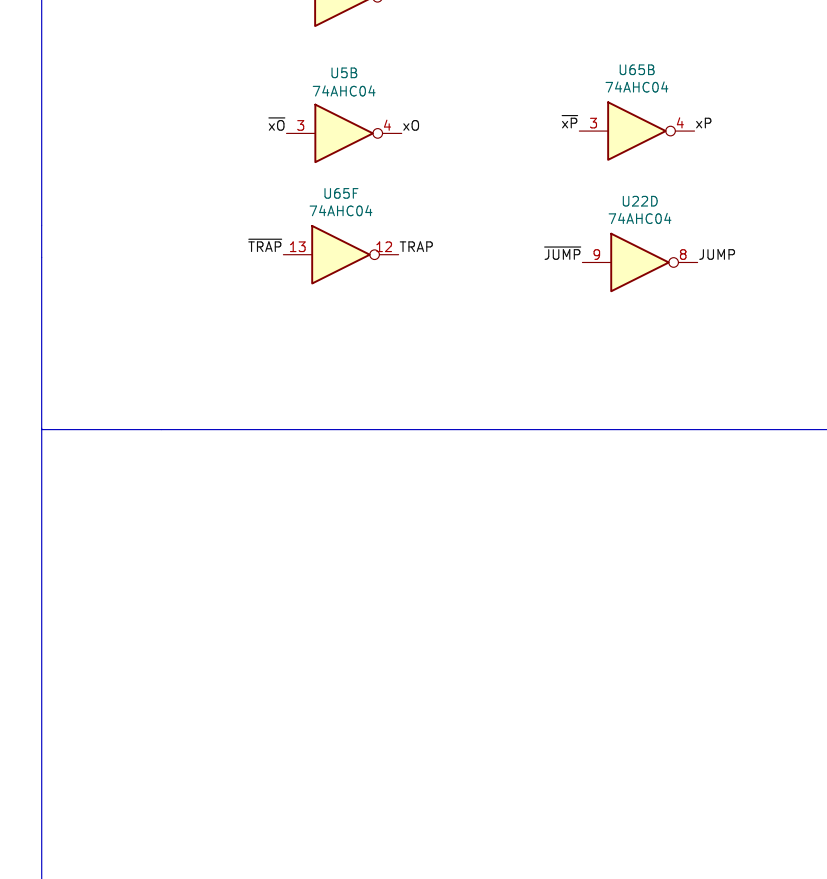
(LOCAL) Page Index (Stack Pointer)



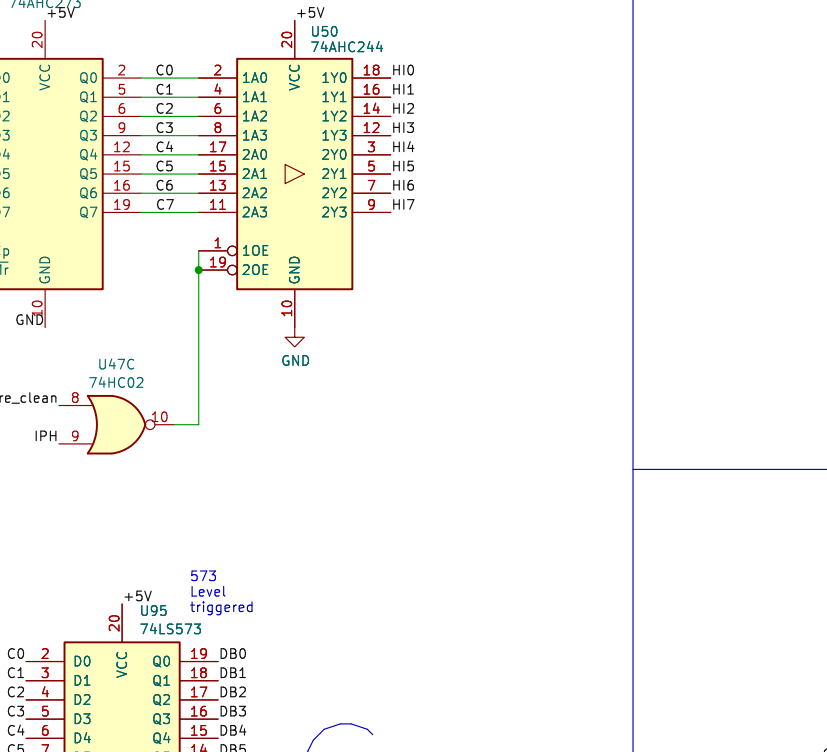
R-Fix



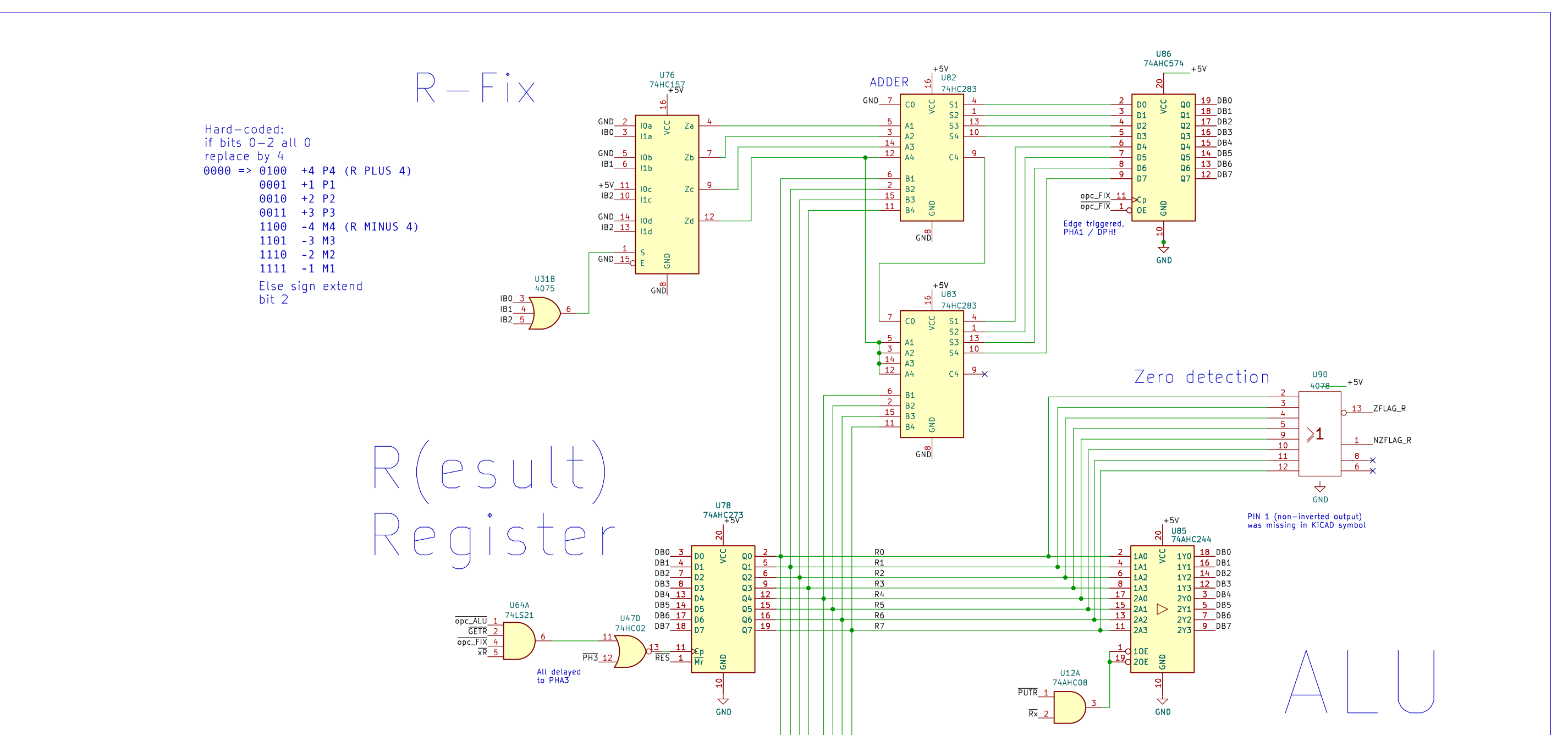
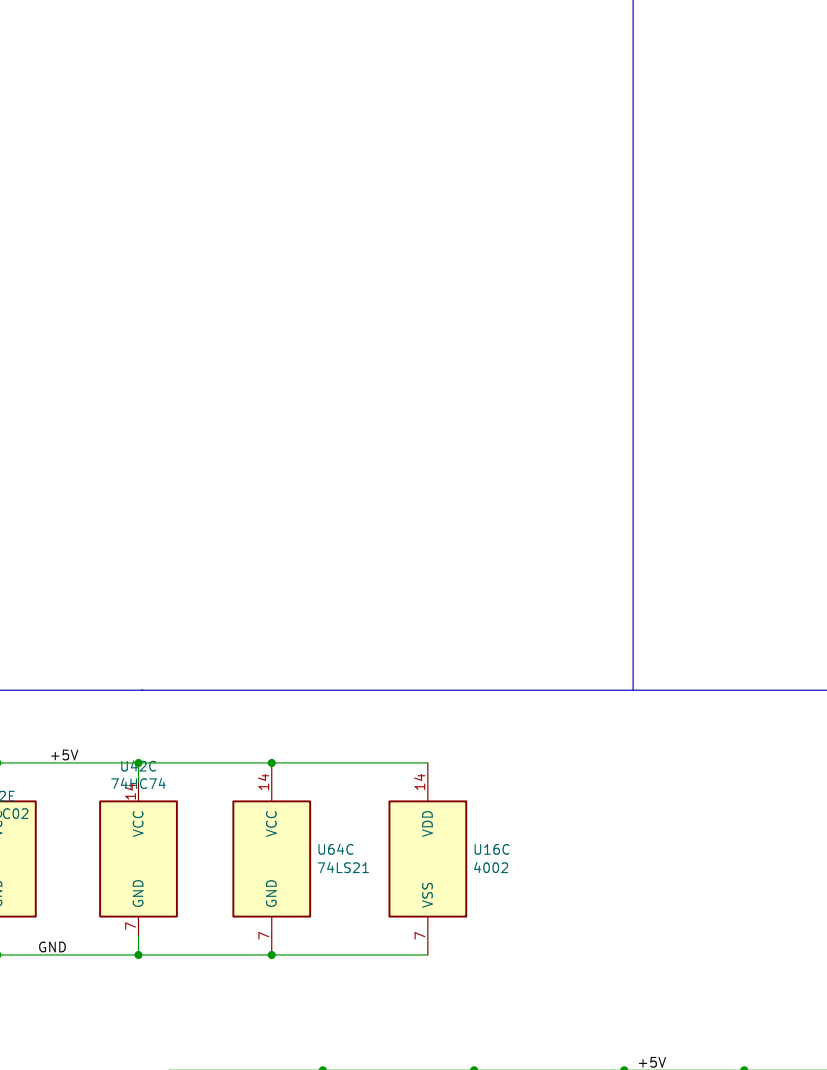
Inverters



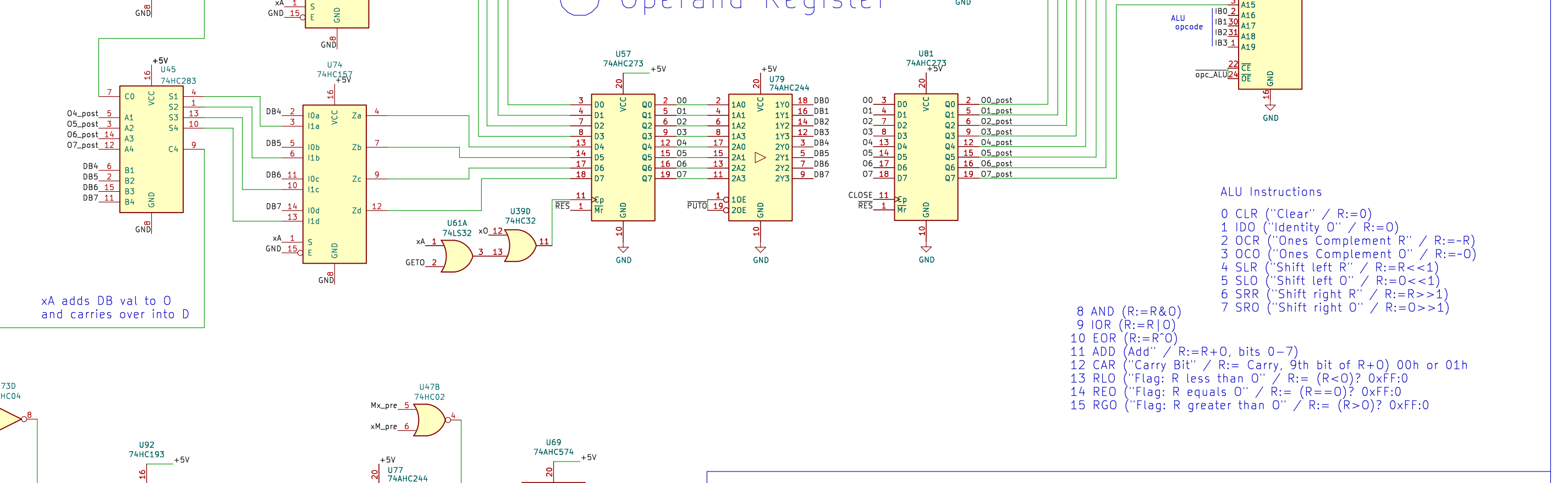
D (DATA) Page Index



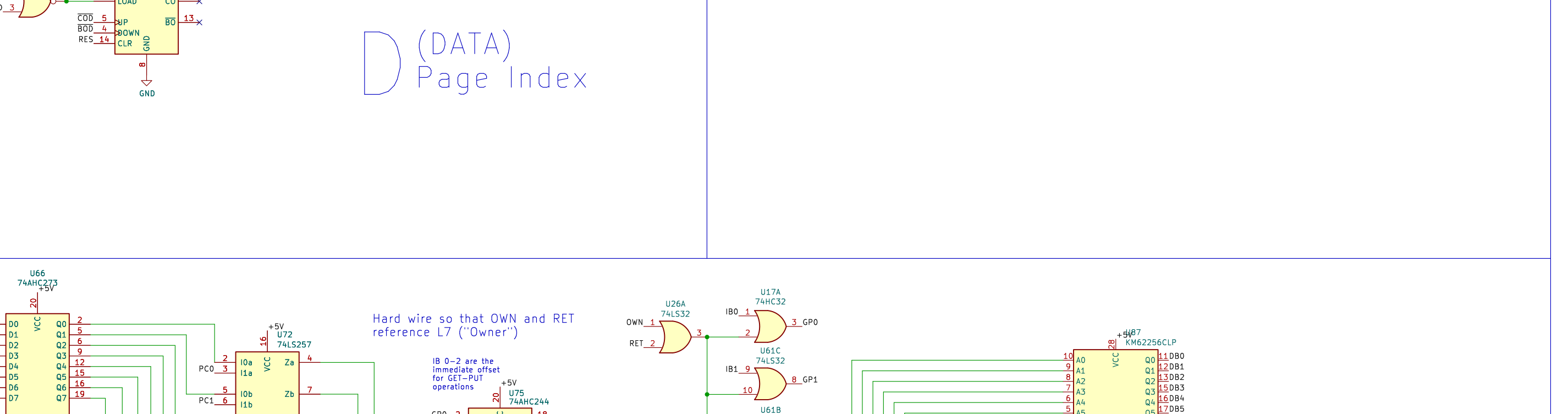
Co



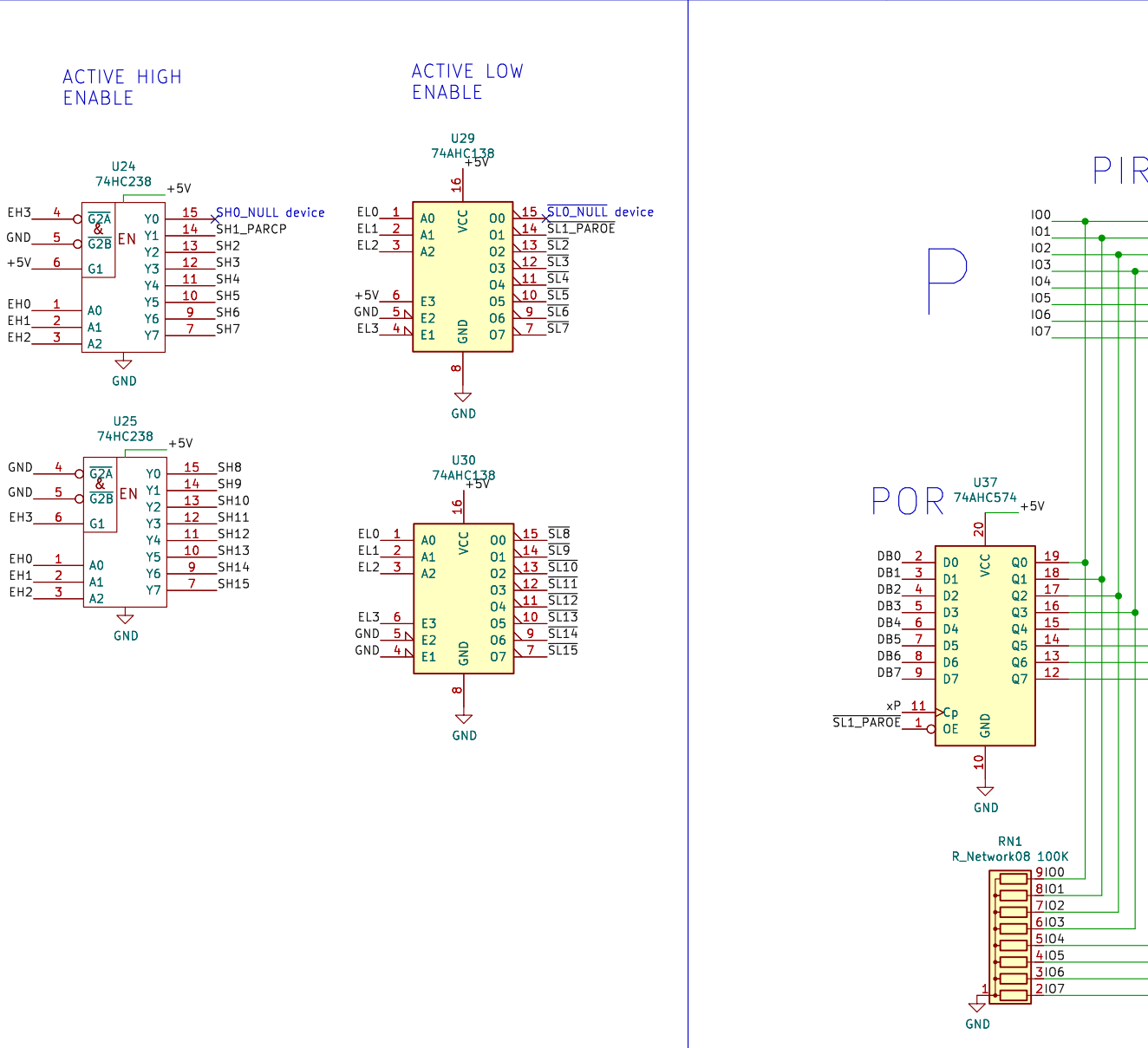
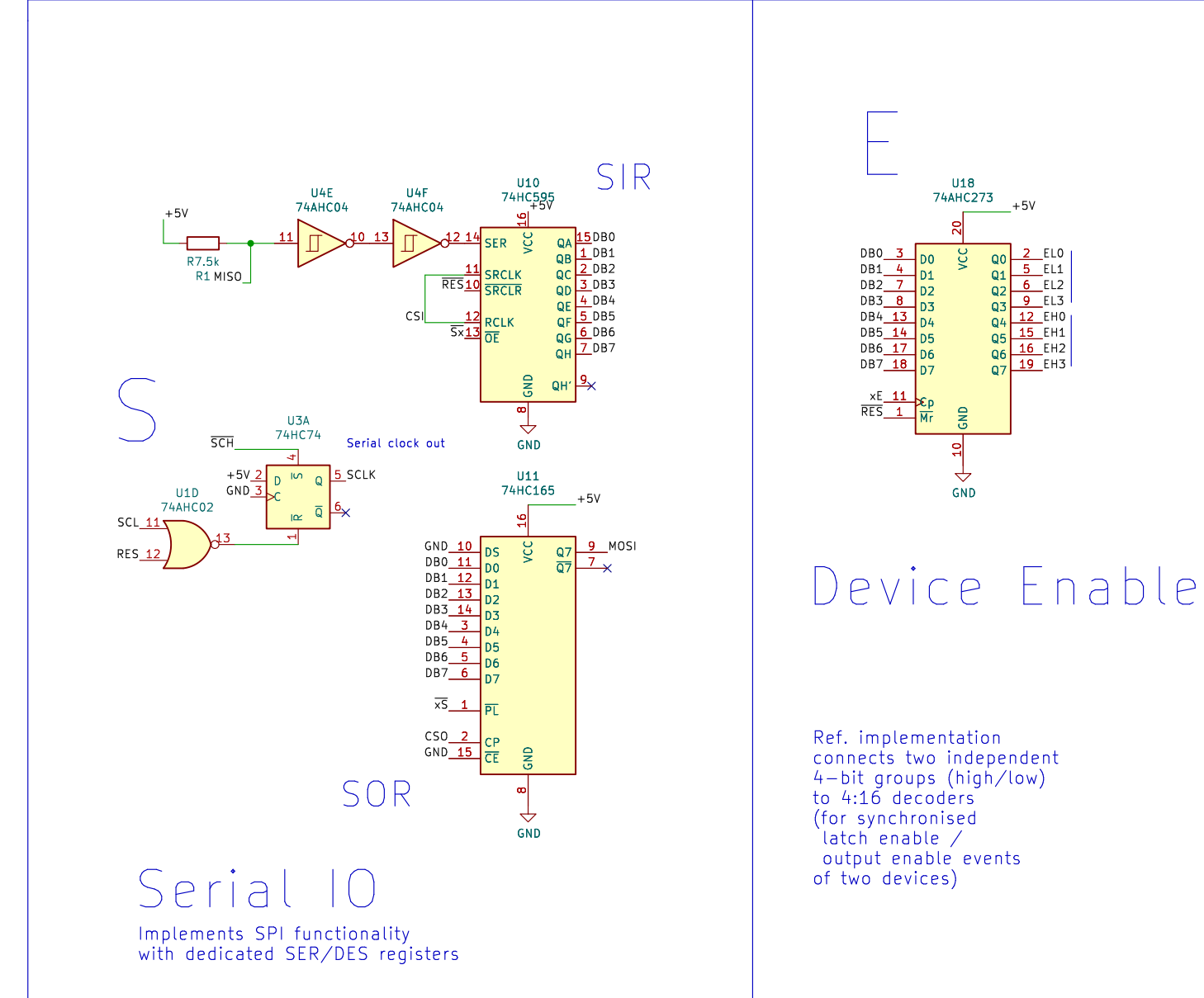
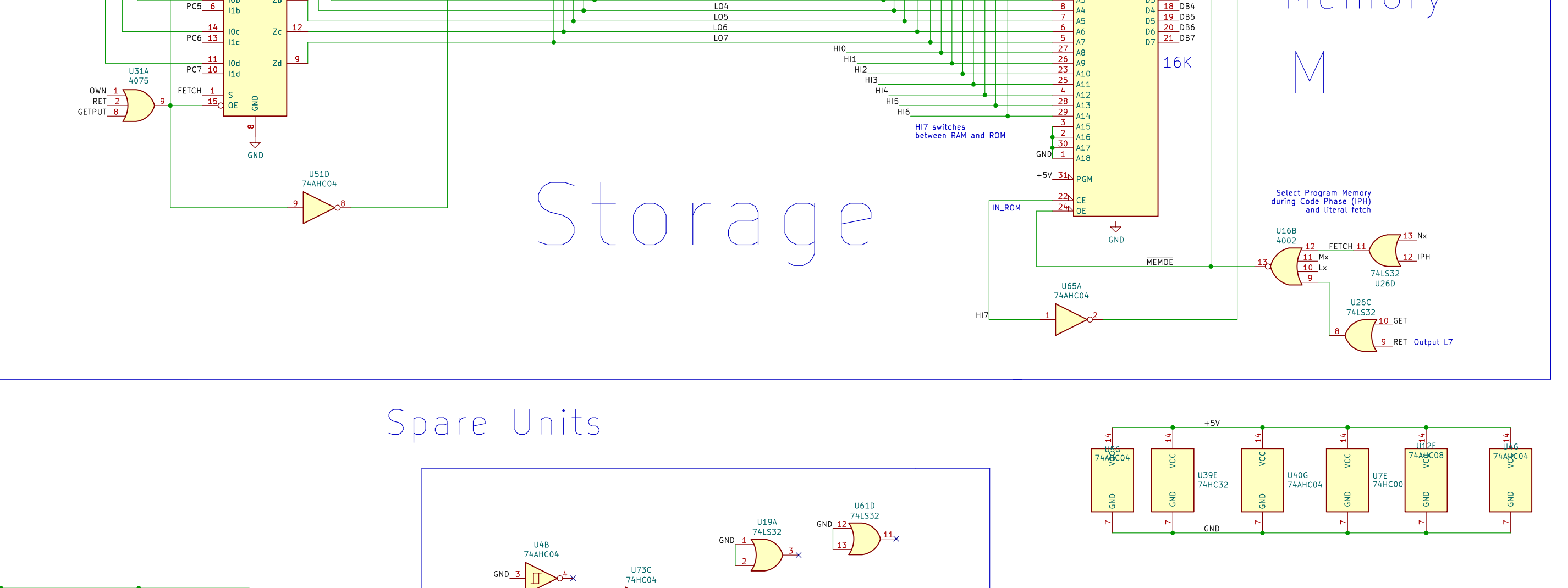
Implementation Semantics:



Low



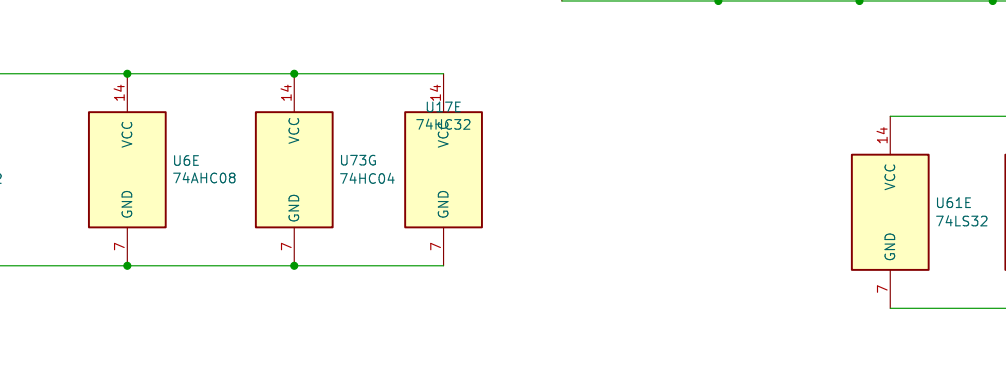
Memory



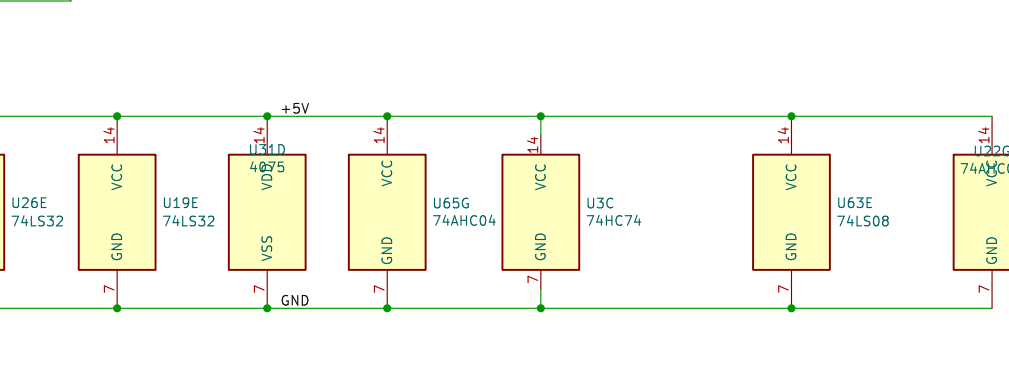
Parallel IO



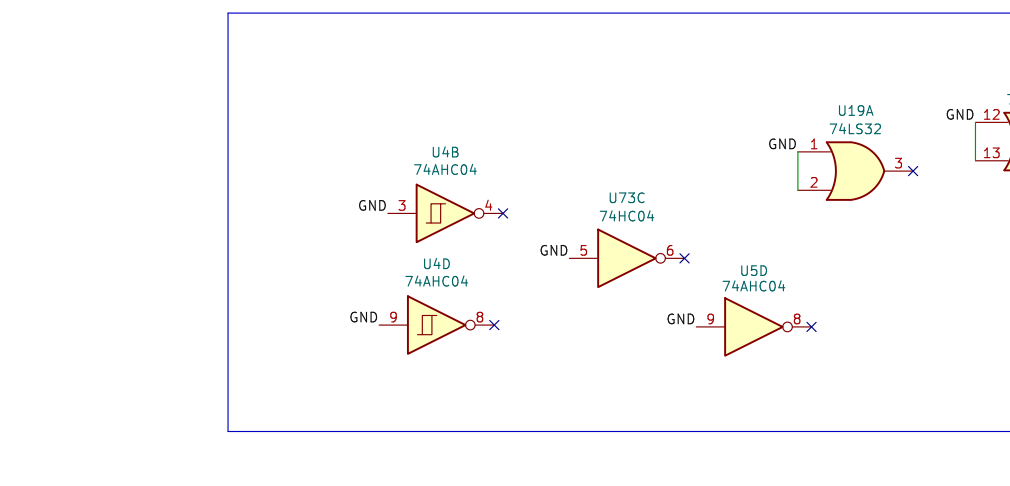
Glue Logic



Spare Units



Storage



Glue Logic

