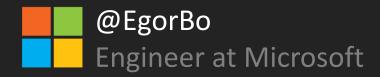
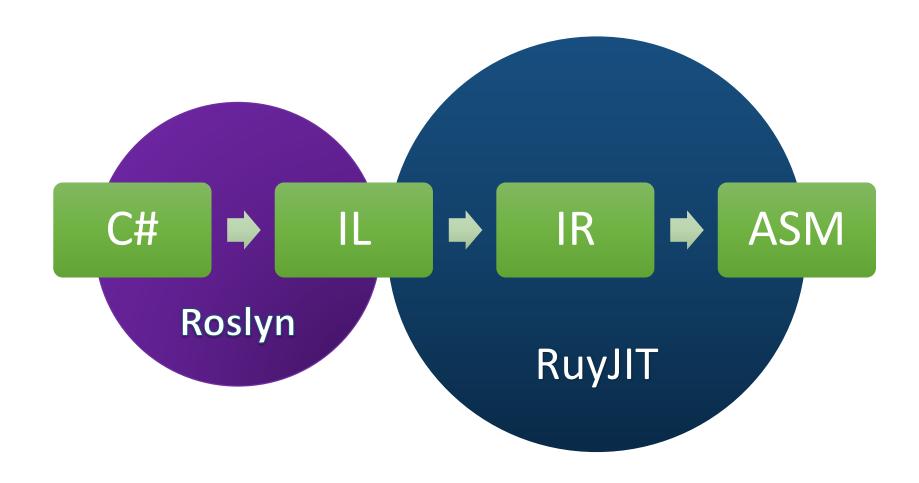
How to add an optimization for C# to JIT compiler



From C# to machine code



Let's start with a simple optimization

X/C

```
double y = x / 2;
                     = x * 0.5
                     = x * 0.5f
float y = x / 2;
double y = x / 10;
                     = x * 0.1
double y = x / 8; = x * 0.125
double y = x / -0.5; = x * -2
                         float x = 48.665f;
                         Console.WriteLine(x / 10f); // 4.8665
                         Console.WriteLine(x * 0.1f); // 4.8665004
```

X/C-let me optimize it in Roslyn!

```
static float GetSpeed(float distance, float time)
{
    return distance / time;
}
...
float speed = GetSpeed(distance, 2);
```

Does Roslyn see "X/C" here? NO! It doesn't inline methods

Where to implement my custom optimization?

Roslyn

- + No time constraints
- + It's written in C# easy to add optimizations, easy to debug and experiment
- No cross-assembly optimizations
- No CPU-dependent optimizations (IL is cross-platform)
- Doesn't know how the code will look like after inlining, CSE, loop optimizations, etc.
- F# doesn't use Roslyn

JIT



- + Inlining, CSE, Loop opts, etc phases create more opportunities for optimizations
- + Knows everything about target platform, CPU capabilities
- Written in C++, difficult to experiment
- Time constraints for optimizations (probably not that important with Tiering)

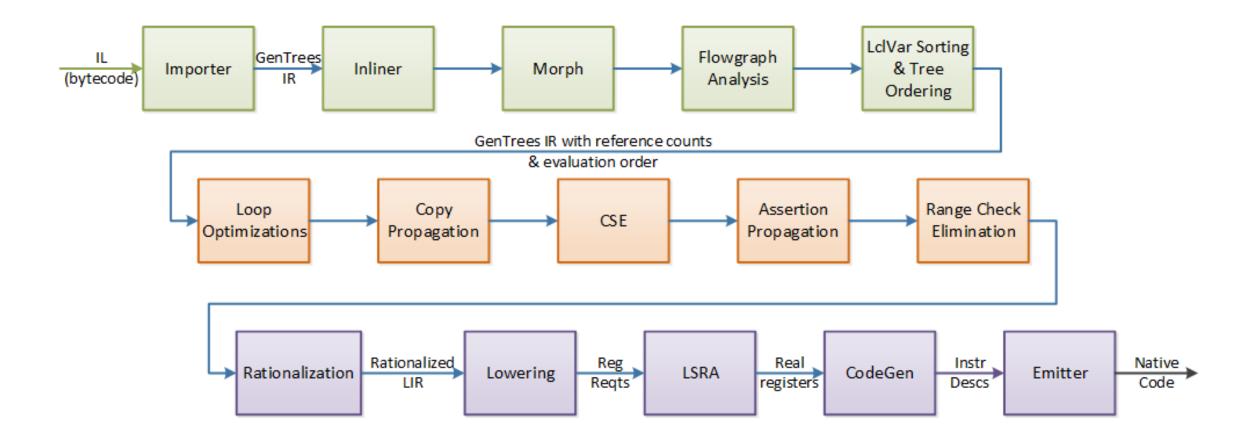
• R2R (AOT)

- + No time constraints (some optimizations are really time consuming, e.g. full escape analysis)
- No CPU-dependent optimizations
- Will be most likely re-jitted anyway?

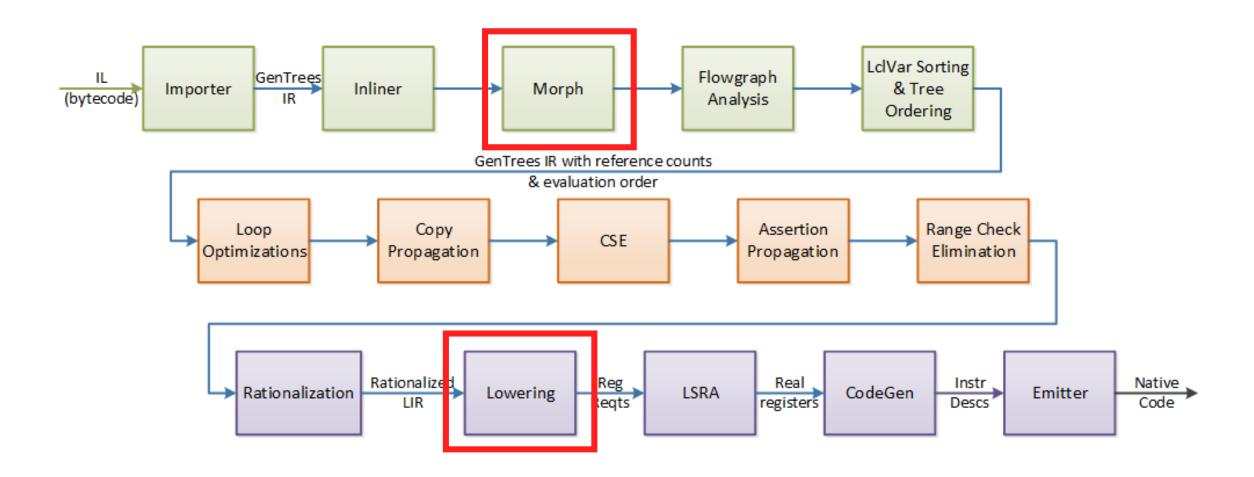
ILLink Custom Step

- + Cross-assembly IL optimizations
- + Written in C#
- + We can manually de-virtualize types/methods/calls (if we know what we are doing)
- Still no inlining, CSE, etc..

RuyJIT: phases

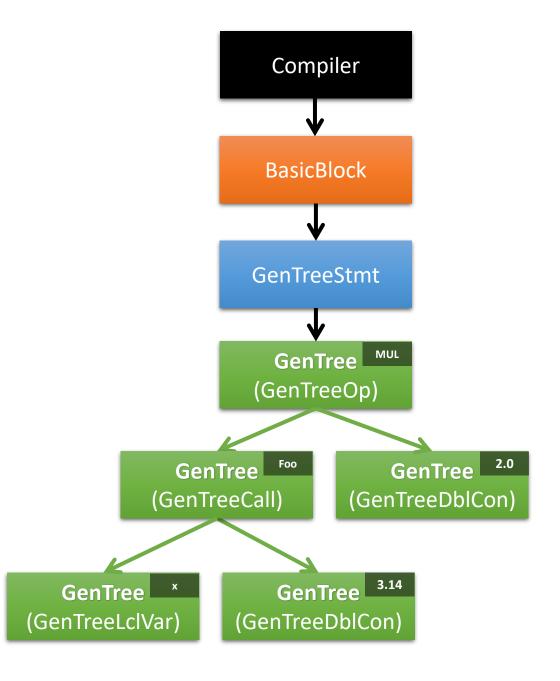


RuyJIT: where to morph my X/C?

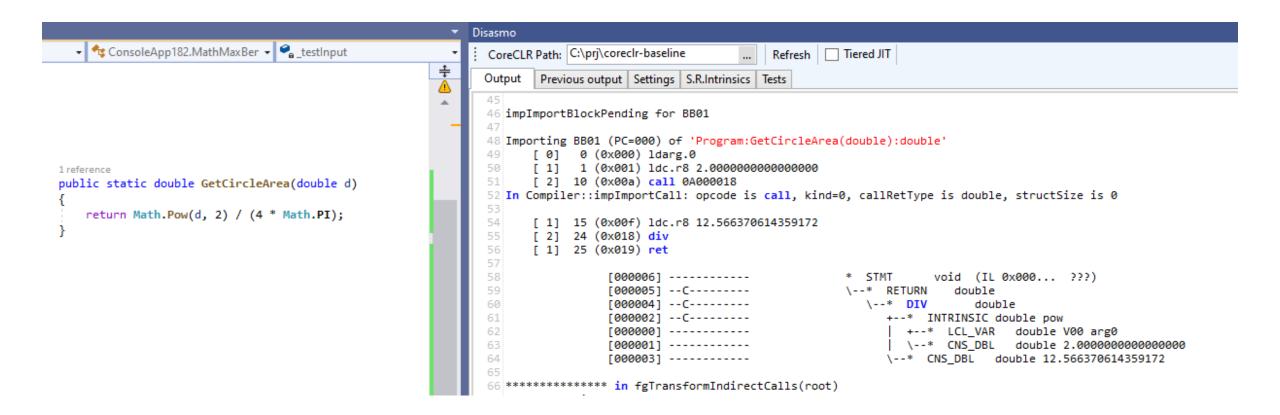


GenTree

```
static float Test(float x)
{
    return Foo(x, 3.14) * 2;
}
```

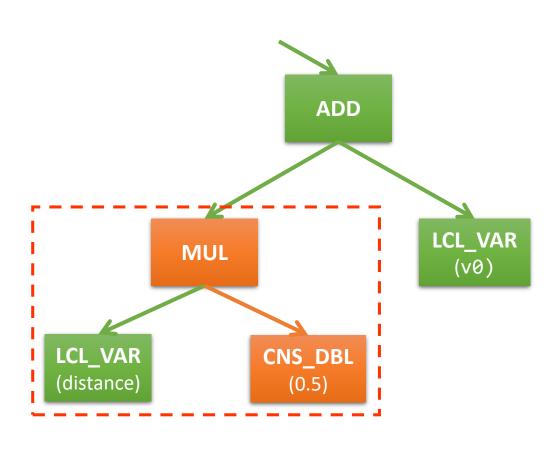


Dump IR via COMPlus_JitDump

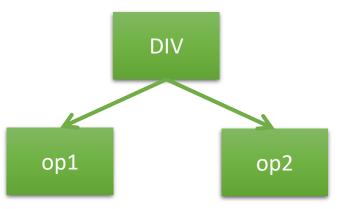


Back to X/C: Morph IR Tree

```
static float Calculate(float distance, float v0)
      return distance / 2 + v0;
                       ADD
                                  LCL_VAR
           DIV
                                    (v0)
                  CNS_DBL
LCL_VAT
(distance)
                     (2.0)
```

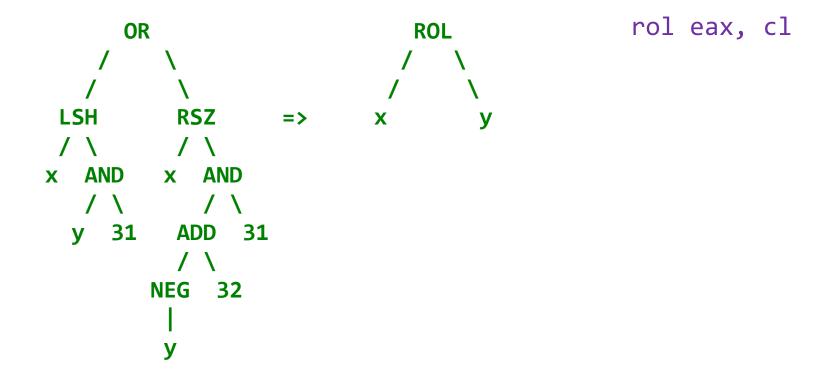


Implementing the opt in morph.cpp



```
case GT_DIV:
   // Replace "val / dcon" with "val * (1.0 / dcon)" if dcon is a power of two.
   // Powers of two within range are always exactly represented,
   // so multiplication by the reciprocal is safe in this scenario
   if (op2->IsCnsFltOrDbl())
       double divisor = op2->AsDblCon()->gtDconVal;
       if (((typ == TYP DOUBLE) && FloatingPointUtils::hasPreciseReciprocal(divisor)) ||
            ((typ == TYP FLOAT) && FloatingPointUtils::hasPreciseReciprocal(forceCastToFloat(divisor))))
           oper = GT MUL;
            tree->ChangeOper(oper);
           op2->AsDblCon()->gtDconVal = 1.0 / divisor;
```

Inspired by GT_ROL



JIT: Optimize "constant_string".Length #26000

note: Description
Descriptio

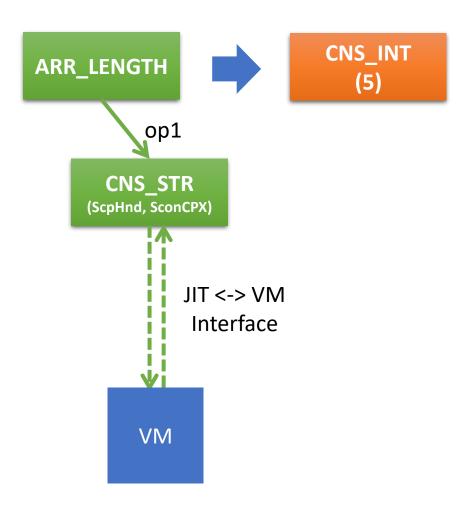
"Hello".Length -> 5

```
static void Append(string str)
    if (str.Length <= 2)</pre>
        QuickAppend(str);
    else
        SlowAppend(str);
```

```
builder.Append("/>");
    Inline, remove if (2 <= 2)
builder.QuickAppend("/>");
```

"Hello".Length => 5

```
case GT ARR LENGTH:
    if (op1->OperIs(GT_CNS_STR))
        GenTreeStrCon* strCon = op1->AsStrCon();
                                     Access VM's data from JIT
        int len = info.compCompHnd->getStringLength(
               strCon->gtScpHnd, strCon->gtSconCPX);
        return gtNewIconNode(len);
    break;
```

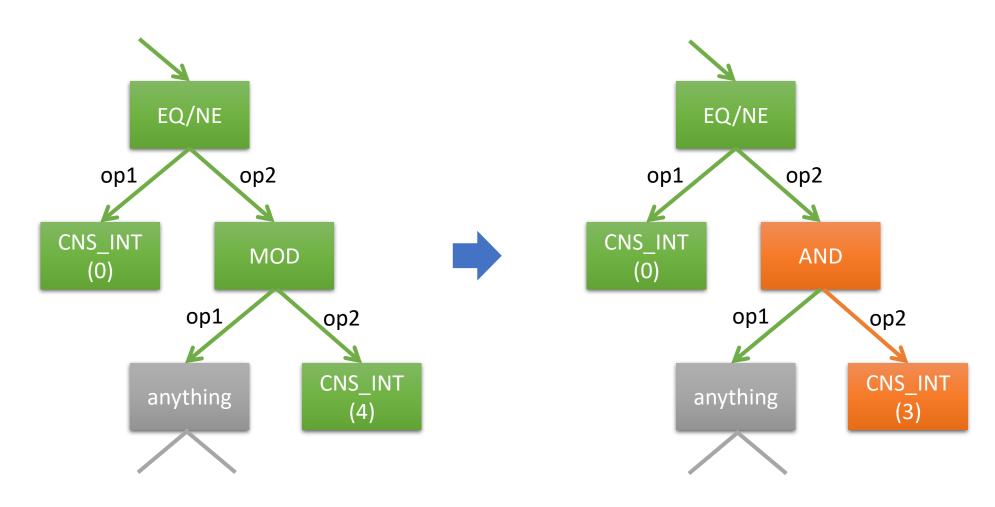


JIT: Transform X % C == 0 to X & (C-1) == 0 #25744

**Open EgorBo wants to merge 9 commits into dotnet:master from EgorBo:jit-opt-mod 🚉

bool Test1(int x) => x % 4 == 0;

bool Test1(int x) => x & 3 == 0;



Roslyn and "!=" operator

(unexpected optimization)

public static bool Test1(int x) => x != 42;

IL_0000: Idarg.0
IL_0001: Idc.i4.42

IL_0002: ceq

IL_0004: ldc.i4.0

IL_0005: ceq

IL_0007: ret

return (x == 42) == false

JIT: ok, it's **GT_NE**

public static bool Test2(int x) => x != 0;

IL_0000: Idarg.0

IL 0001: ldc.i4.0

IL_0002: cgt.un

IL_0004: ret

return (uint)x > 0

JIT: what?.. ok, GT_GT

Optimize Math.Pow(x, c) in JIT #26552

**Dopen EgorBo wants to merge 10 commits into dotnet:master from EgorBo:math-pow

```
Math.Pow(x, 2) \mid x * x
Math.Pow(x, 1) \mid x
Math.Pow(x, 4) \mid x * x * x * x
Math.Pow(x, -1) | 1 / x
// can be added:
Math.Pow(42, 3) | 74088
Math.Pow(1, x) | 1
Math.Pow(2, x) | exp2(x)
Math.Pow(x, \theta) | 1
Math.Pow(x, 0.5) | sqrt(x)
```

JIT: Improve constant folding for bitwise OR #27325

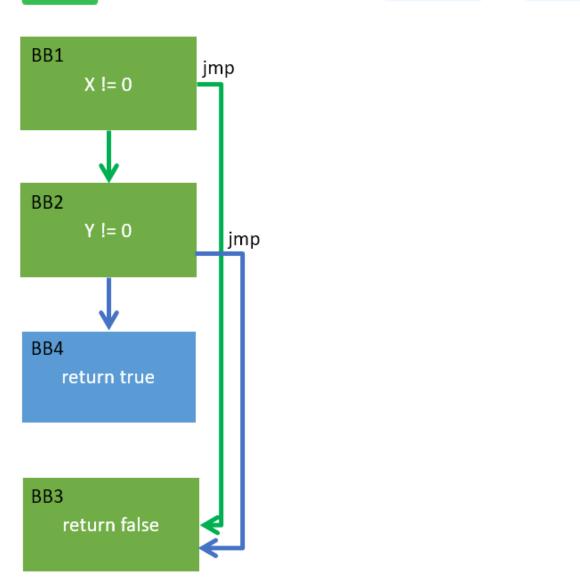
```
↑ Open EgorBo wants to merge 5 commits into dotnet:master from EgorBo:const-fold-or
```

Expand BBJ_RETURN blocks with bool conditions #27167

```
bool AreZero (int x, int y)
{
   return x == 0 && y == 0;
}
```

Expand BBJ_RETURN blocks with bool conditions #27167

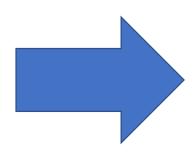
**Dopen EgorBo wants to merge 1 commit into dotnet:master from EgorBo:fg-experiments



Expand BBJ_RETURN blocks with bool conditions #27167

```
pool AreZero (int x, int y)
{
    return x == 0 && y == 0;
}
```

```
G M22205 IG01:
G M22205 IG02:
      test
             ecx, ecx
      jne SHORT G_M22205_IG05
                    ;; bbWeight=0.50
G M22205 IG03:
      test
             edx, edx
             al
      sete
      movzx
            rax, al
             ;; bbWeight=0.50
G_M22205_IG04:
      ret
G_M22205_IG05: ;; bbWeight=0.50
      xor
              eax, eax
G M22205 IG06: ;; bbWeight=0.50
      ret
; Total bytes of code: 16
```



JIT: Optimize simple range checks with `uint` hack #27480

🖺 Open EgorBo wants to merge 12 commits into dotnet:master from EgorBo:range-pattern 🚉

```
Program.cs + X
                                                           ConsoleApp182

    □ Case2(int i)

                   1 reference
               ...int Case1(int i, int[] array)
       15
                      if (i < 0 | | i > = array.Length) / if ((uint) i > = (uint) array.Length)
       16
                     ····throw·new·ArgumentException();
       17
       18
       19
               :...return array[i];
       20
       21
                                 100 %
           No issues found
tmpD432 - Copy.tmp vs. tmpD433.tmp 💠 🗶
                                                                    tmpD433.tmp
tmpD432 - Copy.tmp
        ; Method C:Case1(int,ref):int:this
                                                                               ; Method C:Case1(int,ref):int:this
                                                                          2
        G_M63057_IG01:
                                                                               G M63057 IG01:
        ····rsi
                                                                               ····rsi
                                                                               -----sub----rsp, 32
        ·····sub····rsp,·32
        G M63057 IG02:
                                                                               G M63057 IG02:
                        edx, edx
          .....jl......SHORT-G M63057 IG05
               -mov · · · · · eax, · dword · ptr · [r8+8]
                                                                                               eax, dword ptr [r8+8]
                                                                                               eax, edx
  11
         ·····cmp·····eax, edx
                                                                               -----cmp-----
         ······ile·····SHORT·G M63057 IG05
                                                                         10
                                                                                          .....SHORT G M63057 IG05
  13
                                                                         11
  14
                                                                         12
                                                                               G M63057 IG03:
        G M63057 IG03:
  15
                        edx, eax
                    · · · · · SHORT · G M63057 IG06
  16
               ·movsxd···rax, ·edx
                                                                                      movsxd···rax, edx
                                                                         13
         .....mov....eax,.dword.ptr.[r8+4*rax+16]
                                                                               .....mov....eax, dword.ptr.[r8+4*rax+16]
```

JIT: Optimize simple range checks with `uint` hack #27480

↑ Open EgorBo wants to merge 12 commits into dotnet:master from EgorBo:range-pattern

```
Program.cs* ≠ ×
                                           - % C
ConsoleApp182

→ Ø Main()

      25
      26
                 1 reference
            ⊡ ····int·Case2(int·i)
      28
             +····if·(i·<·10·||·i·>·100)······//·if·((uint)i·-·10·>·90)
      29
             +···+·····throw·new·ArgumentException();
      30
      31
      32
             +···+··return-42;
      33
      34
      35
          No issues found
tmp92C2 - Copy.tmp vs. tmp92C3.tmp 😕 🗶
                                                                   tmp92C3.tmp
tmp92C2 - Copy.tmp
        ; Method C:Case2(int):int:this
                                                                             ; Method C:Case2(int):int:this
                                                                             G M63058 IG01:
       G_M63058_IG01:
       ····rsi
                                                                             ····rsi
       ·····sub····rsp,·32
                                                                             -----sub----rsp, 32
        G M63058 IG02:
                                                                             G M63058 IG02:
                                                                             -----add----edx, --10
        ·····cmp·····edx, ·10
        ·····cmp·····edx, 90
        ·····cmp·····edx, 100
                                                                             .......ja......SHORT-G M63058 IG05
  10
                                                                        10
  11
        .....jg......SHORT-G_M63058_IG05
  12
                                                                        11
  13
       G M63058 IG03:
                                                                             G M63058_IG03:
                                                                        12
  14
        -----mov----eax, 42
                                                                        13
                                                                             -----mov----eax, 42
  15
                                                                        14
        C MCDOED TOOM.
                                                                             G MCDOES TOOM.
```

Disasmo

Auto-vectorization

```
🕶 🔩 Program

    Φ<sub>a</sub> MyTest(float* array, int lenc ▼

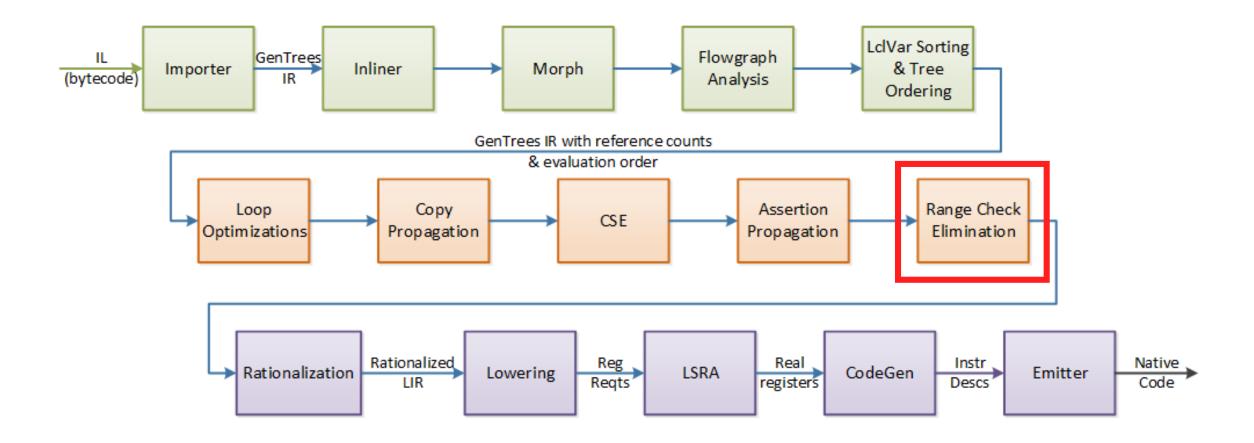
                                                                        CoreCLR Path: C:\prj\coreclr-baseline
oleApp182
                                                                                                                             CoreCLR Path: C:\prj\coreclr-egor
                                                                                                                                                                      Refre
                                                                                                                  Refresh
                                                                                                                                    Previous output | Settings | S.R.Intrinsics | Tests
                                                                                Previous output | Settings | S.R.Intrinsics | Tests
                                                                                                                            Output
       0 references
      □unsafe · class · Program
                                                                                                                            1; Method Program: MyTest(long,int)
                                                                        1; Method Program: MyTest(long,int)
                                                                                                                             G_M57665_IG01:
                                                                         G M57665 IG01:
           0 references
                                                                                                                                    vzeroupper
      ⊟ ····static·void·Main()
                                                                                vzeroupper
                                                                         G M57665 IG02:
                                                                                                                             G M57665 IG02:
        :-----float[]-array-=-new-float[1024];
                                                                                                                                             eax, eax
                                                                                xor
                                                                                         eax, eax
        fixed (float* p = array)
                                                                                test
                                                                                                                                    test
                                                                                                                                             edx, edx
                                                                                         edx, edx
        MyTest(p, 0);
                                                                                                                                    ile
                                                                                                                                             SHORT G M57665 IG04
                                                                                ile
                                                                                         SHORT G M57665 IG04
 9
10
                                                                         G M57665 IG03:
                                                                                             ;; bbWeight=4
                                                                                                                             G M57665 IG03:
                                                                                                                                                 ;; bbWeight=4
                                                                                                                                             ymm0, ymm0, ymm0
                                                                                movsxd
                                                                                         r8, eax
                                                                                                                                    vxorps
11
                                                                                                                                            ymmword ptr[rcx], ymm0
                                                                                         xmm0, xmm0
                                                                                vxorps
           1 reference
                                                                                         dword ptr [rcx+4*r8], xmm0
                                                                                                                                    inc
                                                                                                                                             eax
      vmovss
12
                                                                                         r8d, [rax+1]
                                                                                                                                             eax, edx
                                                                                lea
                                                                                                                                    CMD
13
                                                                                         r9, r8d
                                                                                                                                    j1
                                                                                                                                             SHORT G M57665 IG03
                                                                                movsxd
      14
                                                                                vmovss
                                                                                         dword ptr [rcx+4*r9], xmm0
15
                                                                                                                           8 G M57665 IG04:
                                                                                lea
                                                                                         r9d, [rax+2]
16
        ····//-partially-unrolled-loop:
                                                                                                                                    vzeroupper
                                                                                         r9, r9d
                                                                                movsxd
                  ··array[i·+·0]·=·0;
17
                                                                                                                                    ret
                                                                                vmovss
                                                                                         dword ptr [rcx+4*r9], xmm0
                   -array[i·+·1]·=·0;
                                                                                                                               Total bytes of code: 27
18
                                                                                lea
                                                                                         r9d, [rax+3]
                                                                                         r9, r9d
                   -array[i·+·2]·=·0;
                                                                                movsxd
19
                                                                                         dword ptr [rcx+4*r9], xmm0
                                                                                vmovss
                   rarray[i·+·3]·=·0;
20
                                                                                lea
                                                                                         r9d, [rax+4]
                   rav[i·+·4]·=·0;
21
                                                                                         r9, r9d
                                                                                movsxd
22
                   rarray[i·+·5]·=·0;
                                                                                vmovss
                                                                                         dword ptr [rcx+4*r9], xmm0
23
                  ··array[i·+·6]·=·0;
                                                                                lea
                                                                                         r9d, [rax+5]
                   -array[i·+·7]·=·0;
24
                                                                                         r9, r9d
                                                                                movsxd
25
                                                                                vmovss
                                                                                         dword ptr [rcx+4*r9], xmm0
                                                                                lea
                                                                                         r9d, [rax+6]
        ····//·JIT·will·replace·it·with
26
                                                                                         r9, r9d
                                                                                movsxd
        ....//-Avx.Store(a, Vector256<double>.Zero);
27
                                                                                vmovss
                                                                                         dword ptr [rcx+4*r9], xmm0
        ·····//····or·Vector256.Create(AnyValue);
28
                                                                                add
                                                                                         eax, 7
        +----}
29
                                                                                movsxd
                                                                                         rax, eax
        +---}
30
                                                                                vmovss
                                                                                         dword ptr [rcx+4*rax], xmm0
31
                                                                                         eax, r8d
                                                                                mov
32
                                                                                         eax, edx
```

Disasmo

Auto-vectorization

```
**** BB01
STMT00000 (IL 0x000...0x007)
                                           * ASG
                                                      int
N009 ( 8, 8) [000009] -A-XG-----
N007 ( 6, 6) [000008] *--X---N----
                                                        int
                                                              $44
                                           +--* IND
N006 ( 4, 5) [000006] -----N----
                                            \--* ADD
                                                           long $142
N001 ( 1, 1) [000000] -----
                                                +--* LCL VAR long V01 arg1
N005 ( 3, 4) [000005] -----N----
                                               \--* LSH
                                                              long $141
N003 ( 2, 3) [000002] -----
                                                  +--* CAST
                                                                long <- int $140
N002 ( 1, 1) [000001] -----
                                                  \--* LCL VAR int V02 arg2
                                                  \--* CNS INT long 2 $180
N004 ( 1, 1) [000004] -----
N008 ( 1, 1) [000007] -----
                                           \--* CNS INT
                                                       int
                                                              0 $44
**** BB01
STMT00001 (IL 0x008...0x011)
N011 ( 10, 10) [000021] -A-XG-----
                                           * ASG
                                                      int
N009 ( 8, 8) [000020] *--X---N----
                                           +--* IND
                                                        int
                                                              $44
N008 ( 6, 7) [000018] -----N----
                                             \--* ADD
                                                           long $145
      1, 1) [000010] -----
                                                +--* LCL VAR long V01 arg1
N001 (
                                                             long
N007 ( 5, 6) [000017] -----N----
                                                \--* LSH
                                                                   $144
N005 ( 4, 5) [000014] -----
                                                  +--* CAST
                                                                long <- int $143
N004 ( 3, 3) [000013] -----
                                                     \--* ADD
                                                                   int
                                                                         $200
                                                                    int V02 arg2
N002 ( 1, 1) [000011] -----
                                                       +--* LCL VAR
                                                       \--* CNS INT
                                                                           1 $40
N003 ( 1, 1) [000012] -----
                                                                     int
N006 ( 1, 1) [000016] -----
                                                  \--* CNS INT long
                                                                     2 $180
N010 ( 1, 1) [000019] -----
                                               CNS INT
                                                        int
                                                              0 $44
```

•••



```
public static void Test(int[] a)
    a[0] = 4;
    a[1] = 2;
    for (int i = 0; i < a.Length; i++)</pre>
        a[i] = 0;
    a[1] = 2;
```

```
public static void Test(int[] a)
    if (a.Length <= 0) throw new IndexOutOfRangeException();</pre>
    a[0] = 4;
    if (a.Length <= 1) throw new IndexOutOfRangeException();</pre>
    a[1] = 2;
    for (int i = 0; i < a.Length; i++)
        if (a.Length <= i) throw new IndexOutOfRangeException();</pre>
        a[i] = 0;
    if (a.Length <= 2) throw new IndexOutOfRangeException();</pre>
    a[1] = 2;
```

```
public static void Test(int[] a)
    if (a.Length <= 0) throw new IndexOutOfRangeException();</pre>
    a[0] = 4;
    if (a.Length <= 1) throw new IndexOutOfRangeException();</pre>
    a[1] = 2;
    for (int i = 0; i < a.Length; i++)</pre>
        if (a.Length <= i) throw new IndexOutOfRangeException();</pre>
        a[i] = 0;
    if (a.Length <= 2) throw new IndexOutOfRangeException();</pre>
    a[1] = 2;
```

```
public static void Test(int[] a)
    if (a.Length <= 1) throw new IndexOutOfRangeException();</pre>
    a[1] = 2;
    if (a.Length <= 1) throw new IndexOutOfRangeException();</pre>
    a[0] = 4;
    for (int i = 0; i < a.Length; i++)</pre>
        if (a.Length <= i) throw new IndexOutOfRangeException();</pre>
        a[i] = 0;
    if (a.Length <= 2) throw new IndexOutOfRangeException();</pre>
    a[1] = 2;
```

```
public static void Test(int[] a)
{
    if (a.Length <= 1) throw new IndexOutOfRangeException();
    a[1] = 2;
    a[0] = 4;
    for (int i = 0; i < a.Length; i++)
    {
        a[i] = 0;
    }
    a[1] = 2;
}</pre>
```

rangecheck.cpp (simplified)

```
void RangeCheck::OptimizeRangeCheck(GenTreeBoundsChk* bndsChk)
   // Get the range for this index.
    Range range = GetRange(...);
    // If upper or lower limit is unknown, then return.
    if (range.UpperLimit().IsUnknown() | range.LowerLimit().IsUnknown())
       return;
    // Is the range between the lower and upper bound values.
    if (BetweenBounds(range, 0, bndsChk->gtArrLen))
        m pCompiler->optRemoveRangeCheck(treeParent, stmt);
   return;
```

rangecheck.cpp (simplified)

```
void RangeCheck::OptimizeRangeCheck(GenTreeBoundsChk* bndsChk)
   // Get the range for this index.
    Range range = GetRange(...);
    // If upper or lower limit is unknown, then return.
    if (range.UpperLimit().IsUnknown() | range.LowerLimit().IsUnknown())
        return;
    // Is the range between the lower and upper bound values.
    if (BetweenBounds(range, 0, bndsChk->gtArrLen))
        m pCompiler->optRemoveRangeCheck(treeParent, stmt);
   return;
```

rangecheck.cpp (simplified)

```
void RangeCheck::OptimizeRangeCheck(GenTreeBoundsChk* bndsChk)
   // Get the range for this index.
    Range range = GetRange(...);
    // If upper or lower limit is unknown, then return.
    if (range.UpperLimit().IsUnknown() | range.LowerLimit().IsUnknown())
       return;
    // Is the range between the lower and upper bound values.
    if (BetweenBounds(range, 0, bndsChk->gtArrLen))
        m_pCompiler->optRemoveRangeCheck(treeParent, stmt);
    return;
```

Byte array

```
; Method P:GetByte(int):ubyte
G_M5240_IG01:
                rsi
       push
       sub
                rsp, 32
                esi, ecx
       mov
G M5240 IG02:
                rcx, 0xD1FFAB1E
       mov
                edx, 1
       mov
                CORINFO_HELP_GETSHARED_NONGCSTATIC_BASE
       call
                rax, 0xD1FFAB1E
       mov
                rax, gword ptr [rax]
       mov
                esi, dword ptr [rax+8]
       cmp
       jae
                SHORT G_M5240_IG04
                rdx, esi
       movsxd
                rax, byte ptr [rax+rdx+16]
       movzx
G_M5240_IG03:
       add
                rsp, 32
                rsi
       pop
       ret
G_M5240_IG04:
                CORINFO_HELP_RNGCHKFAIL
       call
       int3
; Total bytes of code: 65
```

Byte array: Roslyn hack (new feature)

```
private static ReadOnlySpan<byte> _data =>
                                                            ; Method P:GetByte(int):ubyte
         new byte[256] { 1, 2, 3, ... };
                                                            G_M5244_IG01:
                                                                   sub
                                                                          rsp, 40
                                                            G M5244 IG02:
                                                                           ecx, 256
public static byte GetByte(int i)
                                                                  cmp
                                                                          SHORT G_M5244_IG04
                                                                  jae
                                                                   movsxd
                                                                          rax, ecx
                                                                          rdx, 0xD1FFAB1E
                                                                   mov
     return _data[i];
                                                                          rax, byte ptr [rax+rdx]
                                                                   movzx
                                                            G_M5244_IG03:
                                                                          rsp, 40
                                                                   add
                                                                   ret
                                                            G_M5244_IG04:
```

call

int3

; Total bytes of code: 37

CORINFO HELP RNGCHKFAIL

Byte array: byte index (my PR)

```
; Method Program:GetByte(int):ubyte

G_M30997_IG01:

G_M30997_IG02:
    movzx    rax, cl
    movsxd    rax, eax
    mov    rdx, 0xD1FFAB1E
    movzx    rax, byte    ptr [rax+rdx]

G_M30997_IG03:
    ret
; Total bytes of code: 21
```

Byte indexer will never go out of bounds!

rangecheck.cpp (simplified)

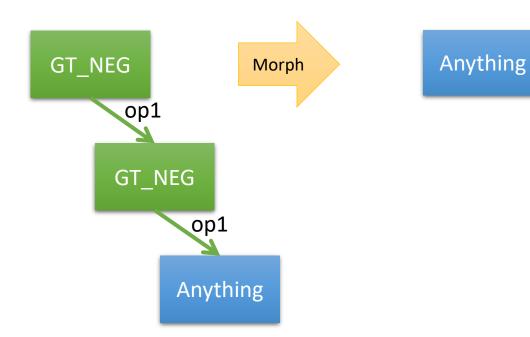
```
void RangeCheck::OptimizeRangeCheck(GenTreeBoundsChk* bndsChk)
   // Get the range for this index.
   Range range = GetRange(...); [Byte.MinValue ... Byte.MaxValue]
   // If upper or lower limit is unknown, then return.
    if (range.UpperLimit().IsUnknown() | range.LowerLimit().IsUnknown())
       return;
    // Is the range between the lower and upper bound values.
    if (BetweenBounds(range, 0, bndsChk->gtArrLen)) ArrLen = 256
       m pCompiler->optRemoveRangeCheck(treeParent, stmt);
   return;
```

Homework! Fix JIT: Optimize -(-x) to x #27442

① Open EgorBo opened this issue 4 days ago · 1 comment

```
int Foo(int a)
{
    return -(-a);
}
```

- 1) Clone CoreCLR repo
- 2) Build it: build.cmd -checked -skiptests
- 3) Open CoreCLR.sln
- 4) Optional: follow debugging-instructions.md
- 5) Open morph.cpp, line ~12755 (`case: GT_NEG`)
- 6) Optimize ©



Loop-related optimizations

Loop Invariant Code Hoisting

```
public static bool Test(int[] a, int c)
{
    for (int i = 0; i < a.Length; i++)
    {
        if (a[i] == c + 44)
            return false;
    }
    return true;
}</pre>
```

Loop Invariant Code Hoisting

```
public static bool Test(int[] a, int c)
{
    int tmp = c + 44;
    for (int i = 0; i < a.Length; i++)
    {
        if (a[i] == tmp)
            return false;
    }
    return true;
}</pre>
```

NYI: Loop-unrolling

```
public static int Test(int[] a)
{
    int sum = 0;
    for (int i = 0; i < a.Length; i++)
    {
        sum += a[i];
    }
    return sum;
}</pre>
```

NYI: Loop-unrolling

```
public static int Test(int[] a)
{
    int sum = 0;
    for (int i = 0; i < a.Length - 3; i += 4)
    {
        sum += a[i];
        sum += a[i+1];
        sum += a[i+2];
        sum += a[i+3];
    }
    return sum;
}</pre>
```

NYI: Loop-unswitch

```
public static int Test(int[] a, bool condition)
{
    int agr = 0;
    for (int i = 0; i < a.Length; i++)
    {
        if (condition)
            agr += a[i];
        else
            agr *= a[i];
    }
    return agr;
}</pre>
```

NYI: Loop-unswitch

```
public static int Test (int[] a, bool condition)
{
    int agr = 0;
    if (condition)
        for (int i = 0; i < a.Length; i++)
            agr += a[i];
    else
        for (int i = 0; i < a.Length; i++)
            agr *= a[i];
    return agr;
}</pre>
```

NYI: Loop-deletion

```
public static void Test()
{
    for (int i = 0; i < 10; i++) { }
}</pre>
```

NYI: Loop-deletion

```
public static void Test()
{
}
```

```
; Method Program:DeadLoop()
    ret
; Total bytes of code: 1
```

```
public static void Zero1000Elements(int[] array)
{
    for (int i = 0; i < 1000; i++)
        array[i] = 0; // bound checks will be inserted here
}</pre>
```

```
public static void Zero1000Elements(int[] array)
{
   int limit = Math.Min(array.Length, 1000);

   for (int i = 0; i < limit; i++)
        array[i] = 0; // bound checks are not needed here!

   for (int i = limit; i < 1000; i++)
        array[i] = 0; // bound checks are needed here

   // so at least we could "zero" first `limit` elements without bound checks
}</pre>
```

```
public static void Zero1000Elements(int[] array)
    int limit = Math.Min(array.Length, 1000);
    for (int i = 0; i < limit - 3; i += 4)
       array[i] = 0;
       array[i+1] = 0;
                           Now we can even unroll the first loop!
       array[i+2] = 0;
       array[i+3] = 0;
    for (int i = limit; i < 1000; i++)
        array[i] = 0; // bound checks are needed here
   // so at least we could "zero" first `limit` elements without bound checks
```

```
public static void Zero1000Elements(int[] array)
{
   int limit = Math.Min(array.Length, 1000);

   memset(array, 0, limit);   Or just replace with memset call

   for (int i = limit; i < 1000; i++)
        array[i] = 0; // bound checks are needed here

   // so at least we could "zero" first `limit` elements without bound checks
}</pre>
```

Q&A

Twitter: EgorBo