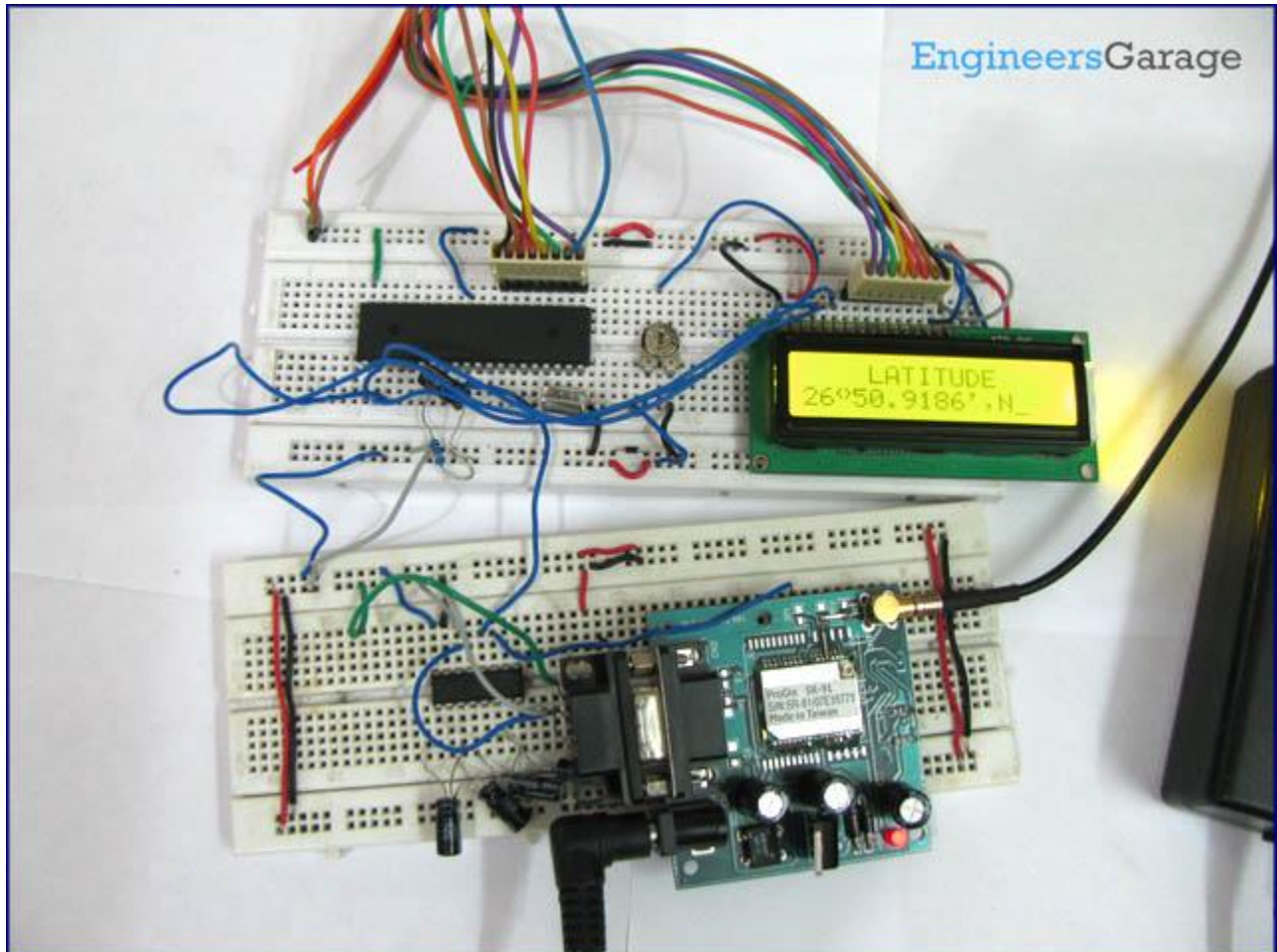


How to interface GPS with 8051 Microcontroller (AT89C51)

The GPS Module has been interfaced with AT89C51 and the location values are displayed on a 16x2 LCD interface.



The GPS module continuously transmits serial data (RS232 protocol) in the form of sentences according to NMEA standards. The latitude and longitude values of the location are contained in the **GPGGA** sentence (refer NMEA format). In this program, these values are extracted from the GPGGA sentence and are displayed on LCD.

The serial data is taken from the GPS module through MAX232 into the SBUF register of 8051 controller. The serial data from the GPS receiver is taken by using the Serial Interrupt of the controller. This data consists of a sequence of NMEA sentences from which GPGGA sentence is identified and processed.

The extraction of location values is done as follows. The first six bytes of the data received are compared with the pre-stored (\$GPGGA) string and if matched then only data is further accounted for; otherwise the process is repeated again. From the comma delimited GPGGA sentence, latitude and longitude positions are extracted by finding the respective comma positions and extracting the data. The latitude and longitude positions extracted are displayed on the LCD interfaced with AT89C51.

To obtain more details (other than latitude and longitude) from the GPS receiver, GPRMS sentence can be used.

The circuit connections are as follows:

Receiver1 (R₁) of MAX232 has been used for the serial communication. The receiver pin of GPS module is connected to R₁IN (pin13) of MAX232. R₁OUT (pin 12) of MAX232 is connected to RxD (P3.0) of AT89C51.

Pins 1-3 of port P1 (P1.0, P1.1 & P1.2 respectively) of AT89C51 are connected to the control pins (RS, R/W& EN) of LCD. The data pins of LCD are connected to Port P2 of the controller. The latitude and longitude positions are displayed on the LCD.

Code :

```
/* Basic program to show latitude and longitude on LCD extracted from GPGGA statement
*/

#include<reg51.h>
#define port2 P2
sbit rs = P1^0;
sbit rw = P1^1;
sbit e = P1^2;
char info[70];
char test[6]={"$GPGGA"};
char comma_position[15];
unsigned int check=0,i;
unsigned char a;
void receive_data();
void lcd_latitude();
void lcd_longitude();

//DELAY FUNCTION
void delay(unsigned int msec)
{
    int i,j ;
    for(i=0;i<msec;i++)
        for(j=0;j<1275;j++);
}
```

```

}

// LCD COMMAND SENDING FUNCTION
void lcd_cmd(unsigned char item)
{
    port2 = item;
    rs= 0;
    rw=0;
    e=1;
    delay(1);
    e=0;
    return;
}

// LCD DATA SENDING FUNCTION
void lcd_data(unsigned char item)
{
    port2 = item;
    rs= 1;
    rw=0;
    e=1;
    delay(1);
    e=0;
    return;
}

// LCD STRING SENDING FUNCTION
void lcd_string(unsigned char *str)
{
    int i=0;
    while(str[i]!='\0')
    {
        lcd_data(str[i]);
        i++;
        delay(10);
    }
    return;
}

// SERIAL PORT SETTING
void serial()
{
    TMOD=0x20;      //MODE=2
    TH1=0xfa;       // 4800 BAUD
    SCON=0x50 ;     // SERIAL MODE 1 ,8- BIT DATA ,1 STOP BIT ,1 START BIT ,
RECEIVING ON
    TR1=1;          //TIMER START
}

void find_comma()
{
    unsigned int i,count=0;
    for(i=0;i<70;i++)
    {
        if(info[i]==',')

```

```

        {
            comma_position[count++]=i;
        }
    }
}
void compare()
{
    IE=0x00;        //Interrupt disable
    find_comma();    //Function to detect position of comma in the string
    lcd_latitude();  //Function to show Latitude
    lcd_longitude(); //Function to show Longitude
    check=0;
    IE=0x90;        //Interrupt enable
}
void receive_data()           interrupt 4
{
    info[check++]=SBUF;        //Read SBUF
    if(check<7)                //Condition to check the required data
    {
        if(info[check-1]!=test[check-1])
            check=0;
    }
    RI=0;
}
void lcd_shape()              //Function to create shape of degree
{
    lcd_cmd(64);
    lcd_data(10);
    lcd_data(17);
    lcd_data(17);
    lcd_data(10);
    lcd_data(0);
    lcd_data(0);
    lcd_data(0);
    lcd_data(0);
}

void lcd_latitude()           //Function to display Latitude
{
    unsigned int c2=comma_position[1]; //Position of second comma
    lcd_shape();
    lcd_cmd(0x01);            // Clear LCD display
    lcd_cmd(0x84);            //Move cursor to position 6 of line 1
    lcd_string("LATITUDE");    //Showing Latitude
    lcd_cmd(0xC0);            //Beginning of second line
    lcd_data(info[c2+1]);
    lcd_data(info[c2+2]);
    lcd_data(0);              //Degree symbol
    lcd_data(info[c2+3]);
    lcd_data(info[c2+4]);
    lcd_data(info[c2+5]);
    lcd_data(info[c2+6]);
    lcd_data(info[c2+7]);
    lcd_data(info[c2+8]);
}

```

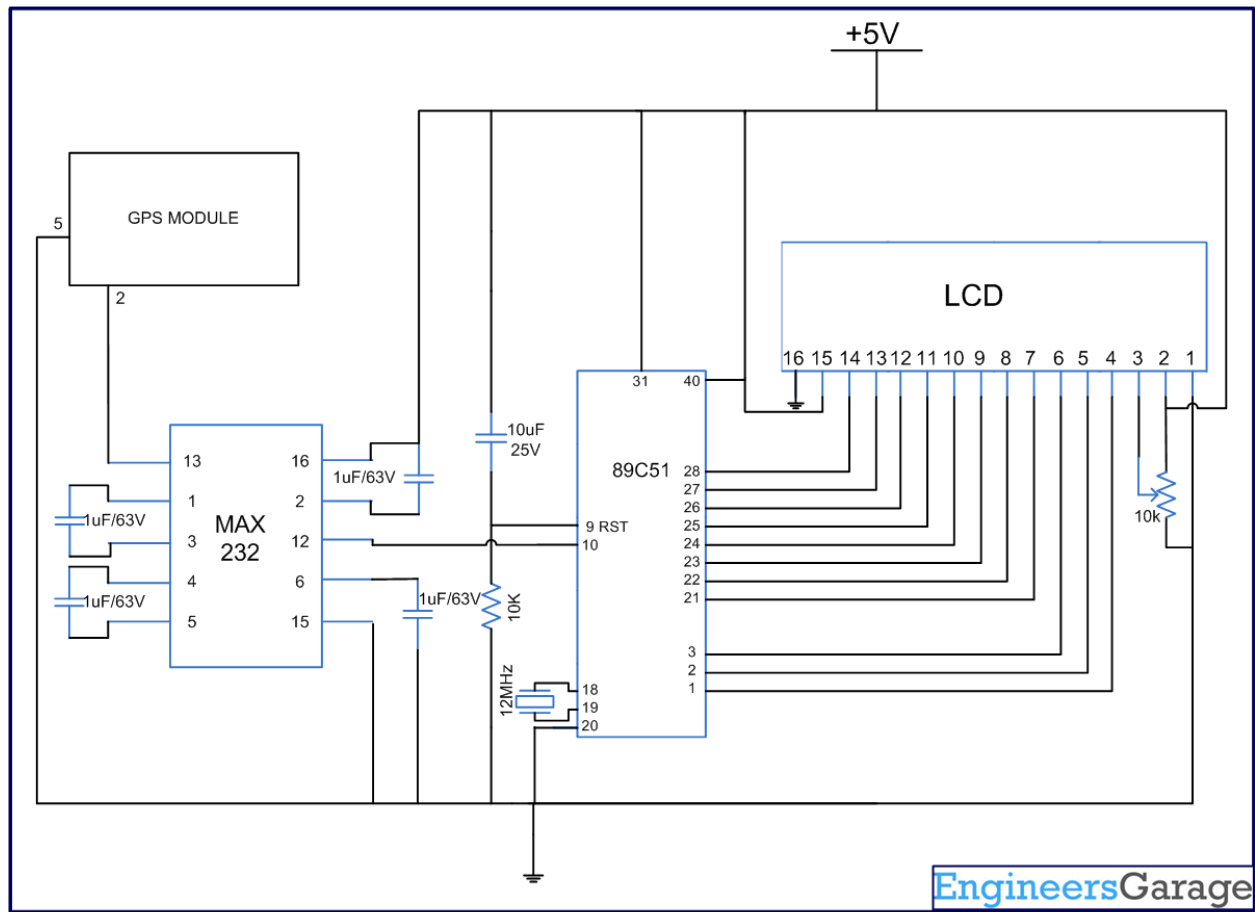
```

        lcd_data(info[c2+9]);
        lcd_data(0x27);           //ASCII of minute sign(')
        lcd_data(info[c2+10]);
        lcd_data(info[c2+11]);
        delay(250);
    }

void lcd_longitude()
{
    unsigned int c4=comma_position[3];
    lcd_cmd(0x01);                //Clear LCD display
    lcd_cmd(0x84);                //Move cursor to position 4 of line 1
    lcd_string("LONGITUDE");      //Showing Longitude
    lcd_cmd(0xC0);                //Beginning of second line
    lcd_data(info[c4+1]);
    lcd_data(info[c4+2]);
    lcd_data(info[c4+3]);
    lcd_data(0);
    lcd_data(info[c4+4]);
    lcd_data(info[c4+5]);
    lcd_data(info[c4+6]);
    lcd_data(info[c4+7]);
    lcd_data(info[c4+8]);
    lcd_data(info[c4+9]);
    lcd_data(info[c4+10]);
    lcd_data(0x27);                //ASCII of minute sign(')
    lcd_data(info[c4+11]);
    lcd_data(info[c4+12]);
    delay(250);
}

void main()
{
    serial();
    lcd_cmd(0x38);                //2 LINE, 5X7 MATRIX
    lcd_cmd(0x0e);                //DISPLAY ON, CURSOR BLINKING
    IE=0x90;
    while(1)
    {
        if(check==69)
            compare();
    }
}

```



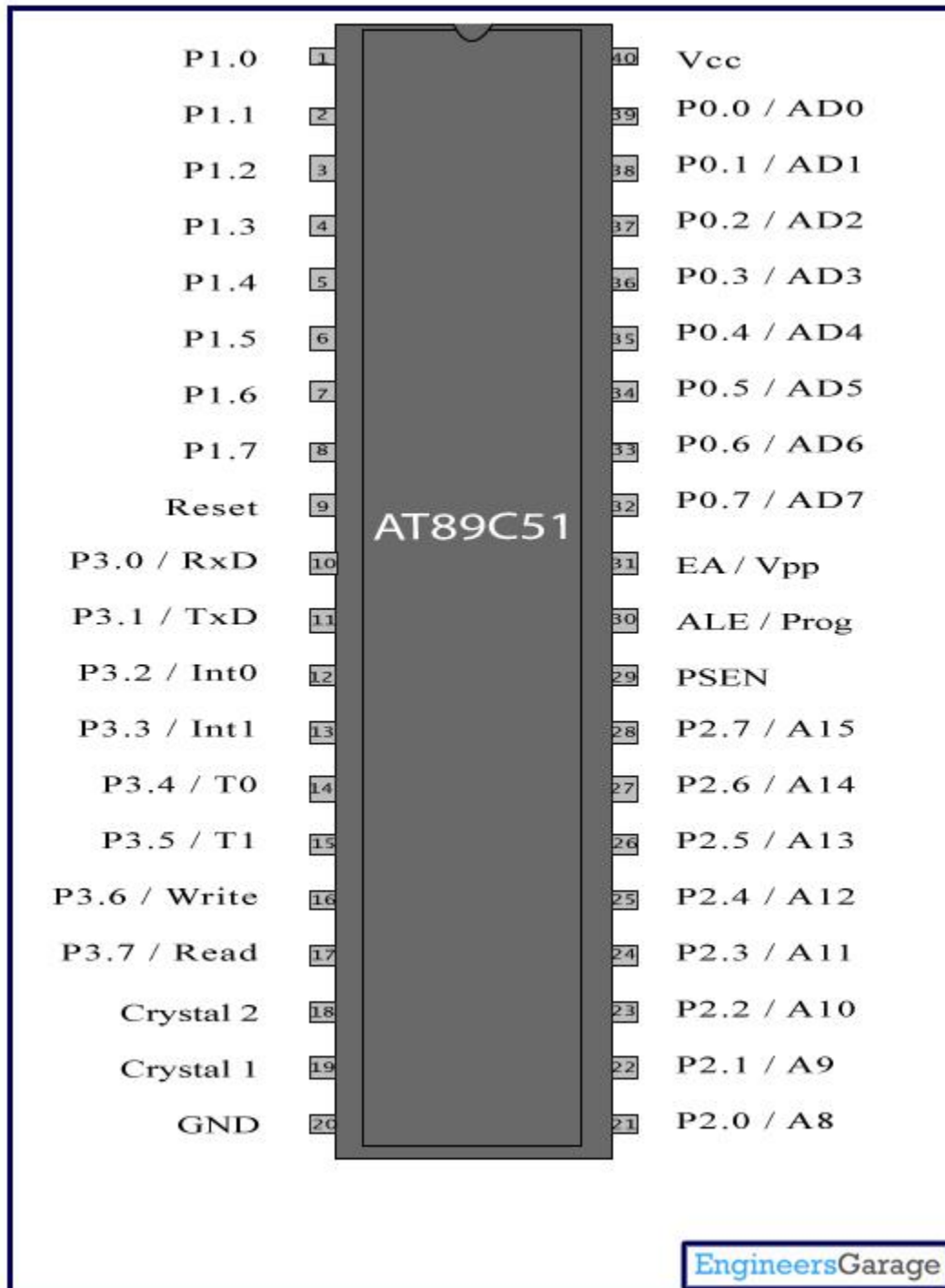
AT89C51 Microcontroller

AT89C51 is an 8-bit [microcontroller](#) and belongs to Atmel's [8051 family](#). **ATMEL 89C51** has 4KB of Flash programmable and erasable read only memory (PEROM) and 128 bytes of RAM. It can be erased and program to a maximum of 1000 times.

In 40 pin AT89C51, there are four ports designated as P_1 , P_2 , P_3 and P_0 . All these ports are 8-bit bi-directional ports, *i.e.*, they can be used as both input and output ports. Except P_0 which needs external pull-ups, rest of the ports have internal pull-ups. When 1s are written to these port pins, they are pulled high by the internal pull-ups and can be used as inputs. These ports are also bit addressable and so their bits can also be accessed individually.

Port P_0 and P_2 are also used to provide low byte and high byte addresses, respectively, when connected to an external memory. Port 3 has multiplexed pins for special functions like [serial communication](#), hardware interrupts, timer inputs and read/write operation from external memory. AT89C51 has an inbuilt UART for serial communication. It can be programmed to operate at different baud rates. Including two [timers](#) & hardware [interrupts](#), it has a total of six interrupts.

Pin Diagram:



Pin Description:

Pin No	Function			Name
1	8 bit input/output port (P ₁) pins			P _{1.0}
2				P _{1.1}
3				P _{1.2}
4				P _{1.3}
5				P _{1.4}
6				P _{1.5}
7				P _{1.6}
8				P _{1.7}
9	Reset pin; Active high			Reset
10	Input (receiver) for serial communication	RxD	8 bit input/output port (P ₃) pins	P _{3.0}
11	Output (transmitter) for serial communication	TxD		P _{3.1}
12	External interrupt 1	Int0		P _{3.2}
13	External interrupt 2	Int1		P _{3.3}
14	Timer1 external input	T ₀		P _{3.4}
15	Timer2 external input	T ₁		P _{3.5}
16	Write to external data memory	Write		P _{3.6}
17	Read from external data memory	Read		P _{3.7}
18	Quartz crystal oscillator (up to 24 MHz)			Crystal 2
19				Crystal 1
20	Ground (0V)			Ground
21	8 bit input/output port (P ₂) pins / High-order address bits when interfacing with external memory			P _{2.0} / A ₈
22				P _{2.1} / A ₉
23				P _{2.2} / A ₁₀
24				P _{2.3} / A ₁₁
25				P _{2.4} / A ₁₂
26				P _{2.5} / A ₁₃
27				P _{2.6} / A ₁₄
28				P _{2.7} / A ₁₅
29	Program store enable; Read from external program memory			PSEN
30	Address Latch Enable			ALE
	Program pulse input during Flash programming			Prog
31	External Access Enable; Vcc for internal program executions			EA
	Programming enable voltage; 12V (during Flash programming)			Vpp
32	8 bit input/output port (P ₀) pins Low-order address bits when interfacing with external memory			P _{0.7} / AD ₇
33				P _{0.6} / AD ₆
34				P _{0.5} / AD ₅
35				P _{0.4} / AD ₄
36				P _{0.3} / AD ₃
37				P _{0.2} / AD ₂
38				P _{0.1} / AD ₁
39				P _{0.0} / AD ₀
40	Supply voltage; 5V (up to 6.6V)			Vcc

MAX232

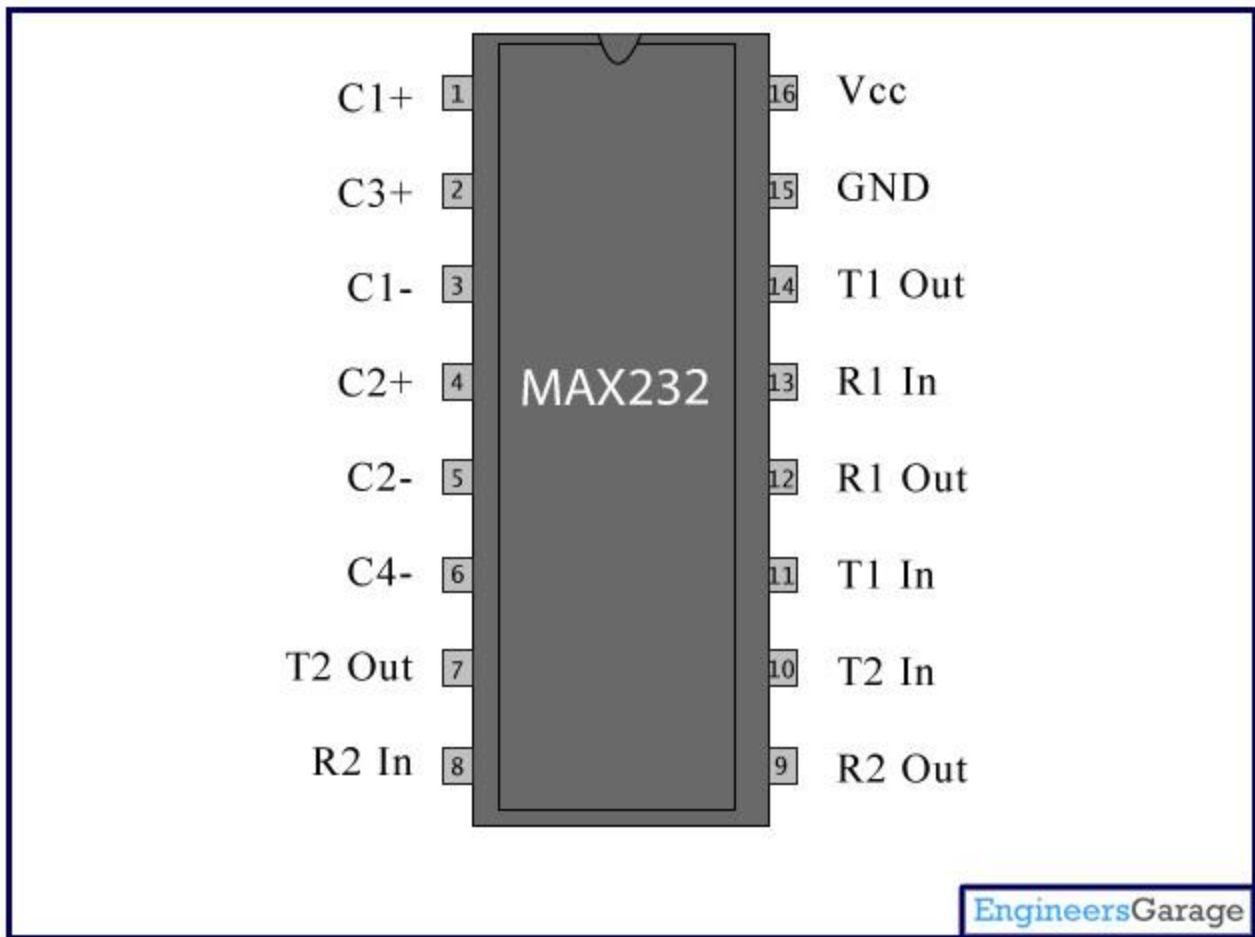
The MAX232 IC is used to convert the TTL/CMOS logic levels to RS232 logic levels during serial communication of microcontrollers with PC. The controller operates at TTL logic level (0-5V) whereas the serial communication in PC works on RS232 standards (-25 V to + 25V). This makes it difficult to establish a direct link between them to communicate with each other.

The intermediate link is provided through MAX232. It is a dual driver/receiver that includes a capacitive voltage generator to supply RS232 voltage levels from a single 5V supply. Each receiver converts RS232 inputs to 5V TTL/CMOS levels. These receivers (R_1 & R_2) can accept $\pm 30V$ inputs. The drivers (T_1 & T_2), also called transmitters, convert the TTL/CMOS input level into RS232 level.

The transmitters take input from controller's serial transmission pin and send the output to RS232's receiver. The receivers, on the other hand, take input from transmission pin of RS232 serial port and give serial output to microcontroller's receiver pin. MAX232 needs four external capacitors whose value ranges from $1\mu F$ to $22\mu F$.

Microcontroller	MAX232		RS232
Tx	$T_{1/2}$ In	$T_{1/2}$ Out	Rx
Rx	$R_{1/2}$ Out	$R_{1/2}$ In	Tx

Pin Diagram:



Pin Description:

Pin No	Function	Name
1	Capacitor connection pins	Capacitor 1 +
2		Capacitor 3 +
3		Capacitor 1 -
4		Capacitor 2 +
5		Capacitor 2 -
6		Capacitor 4 -
7	Output pin; outputs the serially transmitted data at RS232 logic level; connected to receiver pin of PC serial port	T ₂ Out
8	Input pin; receives serially transmitted data at RS 232 logic level; connected to transmitter pin of PC serial port	R ₂ In
9	Output pin; outputs the serially transmitted data at TTL logic level; connected to receiver pin of controller.	R ₂ Out
10	Input pins; receive the serial data at TTL logic level; connected to serial transmitter pin of controller.	T ₂ In
11		T ₁ In
12	Output pin; outputs the serially transmitted data at TTL logic level; connected to receiver pin of controller.	R ₁ Out
13	Input pin; receives serially transmitted data at RS 232 logic level; connected to transmitter pin of PC serial port	R ₁ In
14	Output pin; outputs the serially transmitted data at RS232 logic level; connected to receiver pin of PC serial port	T ₁ Out
15	Ground (0V)	Ground
16	Supply voltage; 5V (4.5V – 5.5V)	Vcc

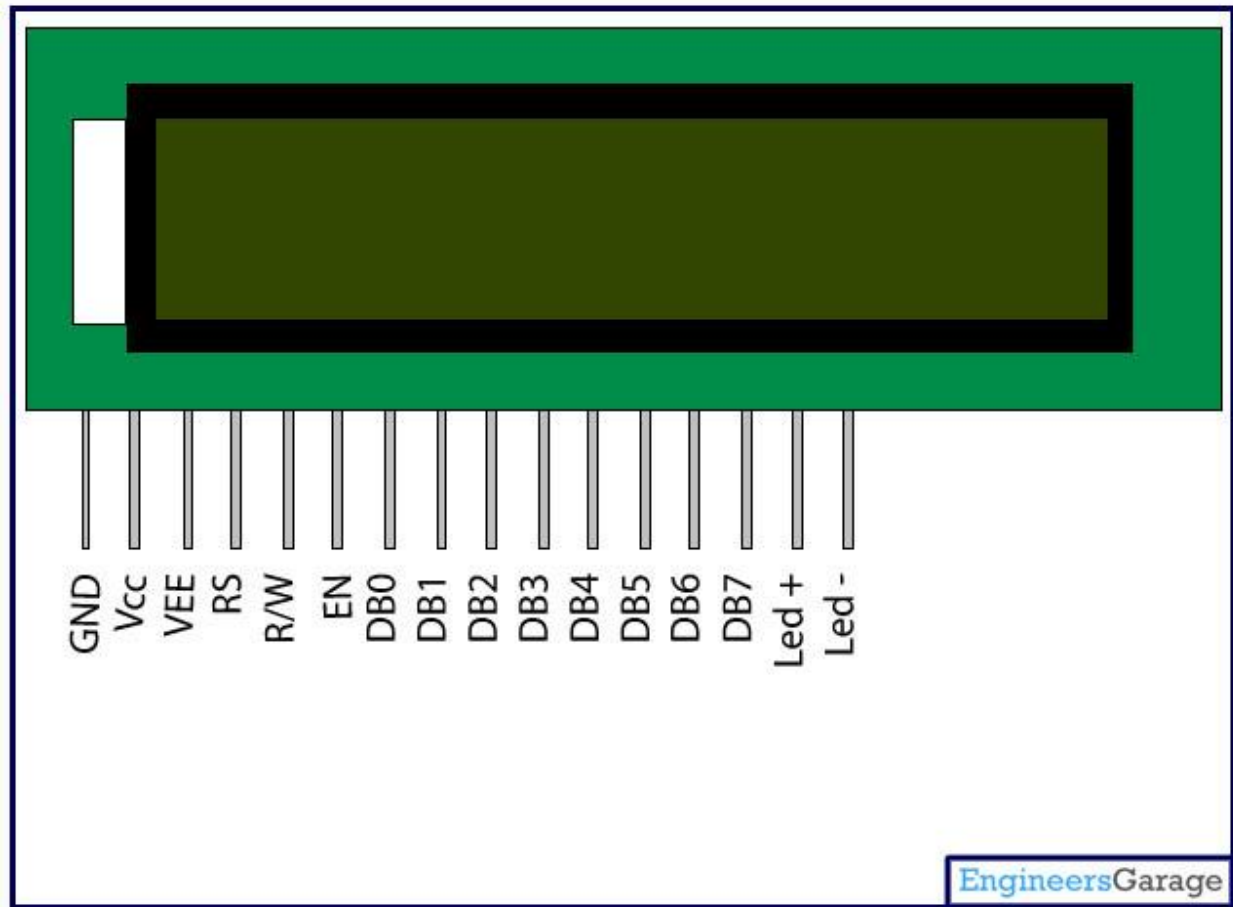
LCD

LCD (Liquid Crystal Display) screen is an electronic display module and find a wide range of applications. A 16x2 LCD display is very basic module and is very commonly used in various devices and circuits. These modules are preferred over [seven segments](#) and other multi segment [LEDs](#). The reasons being: LCDs are economical; easily programmable; have no limitation of displaying special & even [custom characters](#) (unlike in seven segments), [animations](#) and so on.

A **16x2 LCD** means it can display 16 characters per line and there are 2 such lines. In this LCD each character is displayed in 5x7 pixel matrix. This LCD has two registers, namely, Command and Data.

The command register stores the command instructions given to the LCD. A command is an instruction given to LCD to do a predefined task like initializing it, clearing its screen, setting the cursor position, controlling display etc. The data register stores the data to be displayed on the LCD. The data is the ASCII value of the character to be displayed on the LCD. Click to learn more about internal structure of a [LCD](#).

Pin Diagram:



Pin Description:

Pin No	Function	Name
1	Ground (0V)	Ground
2	Supply voltage; 5V (4.7V – 5.3V)	Vcc
3	Contrast adjustment; through a variable resistor	V _{EE}
4	Selects command register when low; and data register when high	Register Select
5	Low to write to the register; High to read from the register	Read/write
6	Sends data to data pins when a high to low pulse is given	Enable
7	8-bit data pins	DB0
8		DB1
9		DB2
10		DB3
11		DB4
12		DB5
13		DB6
14		DB7
15	Backlight V _{CC} (5V)	Led+
16	Backlight Ground (0V)	Led-

