CON1

VTARGET	1	2	VCC1
TRST	3	4	GND
TDI	5	6	GND
TMS/SWDIO	7	8	GND
TCK/SWCLK	9	10	GND
RTCK	11	12	GND
TDO/SWO	13	14	GND
RESET	15	16	GND
	17	18	GND
	19	20	GND

ARM JTAG

CON2

VTARGET	1	2	VCC1
TRST	3	4	GND
TDI	5	6	GND
TMS/SWDIO	7	8	GND
TCK/SWCLK	9	10	GND
RTCK	11	12	GND
TDO/SWO	13	14	GND
RESET	15	16	GND
	17	18	GND
	19	20	GND

ARM JTAG

CON3

VTARGET	01	02	TMS/SWDIO
GND	03	04	TCK/SWCLK
GND	05	06	TDO/SWO
ISP_CTRL	07	08	TDI
GND	09	10	RESET
	11	12	
	13	14	
GND	15	16	
GND	17	18	
GND	19	20	

Cortex Debug+ETM

CON4

VTARGET	01	02	TMS/SWDIO
GND	03	04	TCK/SWCLK
GND	05	06	TDO/SWO
ISP_CTRL	07	08	TDI
GND	09	10	RESET

Cortex Debug

CON5

VTARGET	01	02	TMS/SWDIO
	03	04	TCK/SWCLK
	05	06	TDO/SWO
ISP_CTRL	07	08	TDI
GND	09	10	RESET

SB-Link

JP1

VTARGET
VCC1

Target
Power

JP2

1	SB_RXD
2	SB_TXD
3	GND

ISP UART

Steckerbelegungen:
https://www.keil.com/support/man/docs/jlink/jLink_connectors.htm

v1.0 Initial Version
v1.1 ISP UART Header added