

ISLAMIC UNIVERSITY OF TECHNOLOGY (IUT)

ORGANISATION OF ISLAMIC COOPERATION (OIC)

Department of Computer Science and Engineering (CSE)

MID SEMESTER EXAMINATION

SUMMER SEMESTER, 2022-2023

DURATION: 1 HOUR 30 MINUTES

FULL MARKS: 75

CSE 4205: Digital Logic Design

Programmable calculators are not allowed. Do not write anything on the question paper.

Answer all **3 (three)** questions. Figures in the right margin indicate full marks of questions with corresponding COs and POs in parentheses.

1.
 - $F_1(a, c, b, d) = M(5, 6, 9, 11, 12, 14) \cdot D(0, 1, 2, 4, 8)$
 - $F_2(a, c, b, d, e) = m(0, 1, 2, 5, 7, 9, 11, 19, 21, 22, 23, 25, 29) + d(4, 10, 13, 17, 24, 30, 31)$

- a) Simplify the above mentioned Boolean expressions into POS format using K-map.

6 + 9
(CO1)
(PO1)

- b) Draw the logic diagrams of the simplified expressions from Question 1.a.

5
(CO1)
(PO1)

- c) Find the compliments of the above-mentioned Boolean expressions.

5
(CO1)
(PO1)

2. a) Analyze the logic circuit shown in Figure 1 and explain its operation.

6.5
(CO3)
(PO1)

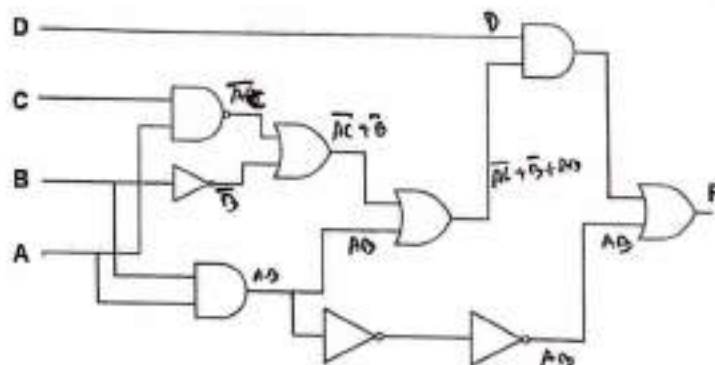


Figure 1: A logic circuit for Question 2.a

- b) Can the same functionality be achieved using lesser amount of gates? Justify your answer.

6.5
(CO₂)
(PO₂)

- c) Design a combinational circuit following proper steps that takes two 4-bit BCD numbers, X and Y , as input and performs BCD addition.

12
(CO5)
(PO1)

3. a) Simplify the following boolean expressions into both SOP and POS formats applying algebraic methods and draw the logic diagrams. Mention corresponding postulates and theorems for each step. 7 + 7
(CO1)
(PO1)
- i. $\overline{A(\overline{CB} + BD) + \overline{AB}}$
- ii. $\overline{CC} + (DB + \overline{BC})B$
- b) With proper explanation design a combinational circuit that takes two 4-bit binary numbers, A and B , and can perform both addition and subtraction. 7
(CO5)
(PO1)
- c) Differentiate between: 2 + 2
(CO3)
(PO1)
- i. Canonical form and Standard form of Boolean function
- ii. Minterm and Maxterm