

Yizuo Chen

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[LinkedIn](#) — [GitHub](#) — [Portfolio](#)

Profile

Fourth-year Electrical Engineering student at BCIT with strong academic performance and hands-on experience in circuit design, embedded/FPGA systems, and signal processing.

Research interests: *Embedded and FPGA systems, micro and nano electronic devices, computer vision, control and automation, advanced sensors, electronic instruments and equipment for aerospace applications, signal processing, and analog/digital communications.*

Capstone Project (In Progress)

BCIT School of Energy, in collaboration with **UBC Faculty of Medicine – Division of Neurology** and **BC Children’s Hospital**

Supervisors: Prof. Julie Robillard, Prof. Craig Hennessey

Developing and integrating advanced features into social robots (Nao Robot) to support research on pediatric anxiety management and pain relief applications.

Education

B.Eng., Electrical Engineering, British Columbia Institute of Technology (2022–2026)

GPA (Cumulative): 89% (4.00/4.00); GPA (Senior Years): 87%(4.00/4.00)

Selected relevant coursework: Signal Processing and Filters (95%); Electromagnetic Fundamentals (94%); Advanced Circuit Analysis (82%); Real-Time Embedded Systems (in progress); Digital Image and Video Processing (in progress)

Experience

Junior Control Engineer (Co-op), TKT Innovations Ltd., Langley, BC (May–Aug 2025)

- Optimized Siemens TIA Portal PLC logic for industrial dishwashing systems, improving timing/temperature control and reducing handling errors.
- Standardized control code architecture to align with industrial best practices and regulatory requirements.

Electrical Engineering Intern (Co-op), Canature Processing, Langley, BC (Jun–Sep 2023)

- Diagnosed and resolved production-line logic issues, reducing material waste and improving throughput.
- Executed test plans and authored QA documentation for certification readiness.

Visiting Student, AMPEL (UBC), Vancouver, BC (Jun–Sep 2022)

- Produced Fusion 360 models and 3D-printed components to support graduate-level experimental setups; contributed to laboratory workflow improvements.

Selected Projects

NFC Attendance System on RPi Pico 2W + FreeRTOS (In Progress) — [GitHub](#)

Built an NFC-based attendance system using a PN532 reader to detect and validate student UIDs for BCIT junior-level ECE lectures (for Prof. Eduardo Casas). Rewrote and improved the original Raspberry Pi PN532 driver to support Raspberry Pi Pico 2W under FreeRTOS. Implemented real-time UID logging with LED/buzzer feedback, onboard flash storage, and data offload to a laptop via USB connection. System supports wireless deployment, timing pulses for real-time scheduling, and Wi-Fi/Bluetooth attendance display.

Face Recognition Software (MATLAB – Digital Image & Video Processing Design Project) (In Progress)

Developing a face-recognition system in MATLAB using face detection, feature extraction, and PCA eigenface classification. Implemented image filtering and histogram equalization to improve recognition accuracy under varied lighting. Final system will support real-time recognition with confidence-level output.

Ultrasonic Radar (HC-SR04) on DE0-Nano-SoC — [GitHub](#)

Built an ultrasonic distance-sensing “radar” using HC-SR04 and Altera DE0-Nano-SoC (Cyclone V). Wrote SystemVerilog for sensor timing, servo PWM, and UART; developed a Python visualization for real-time display. Supports automated sweep with thresholds and manual control via joystick/encoder; achieved stable, accurate range reporting.

Industrial Dishwasher PLC Optimization

Enhanced Siemens PLC logic in TIA Portal for a pet-food processing line, tightening timing/temperature control and reducing plate handling faults; improved end-of-line efficiency and reliability.

Remote-Control Pi Car (CV + Targeting)

Designed a custom PCB (motor driver, LEDs, I/O) and enclosure; programmed in C++/OpenCV (tested in Visual Studio, deployed on Raspberry Pi with Code::Blocks). Implemented manual keyboard control via server–client and autonomous mode for target detection and engagement.

Automated Ball-Sorting Machine (C++/OpenCV) — [GitHub](#)

Built the mechanical structure and control software to detect and sort colored balls in queue. Added automated, push-button manual, and server–client remote modes; integrated computer vision with servo control for accurate sorting.

Portable DC Power Supply

Designed enclosure in SolidWorks and PCB in KiCad (fabricated and printed by BCIT). Assembled/soldered components and validated adjustable -25–25 V DC output.

Competitions & Activities

BCIT Engineering Competition – Senior Design (Upcoming)

Selected to compete in the Senior Design category, serving as a design engineer on a multi-disciplinary team to develop and present an engineering solution for a real world engineering problem.

Technical Skills

Programming & Tools: C, C++, Python, MATLAB/Simulink, SystemVerilog, OpenCV, Intel Quartus Prime, VS Code, VS Studio, ModelSim, LabVIEW, Siemens TIA Portal, AutoCAD, SolidWorks, Git, Linux, LaTeX

Embedded & Interfaces: TI MCUs, Raspberry Pi, RPi Pico 2W, Arduino, UART/SPI/I2C, firmware development, troubleshooting & debugging

Hardware & PCB: Schematic capture, KiCad PCB layout, soldering & assembly, 3D printing (G-code)

Controls & DAQ: PLC programming (TIA Portal), automated system design, real-time data acquisition

Math & Analysis: Signal processing, Linear Algebra, Multivariable Calculus, Transform Methods, Probability & Statistics for Engineers

Test & Measurement: Oscilloscope, DMM, Logic Analyzer, Spectrum Analyzer

Documentation: Assembly drawings, user manuals, line diagrams, manufacturing documentation, technical reports

Soft Skills

- Work independently and in collaborative, multidisciplinary teams
- Strong problem-solving and analytical thinking abilities
- Adaptability and quick learning in fast-paced environments