

# **Design Notes (Consolidated)**

## **Team 6:**

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Design reviewed by individuals from two teams:

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### Board Layout

- Silk screen overlap throughout board.
- Silk screen is off the board in top left corner.
- Chunk of trace in DRC.
- Team name and date needs to be on board.
- Ground stitching vias are not present.
- ERC Warnings (Components that don't need values).
  - Ext. connectors and LEDs.
- Traces might need to be rounded out.
- Via 'fencing' or putting via's close to important grounds may be advantageous

### Schematic

- Pin 22 not grounded.
- AVCC and AREF are floating.
- User-Button not a very meaningful name.
- Labeling is done very well.
- Bypass capacitors lacking on VCC and likely 5V regulator.