

Design notes

Board Layout

- silk screen overlap throughout board
- silk screen is off of the board in top left corner
- chunk of trace in DRC (just a warning, won't break anything)
- team name and date needs to be on board
- ground stitching vias are not present
- you guys have a bunch of ERC warnings (components that don't need values, such as external connectors and LEDs, need an inherent value associated) (just a warning, won't break anything)

Schematic

- pin 22 is not grounded
- AVCC and AREF are floating, double check that this is okay. (it should probably have a pullup or pulldown cap)
- specify function of "user-button" not the most meaningful name
- labeling is very well done