

ATmega328P							
<u>1</u>	PC6/PCINT14/ $\overline{\text{RESET}}$	PC5/ADC5/SCL/PCINT13	<u>28</u>	PBx = Port B, bit # x		PCINTx = Pin Change Interrupt Request # x	
<u>2</u>	PD0/PCINT16/RXD	PC4/ADC4/SDA/PCINT12	<u>27</u>	PCx = Port C, bit # x		$\overline{\text{RESET}}$ = Reset (active low)	
<u>3</u>	PD1/PCINT17/TXD	PC3/ADC3/PCINT11	<u>26</u>	PDx = Port D, bit # x		RXD = USART Receive (input)	
<u>4</u>	PD2/PCINT18/INT0	PC2/ADC2/PCINT10	<u>25</u>	-----		SCK = SPI Bus Master Clock Input	
<u>5</u>	PD3/PCINT19/OC2B/INT1	PC1/ADC1/PCINT9	<u>24</u>	ADCx = Analog to Digital Converter channel x		SCL = I2C Serial Clock	
<u>6</u>	PD4/PCINT20/XCK/T0	PC0/ADC0/PCINT8	<u>23</u>	AIN0 = Analog Comparator Input - Positive		SDA = I2C Serial Data	
<u>7</u>	VCC	GND	<u>22</u>	AIN1 = Analog Comparator Input - Negative		$\overline{\text{SS}}$ = SPI Slave Select (active low)	
<u>8</u>	GND	AREF	<u>21</u>	AREF = Analog Reference		T0 = Timer/Counter 0 External Counter Input	
<u>9</u>	PB6/PCINT6/XTAL1/TOSC1	AVCC	<u>20</u>	AVCC = Supply Voltage for the A/D Converter		T1 = Timer/Counter 1 External Counter Input	
<u>10</u>	PB7/PCINT7/XTAL2/TOSC2	PB5/SCK/PCINT5	<u>19</u>	CLK0 = Divided Clock Output		TOSC1 = Timer Oscillator pin 1	
<u>11</u>	PD5/PCINT21/OC0B/T1	PB4/MISO/PCINT4	<u>18</u>	GND = Ground		TOSC2 = Timer Oscillator pin 2	
<u>12</u>	PD6/PCINT22/OC0A/AIN0	PB3/MOSI/OC2A/PCINT3	<u>17</u>	ICP1 = Input Capture for Timer/Counter 1		TXD = USART Transmit (input)	
<u>13</u>	PD7/PCINT23/AIN1	PB2/ $\overline{\text{SS}}$ /OC1B/PCINT2	<u>16</u>	INT0 = External Interrupt Request 0		VCC = Digital Supply Voltage	
<u>14</u>	PB0/PCINT0/CLKO/ICP1	PB1/OC1A/PCINT1	<u>15</u>	INT1 = External Interrupt Request 1		XCK = USART External Clock Input/Output	
				MISO = SPI Master In / Slave Out		XTAL1 = Crystal Oscillator pin 1	
				MOSI = SPI Master Out / Slave In		XTAL2 = Crystal Oscillator pin 2	
				OC0A = Output Compare Timer/Counter 0 Match A Output			
				OC0B = Output Compare Timer/Counter 0 Match B Output			
				OC1A = Output Compare Timer/Counter 1 Match A Output			
				OC1B = Output Compare Timer/Counter 1 Match B Output			
				OC2A = Output Compare Timer/Counter 2 Match A Output			
				OC2B = Output Compare Timer/Counter 2 Match B Output			