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| NTERFACE PIN FUNCTION | | |
|-----------------------|-----------------|--|
| PIN NO. | SYMBOL | FUNCTION |
| 1 | V _{SS} | Ground |
| 2 | V _{DD} | Supply voltage for logic |
| 3 | V ₀ | Operating voltage for LCD |
| 4 | RS | H: Data/L: Instruction code |
| 5 | R/W | H: Read (MPU \rightarrow Module)/L: Write (MPU \rightarrow Module) |
| 6 | E | H → L chip enable signal |
| 7 | DB0 | Data bus line |
| 8 | DB1 | Data bus line |
| 9 | DB2 | Data bus line |
| 10 | DB3 | Data bus line |
| 11 | DB4 | Data bus line |
| 12 | DB5 | Data bus line |
| 13 | DB6 | Data bus line |
| 14 | DB7 | Data bus line |
| 15 | A | Supply power for LED+ |
| 16 | R | Supply power for Red- |
| 17 | G | Supply power for Green- |
| 18 | В | Supply power for Blue- |

