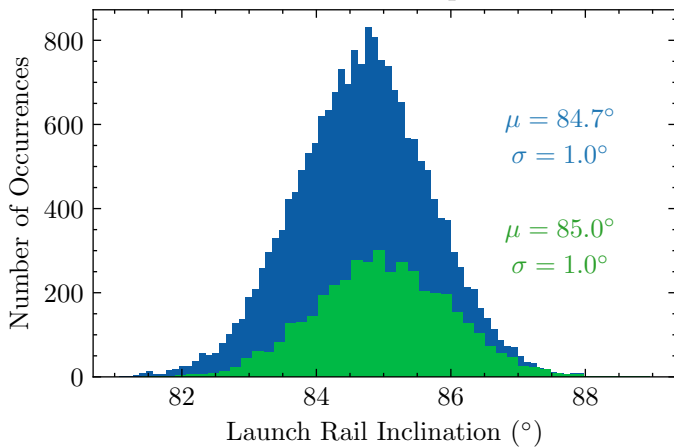


Simulation Inputs



Simulation Outputs

