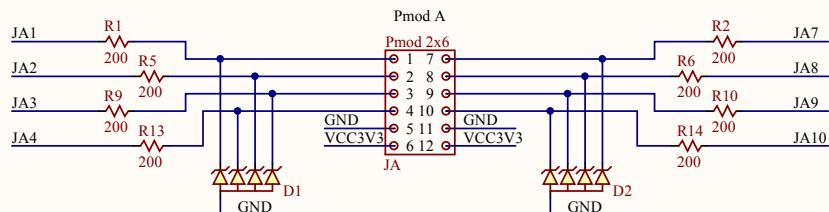
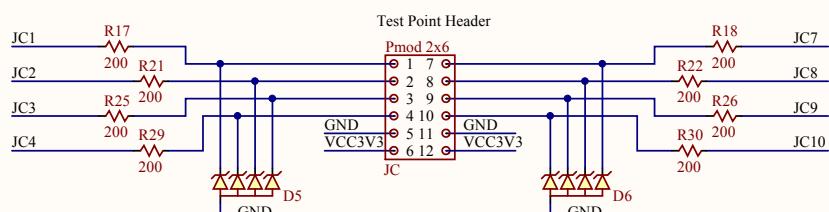


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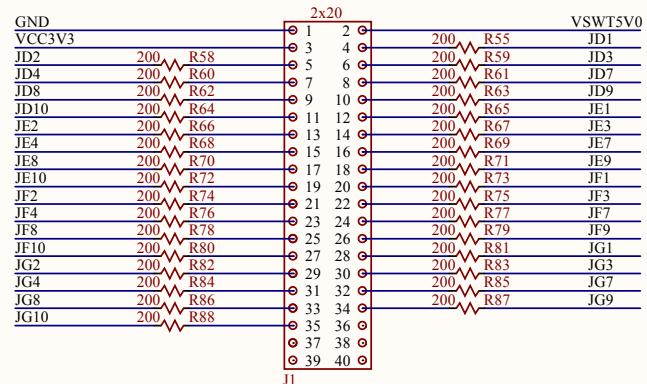
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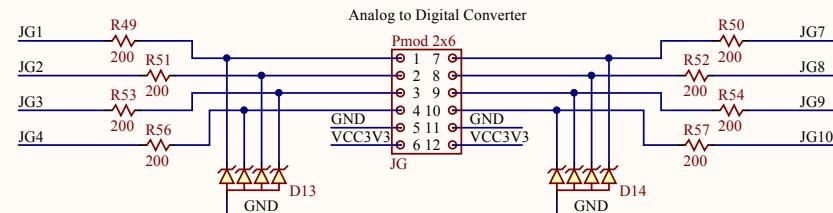
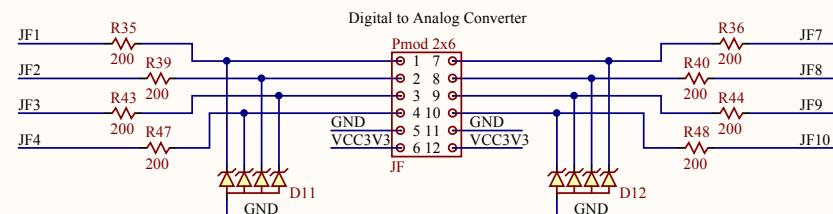
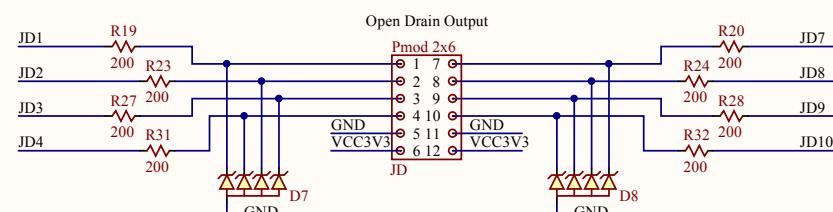
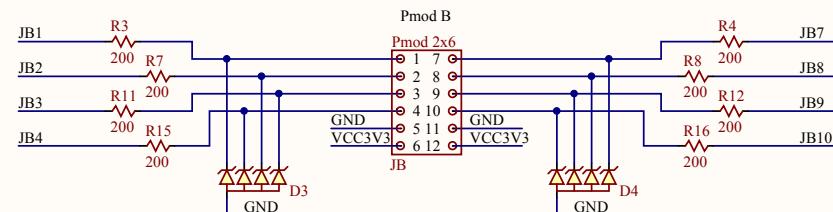
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Title		Enlightenment	Rev
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Foot	F2	Foot	DIGILENT®
Foot	F3	Foot	BEYOND THEORY
Foot	F4	Foot	
Foot	F5	Foot	
Foot	F6	Foot	
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Engineer	EG		
Author	GMA		
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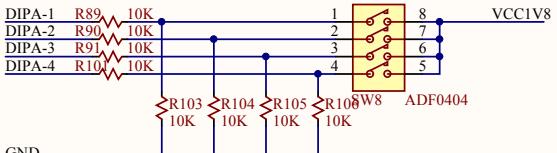
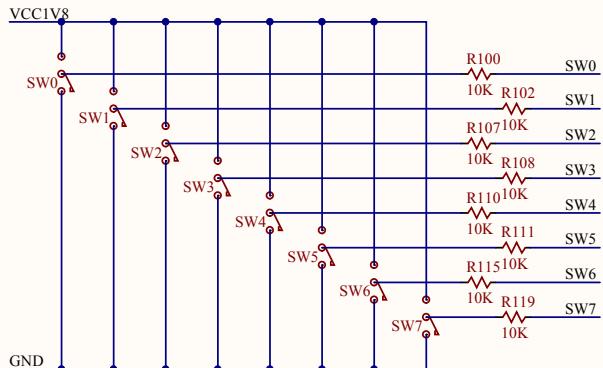
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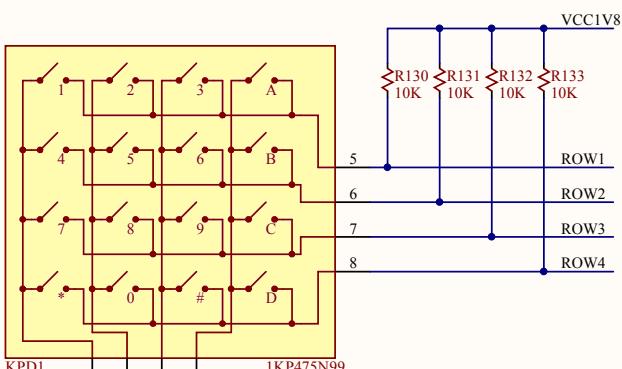
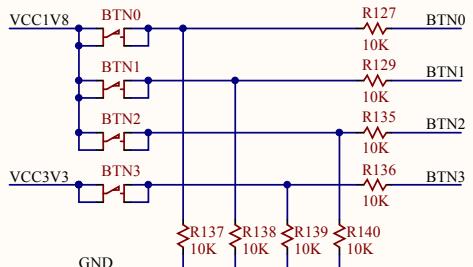
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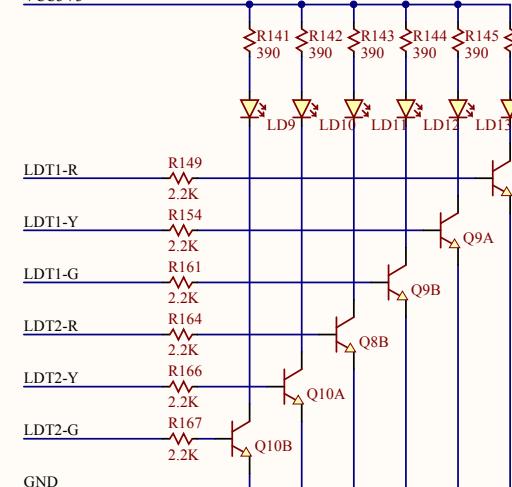
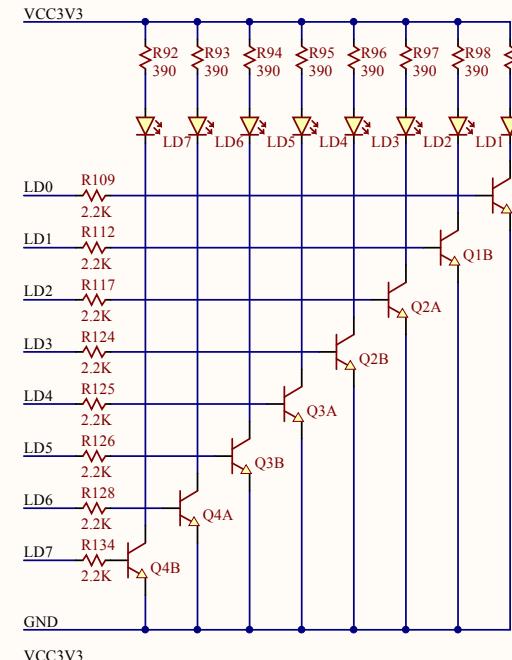
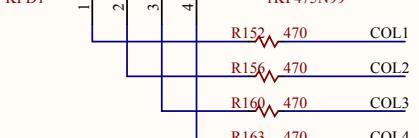
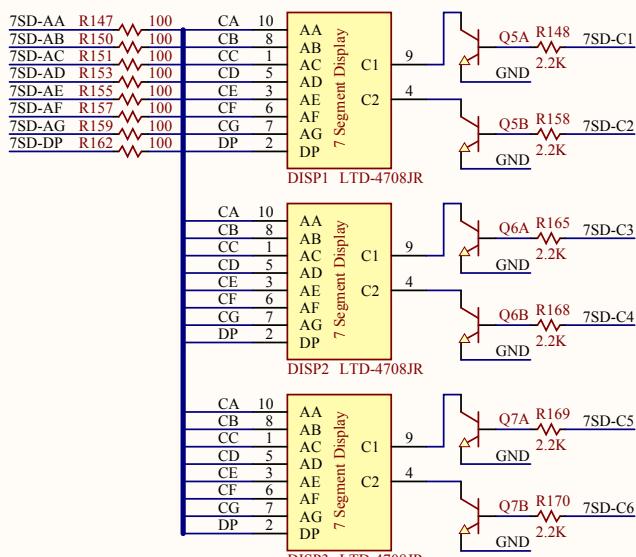
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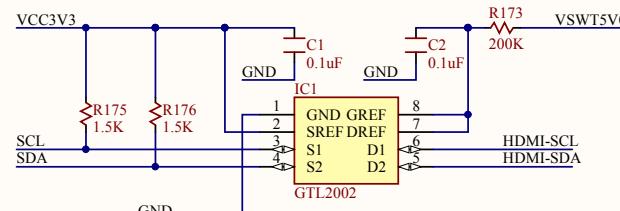
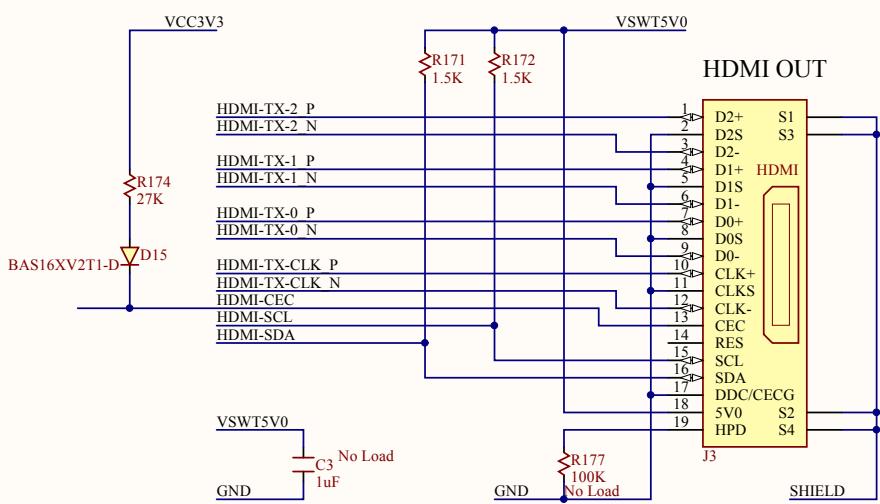
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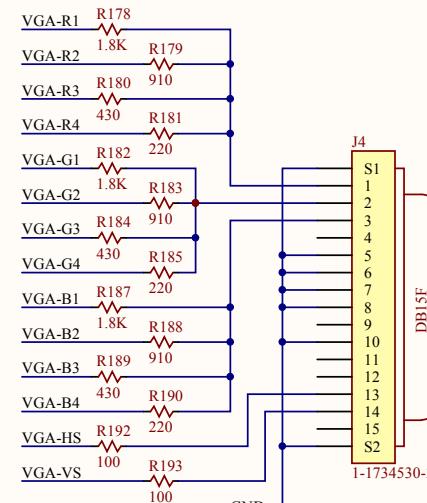
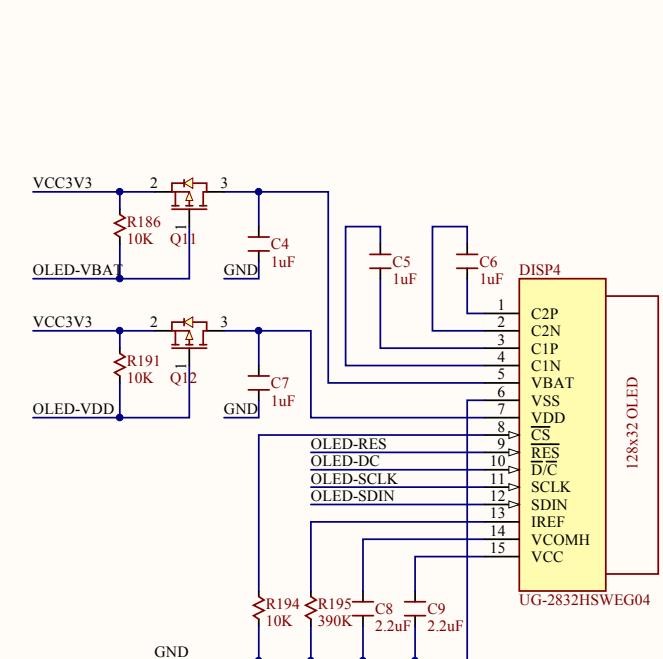
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This is a bidirectional buffer we have used before. Okay?

B

A



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Engineer EG

Author GMA

Date 6/20/2012

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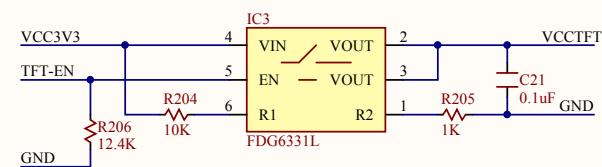
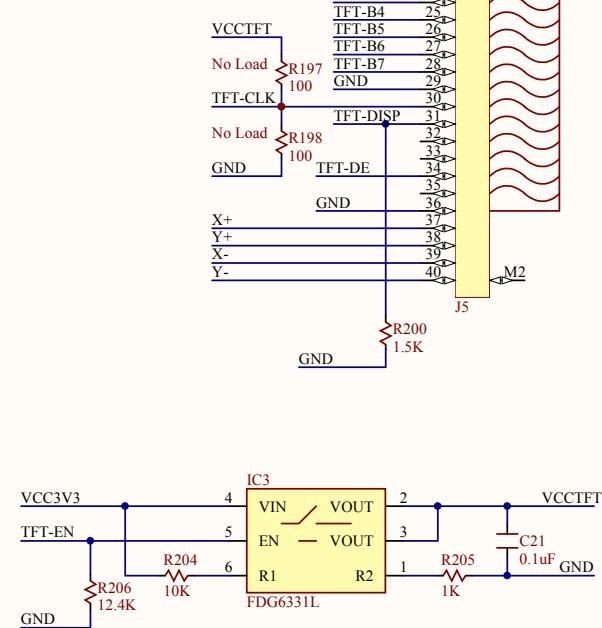
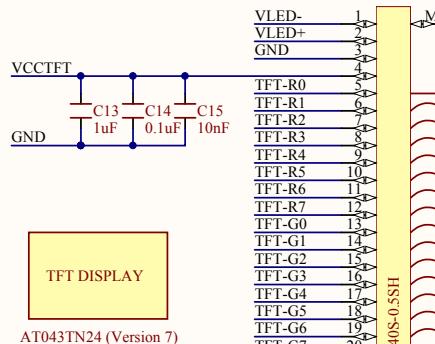
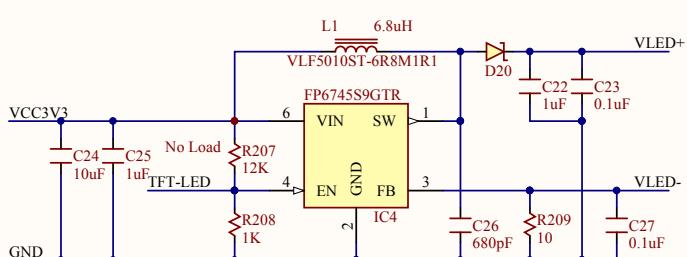
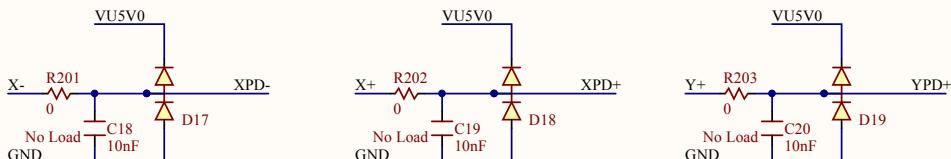
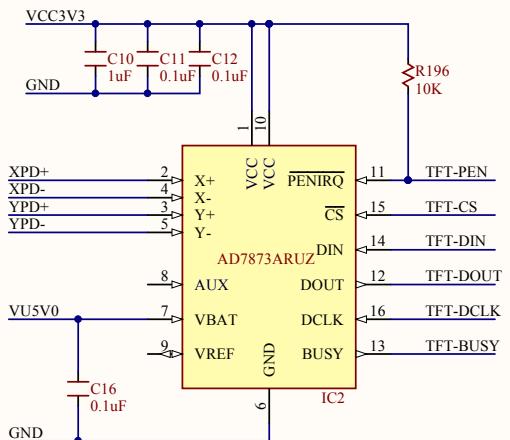
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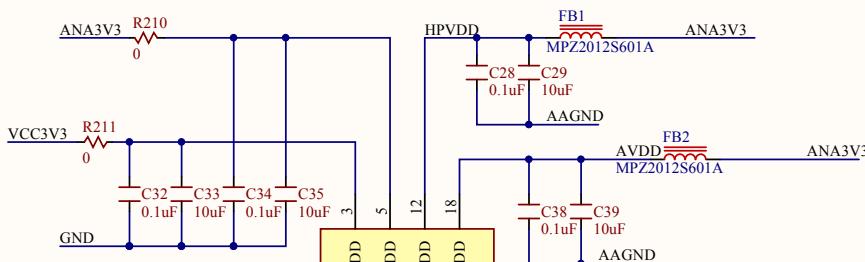
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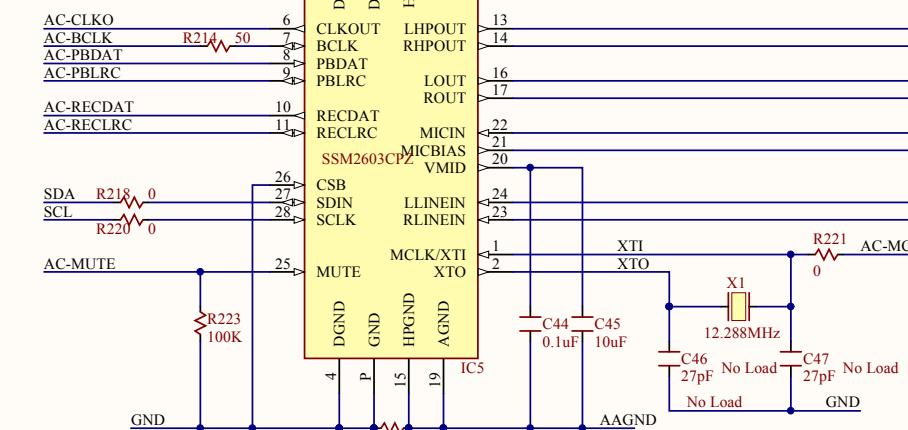
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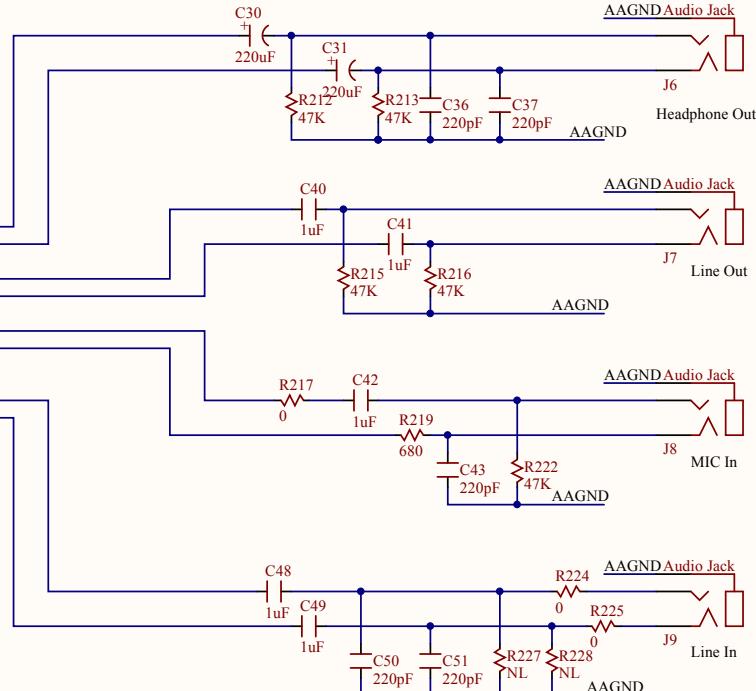
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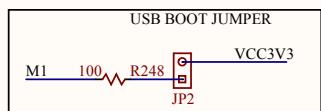
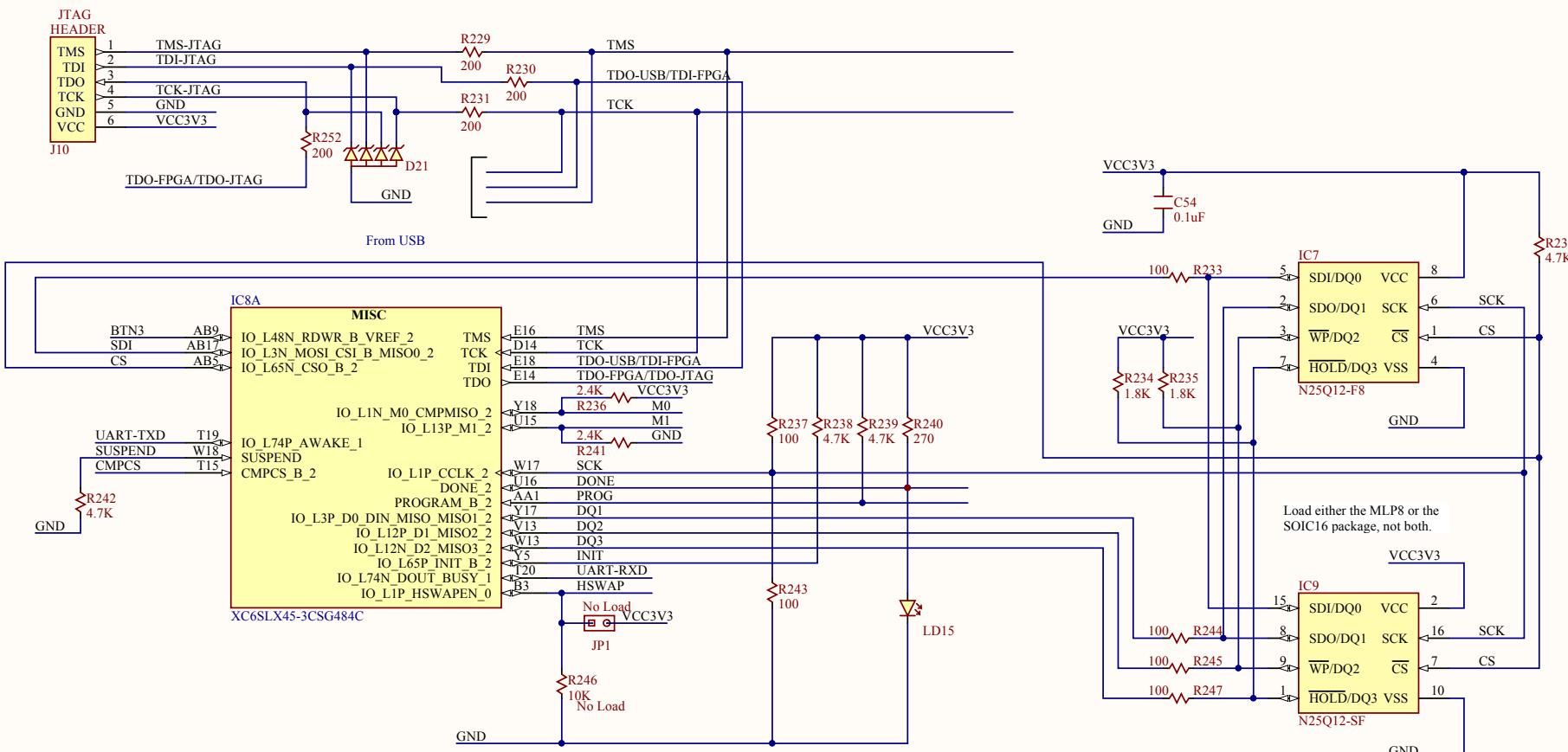


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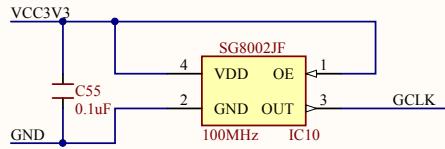
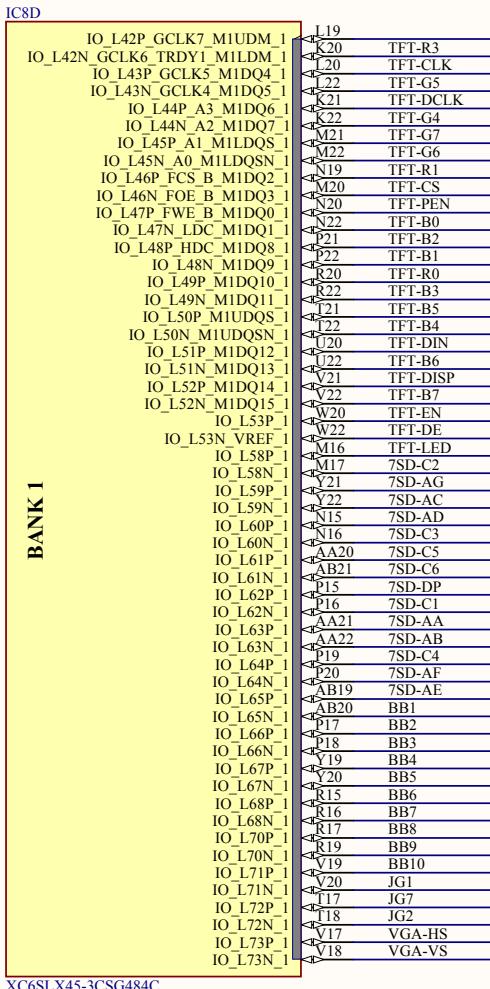
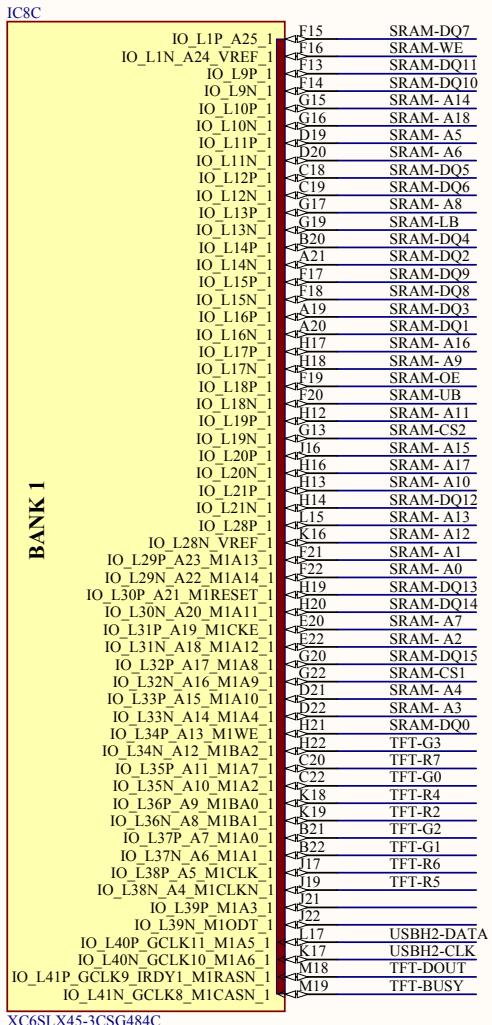
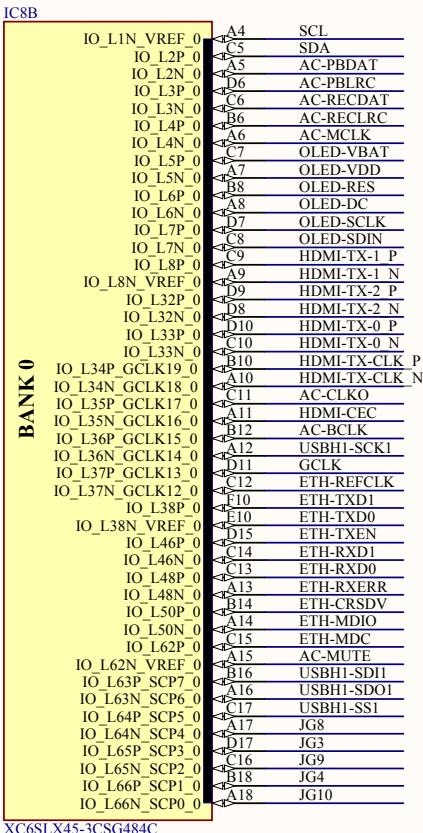




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Circuit Configuration	
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Author	GMA
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DIGILENT BEYOND THEORY



IC8E

IO_L2P_CMPCLK_2	AA18	JA1
IO_L2N_CMPPMOSL_2	AB18	JA7
IO_L4P_2	AA16	JA2
IO_L4N_VREF_2	AB16	JA8
IO_L5P_2	Y15	JA3
IO_L5N_2	AB15	JA9
IO_L13N_D10_2	V15	JA4
IO_L14P_D11_2	W15	JA10
IO_L14N_D12_2	Y16	JB1
IO_L15P_2	AA14	JB7
IO_L15N_2	AB14	JB2
IO_L16P_2	W14	JB8
IO_L16N_VREF_2	Y14	JB3
IO_L20P_2	T14	JB9
IO_L20N_2	U14	JB4
IO_L29P_GCLK3_2	W11	JB10
IO_L29N_GCLK2_2	Y10	JC1
IO_L30P_GCLK1_D13_2	AA12	JC7
IO_L30N_GCLK0_USERCCLE_2	AB12	JC2
IO_L31P_GCLK31_D14_2	Y11	JC8
IO_L31N_GCLK30_D15_2	AB11	JC3
IO_L32P_GCLK29_2	AA10	JC9
IO_L32N_GCLK28_2	AB10	JC4
IO_L41P_2	Y13	JC10
IO_L41N_VREF_2	AB13	JD1
IO_L42P_2	W12	JD7
IO_L42N_2	Y12	JD2
IO_L43P_2	R11	JD8
IO_L43N_2	U11	JD3
IO_L44P_2	IO1	JD9
IO_L44N_2	W10	JD4
IO_L45P_2	T10	JD10
IO_L45N_2	U10	JE1
IO_L46P_2	Y9	JE7
IO_L46N_2	V9	JE2
IO_L47P_2	W9	JE8
IO_L47N_2	Y8	JE3
IO_L48P_D7_2	Y9	JE9
IO_L49P_D3_2	AA8	JE4
IO_L49N_D4_2	AB8	JE10
IO_L50P_2	V7	JF1
IO_L50N_2	W8	JF7
IO_L62P_D5_2	Y6	JF8
IO_L62N_D6_2	V7	JF3
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IO_L63N_2	AA6	JF4
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IC8F

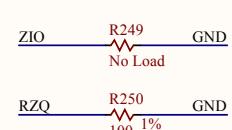
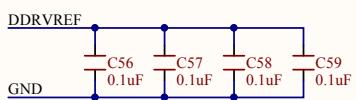
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IO_L7N_3	Y4	LD1
IO_L8P_3	V3	LD3
IO_L8N_3	AB3	LD6
IO_L9P_3	W3	LD0
IO_L9N_3	W1	LDS
IO_L10P_3	U8	LDT1-R
IO_L10N_3	T7	LDT1-G
IO_L11P_3	F8	LDT2-R
IO_L11N_3	R7	LDT2-G
IO_L12P_3	AA4	LD7
IO_L12N_3	AB4	LD4
IO_L13P_3	U6	LDT2-Y
IO_L13N_3	W5	SW0
IO_L18P_3	U4	SW1
IO_L18N_3	V3	SW2
IO_L21P_3	P4	SW3
IO_L21N_3	R4	SW4
IO_L22P_3	P6	SW5
IO_L22N_3	P5	SW6
IO_L23P_3	P8	SW7
IO_L23N_3	P7	VGA-B4
IO_L24P_3	N7	VGA-B3
IO_L24N_3	N6	VGA-G4
IO_L25P_3	M8	VGA-B1
IO_L25N_3	M7	VGA-B2
IO_L26P_3	T4	VGA-G1
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IO_L32N_M3DQ15_3	V1	DDR-DQ15
IO_L33P_M3DQ12_3	U3	DDR-DQ12
IO_L33N_M3DQ13_3	U1	DDR-DQ13
IO_L34P_M3UDQSN_3	T2	DDR-UDQS_P
IO_L34N_M3UDQSN_3	R3	DDR-DQ10
IO_L35P_M3DQ10_3	R1	DDR-DQ11
IO_L35N_M3DQ11_3	P2	DDR-DQ8
IO_L36P_M3DQ8_3	P1	DDR-DQ9
IO_L36N_M3DQ9_3	N3	DDR-DQ0
IO_L37P_M3DQ0_3	N1	DDR-DQ1
IO_L37N_M3DQ1_3	M2	DDR-DQ2
IO_L38P_M3DQ2_3	M1	DDR-DQ3
IO_L38N_M3DQ3_3	L3	DDR-LDQS_P
IO_L39P_M3LDQSN_3	L1	DDR-LDQS_N
IO_L39N_M3LDQSN_3	K2	DDR-DQ6
IO_L40P_M3DQ6_3	K1	DDR-DQ7

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IC8G

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IO_L41N_GCLK26_M3DQ5_3	I1	DDR-DQ5
IO_L42P_GCLK25_TRDY2_M3UDM_3	H2	DDR-UDM
IO_L42N_GCLK24_M3LDM_3	H1	DDR-LDM
IO_L43P_GCLK23_M3RASN_3	N4	DDR-RAS
IO_L43N_GCLK22_IRDY2_M3CASN_3	P3	DDR-CAS
IO_L44P_GCLK21_M3A5_3	G3	DDR-A5
IO_L44N_GCLK20_M3A6_3	G1	DDR-A6
IO_L45P_M3A3_3	M4	DDR-A3
IO_L45N_M3ODT_3	M3	DDR-ODT
IO_L46P_M3CLK_3	F2	DDR-CK_P
IO_L46N_M3CLKN_3	F1	DDR-CK_N
IO_L47P_M3A0_3	M5	DDR-A0
IO_L47N_M3A1_3	L4	DDR-A1
IO_L48P_M3BA0_3	E3	DDR-BA0
IO_L48N_M3BA1_3	E1	DDR-BA1
IO_L49P_M3A7_3	K4	DDR-A7
IO_L49N_M3A2_3	K3	DDR-A2
IO_L50P_M3WE_3	D2	DDR-WE
IO_L50N_M3BA2_3	D1	DDR-BA2
IO_L51P_M3A10_3	K6	DDR-A10
IO_L51N_M3A4_3	K5	DDR-A4
IO_L52P_M3A8_3	C3	DDR-A8
IO_L52N_M3A9_3	C1	DDR-A9
IO_L53P_M3CKE_3	H6	DDR-CKE
IO_L53N_M3A12_3	J4	DDR-A12
IO_L54P_M3RESET_3	B2	VGA-R2
IO_L54N_M3A11_3	B1	DDR-A11
IO_L55P_M3A13_3	H4	VGA-R4
IO_L55N_M3A14_3	H3	VGA-R3
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IO_L57N_VREF_3	H8	COL1
IO_L58P_3	J7	COL2
IO_L58N_3	K8	COL3
IO_L59P_3	K7	COL4
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IO_L60P_3	F3	ROW2
IO_L60N_3	G8	ROW3
IO_L73P_3	G7	ROW4
IO_L73N_3	G6	DIPA-1
IO_L74P_3	G4	DIPA-2
IO_L74N_3	F5	DIPA-3
IO_L75P_3	E5	DIPA-4
IO_L75N_3	F8	DIPB-1
IO_L80P_3	F7	DIPB-2
IO_L80N_3	C4	DIPB-3
IO_L81P_3	D3	DIPB-4
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IO_L82N_3	A3	BTN2
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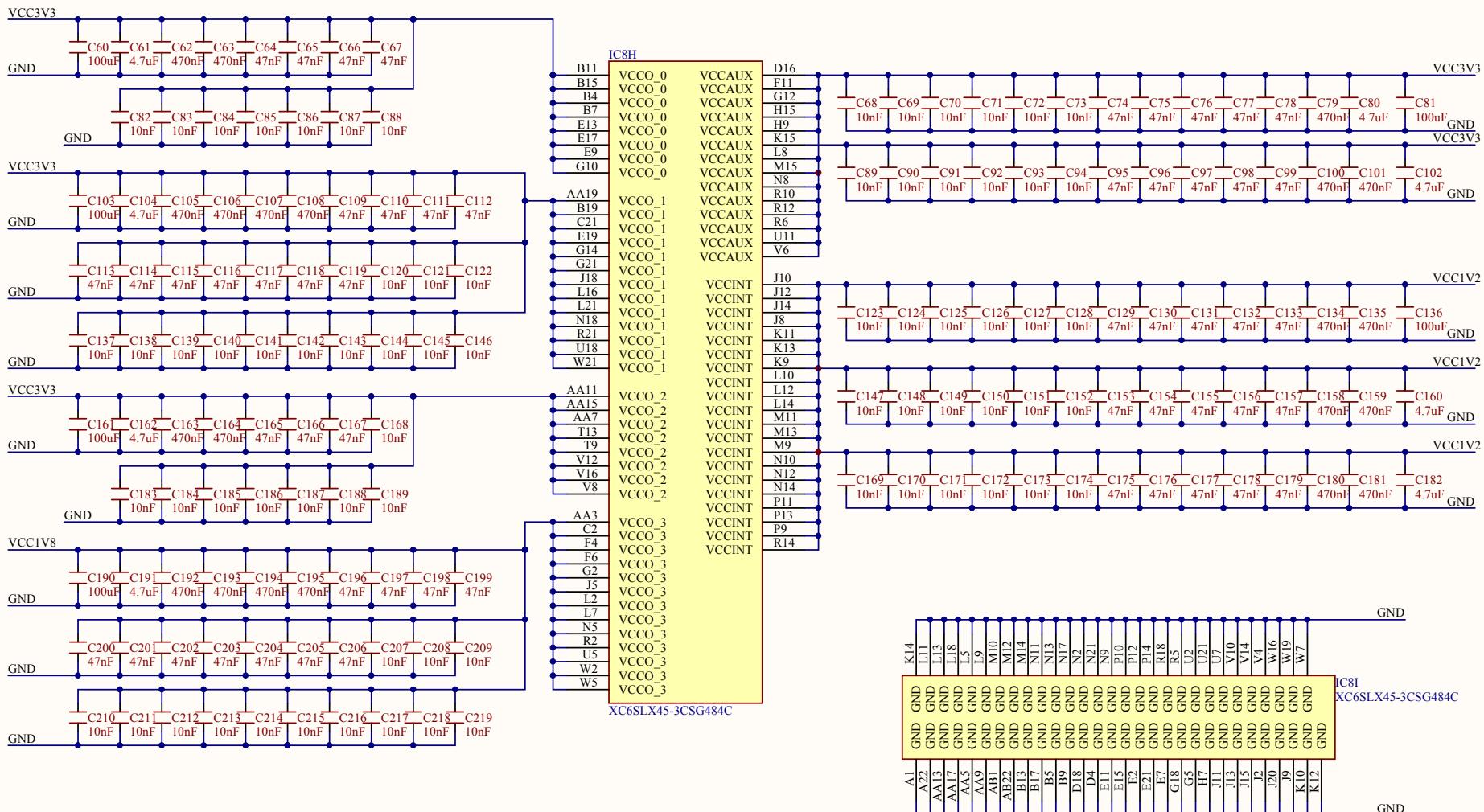
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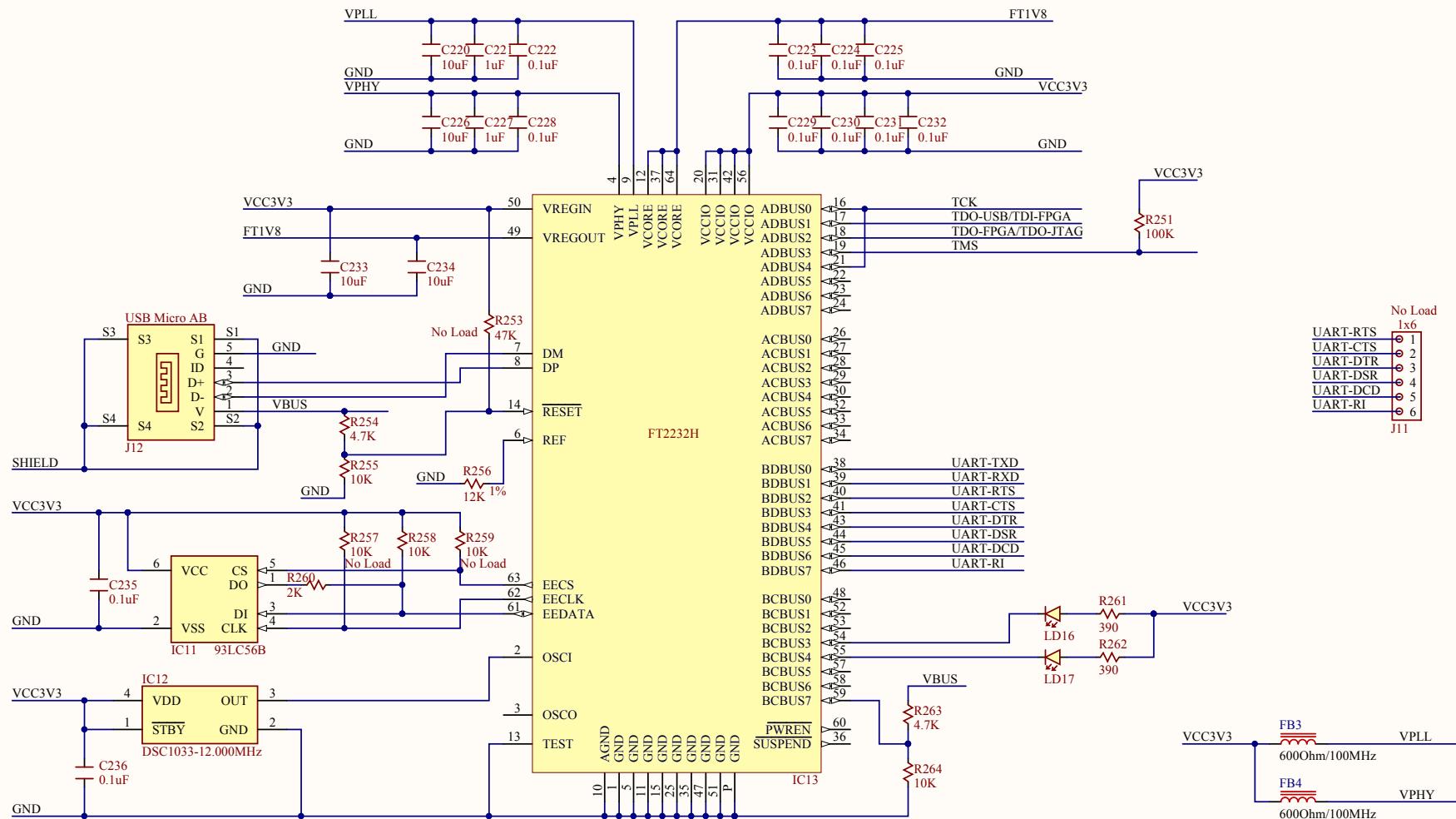


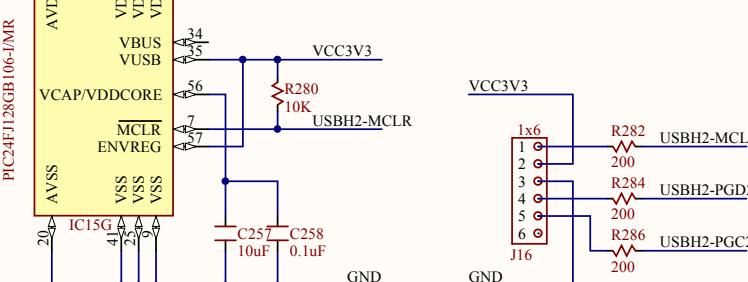
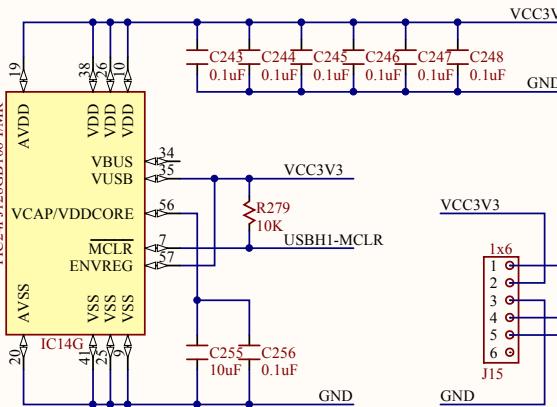
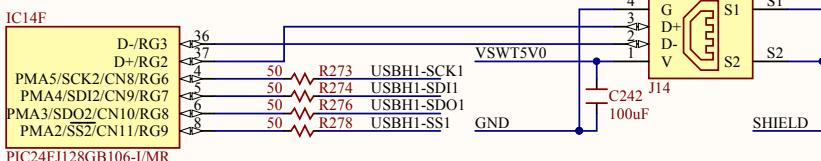
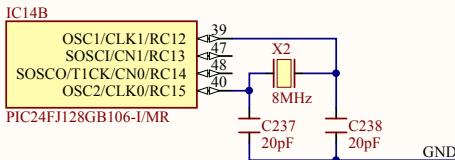
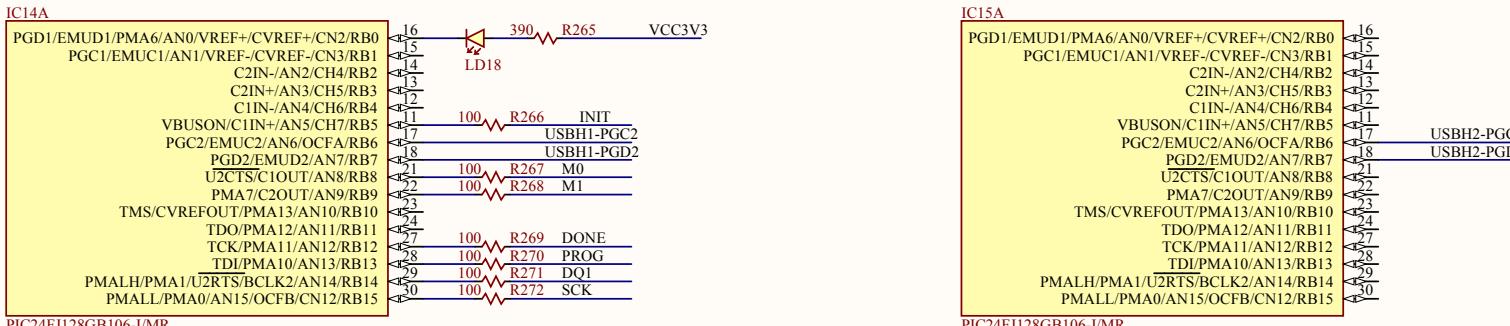
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Author	GMA
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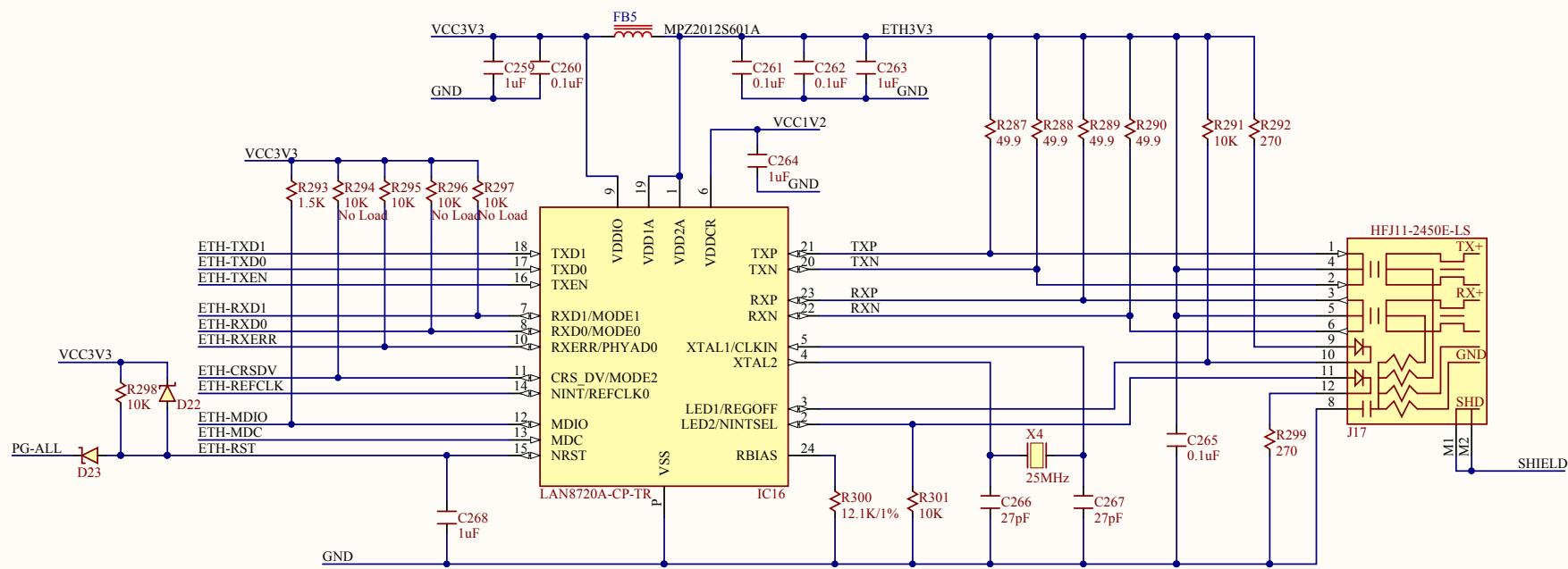
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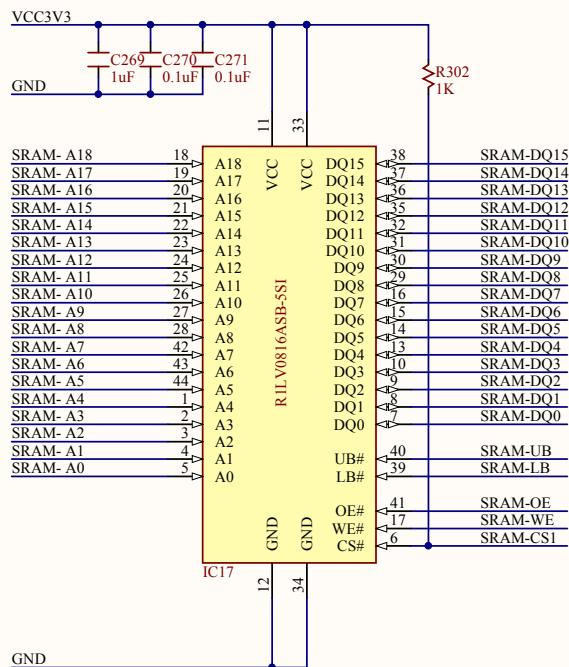
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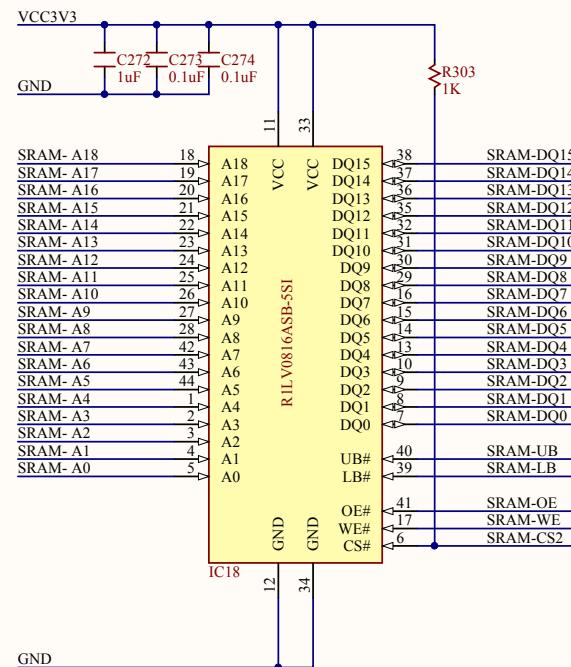
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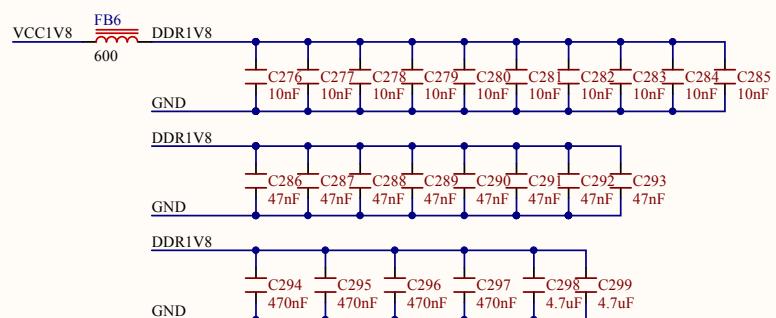
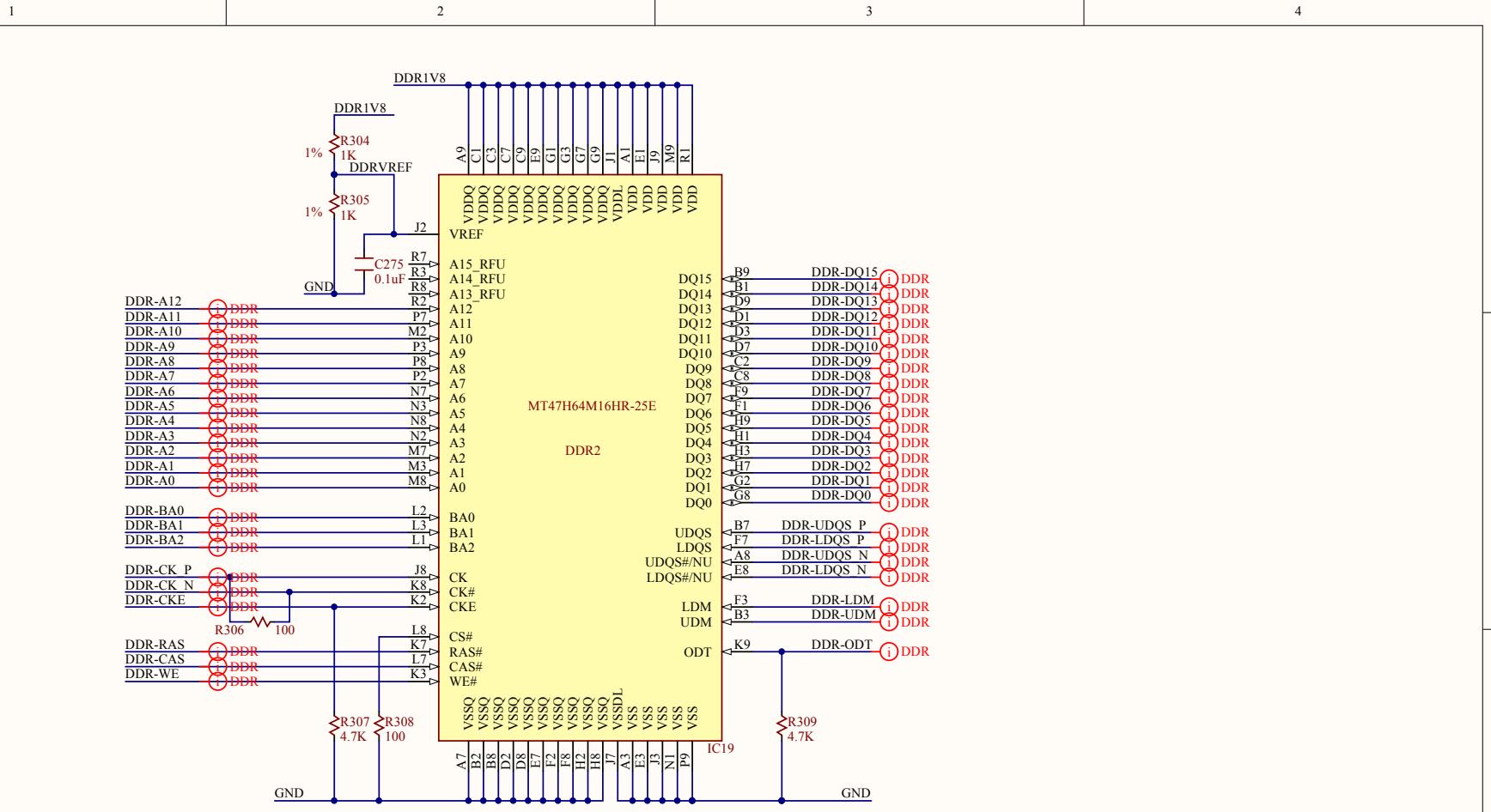
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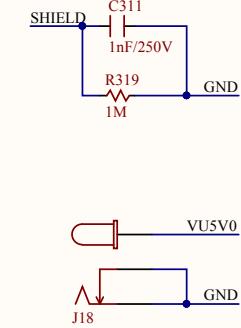
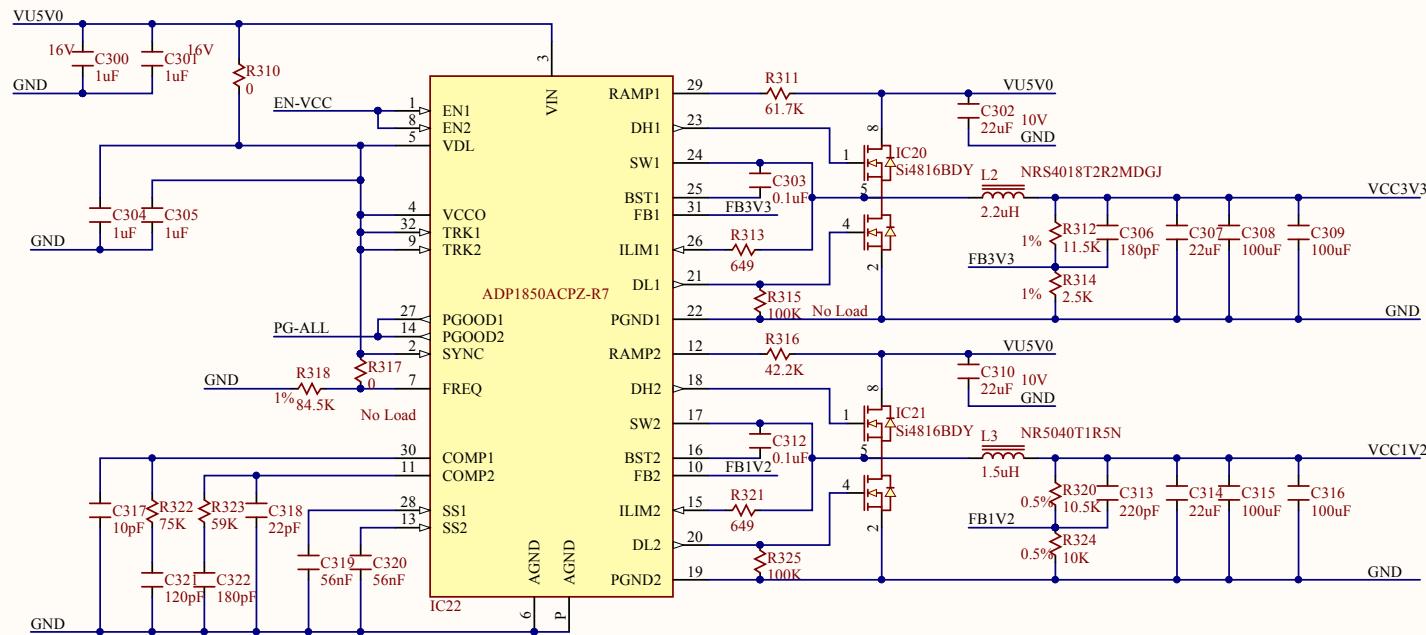


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