

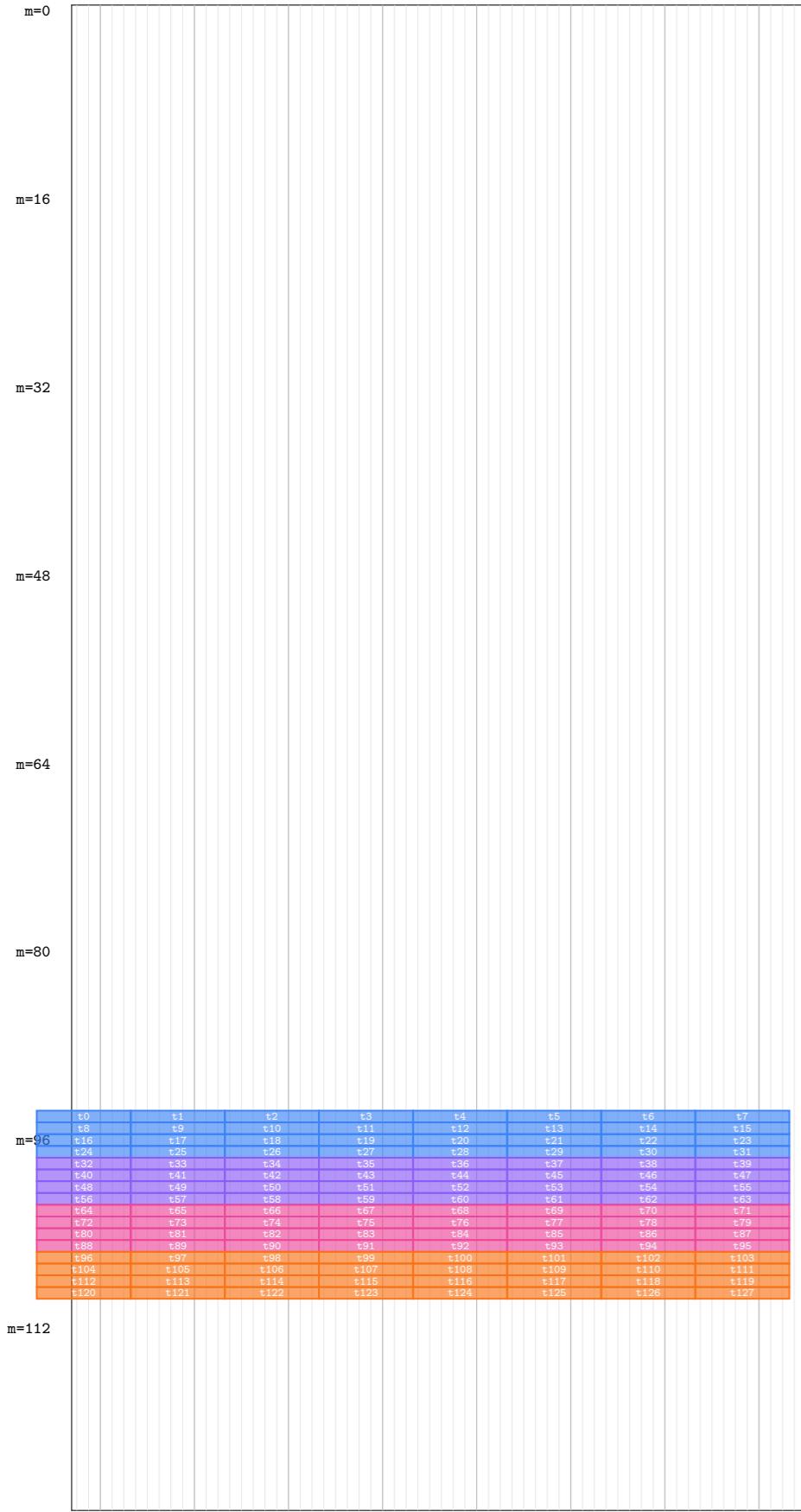
G2S A (m,k) pipe=2 k_tile=0 step 6/7 (cm=6, ck=0) (cpy_m=8, cpy_k=1) (V=8 half = 16B)

W0 W1 W2 W3

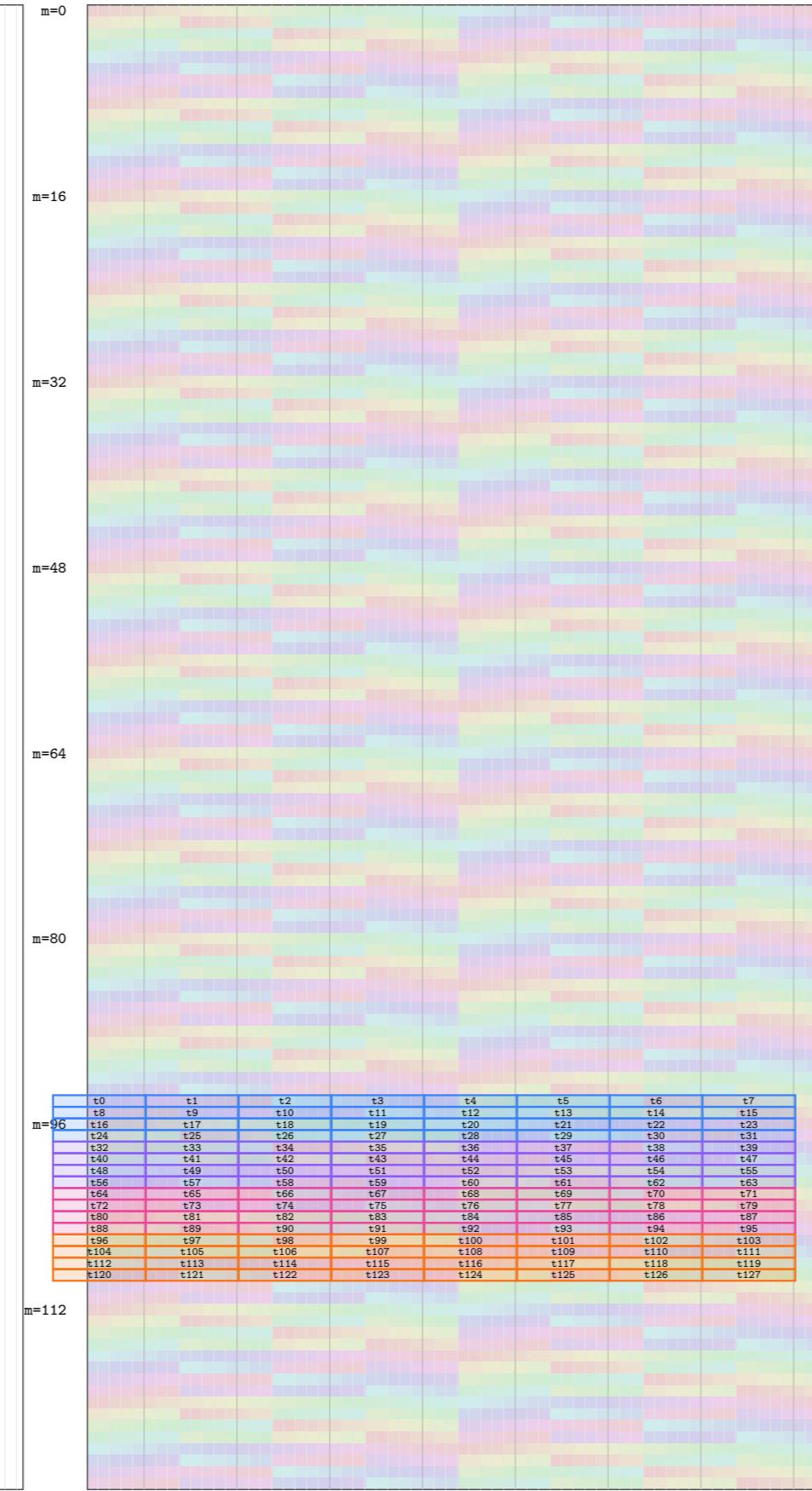
Overlay box = one thread cp.async. Text = tid.



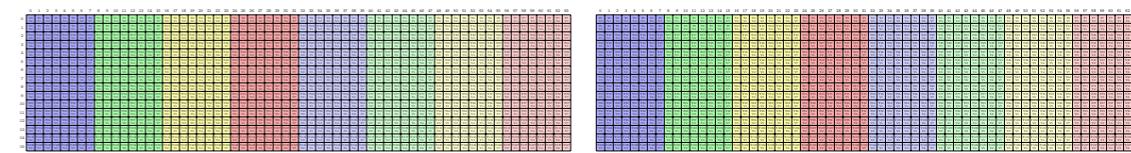
gA (m,k) CTA tile



sA (m,k) bank-colored



CUTE:



t0	t1	t2	t3	t4	t5	t6	t7
t8	t9	t10	t11	t12	t13	t14	t15
t16	t17	t18	t19	t20	t21	t22	t23
t24	t25	t26	t27	t28	t29	t30	t31
t32	t33	t34	t35	t36	t37	t38	t39
t40	t41	t42	t43	t44	t45	t46	t47
t48	t49	t50	t51	t52	t53	t54	t55
t56	t57	t58	t59	t60	t61	t62	t63
t64	t65	t66	t67	t68	t69	t70	t71
t72	t73	t74	t75	t76	t77	t78	t79
t80	t81	t82	t83	t84	t85	t86	t87
t88	t89	t90	t91	t92	t93	t94	t95
t96	t97	t98	t99	t100	t101	t102	t103
t104	t105	t106	t107	t108	t109	t110	t111
t112	t113	t114	t115	t116	t117	t118	t119
t120	t121	t122	t123	t124	t125	t126	t127

m=112

t0	t1	t2	t3	t4	t5	t6	t7
t8	t9	t10	t11	t12	t13	t14	t15
t16	t17	t18	t19	t20	t21	t22	t23
t24	t25	t26	t27	t28	t29	t30	t31
t32	t33	t34	t35	t36	t37	t38	t39
t40	t41	t42	t43	t44	t45	t46	t47
t48	t49	t50	t51	t52	t53	t54	t55
t56	t57	t58	t59	t60	t61	t62	t63
t64	t65	t66	t67	t68	t69	t70	t71
t72	t73	t74	t75	t76	t77	t78	t79
t80	t81	t82	t83	t84	t85	t86	t87
t88	t89	t90	t91	t92	t93	t94	t95
t96	t97	t98	t99	t100	t101	t102	t103
t104	t105	t106	t107	t108	t109	t110	t111
t112	t113	t114	t115	t116	t117	t118	t119
t120	t121	t122	t123	t124	t125	t126	t127

m=112