EEE104 – Digital Electronics (I) Lecture 4

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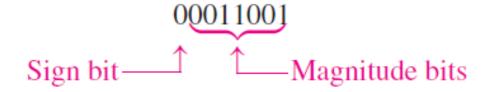
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In This Session

- Signed Numbers.
- Arithmetic Operations with Signed Numbers

 The left-most bit in a signed binary number is the sign bit.



- The sign bit is 0 is for a positive number, and is 1 is for a negative number.
- Digital systems such as computers usually use 2's complement system to represent signed numbers.
- The 2's complement of a number is calculated by inverting its bits and adding 1.

In the 2's complement system

 A positive number is represented as a zero sign bit followed by true binary magnitude bits, e.g. +25 is

 Negative numbers are the 2's complements of the corresponding positive numbers, e.g. -25 is 11100111.

```
original 00011001 (+25)

1's complement 11100110

2's complement 11100111 (-25)
```

In the 2's complement system

 Positive numbers are the 2's complements of the corresponding negative numbers.

```
-(-25) = +25
original 11100111 (-25)

1's complement 00011000

2's complement 00011001 (+25)
```

- We can add an infinite number of 0's to the left of a positive number and will not change its value, e.g. 011 (+3) = 00011 (+3).
- We can add an infinite number of 1's to the left of a negative number and will not change its value, e.g. 101 (-3) = 11101 (-3).

original	101 (-3)	11101 (-3)
1's complement	010	00010
2's complement	011 (+3)	00011 (+3)

The decimal value of signed binary numbers

- It is determined by summing the weights in all bit positions where there are 1s.
- The weight of the sign bit is calculated as that of a magnitude bit but given a negative value.

$$(-2^7)2^6 \ 2^5 \ 2^4 \ 2^3 \ 2^2 \ 2^1 \ 2^0$$

The decimal value of signed binary numbers

Positive number

$$64 + 16 + 4 + 2 = +86$$

Negative number

$$-128 + 32 + 8 + 2 = -86$$

Range of signed integer numbers

The number of different combinations of n bits is

Total combinations
$$= 2^n$$

e.g. 8 bits for 256 numbers.

The range of values for n-bit numbers is

Range =
$$-(2^{n-1})$$
 to $+(2^{n-1} - 1)$

e.g. 8 bits for -128 to +127.

Addition

Add the two numbers and discard any final carry bit.

Both numbers positive
$$00000111$$
 7 $+ 00000100$ $+ 4$ 11

Positive number with magnitude larger than 00001111 15 15 negative number $+ 11111010$ $+ -6$ $1 00001001$

Addition

Positive number with magnitude larger than negative number

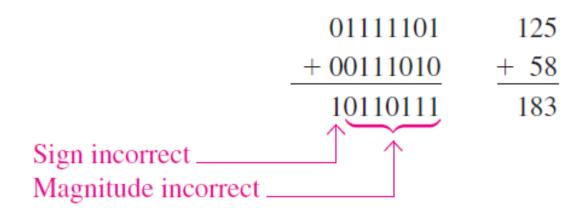
$$\begin{array}{r}
00010000 & 16 \\
+ 11101000 & + -24 \\
\hline
111111000 & -8
\end{array}$$

Both numbers negative 11111011
$$-5$$

 $+ 11110111$ $+ -9$
Discard carry \longrightarrow 1 11110010 -14

Addition

- Overflow: when two numbers are added, the number of bits required to represent the sum exceeds the number of bits in the two numbers.
- It occurs only when both numbers are positive or negative.



Subtraction

- Subtraction is implemented through addition.
- Change the sign of the subtrahend and add to the minuend, e.g. subtracting +6 is equivalent to adding -6.
- The sign of a binary number is changed by taking its 2's complement.

minuend

subtrahend difference

Subtraction

```
00001000 - 00000011
In this case, 8 - 3 = 8 + (-3) = 5.
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00001000 \qquad \text{Minuend (+8)}
+ 11111101 \qquad \text{2's complement of subtrahend (-3)}
\text{Discard carry} \longrightarrow 1 \ 00000101 \qquad \text{Difference (+5)}
```

$$00001100 - 1\overline{1}110111$$

In this case,
$$12 - (-9) = 12 + 9 = 21$$
.

Multiplication — Partial Products Method

- Compute the magnitude product of corresponding positive numbers.
- Attach a 0 sign bit. If the signs of the two numbers are different (negative product), take the 2's complement of the outcome.

Multiplication

Multiply the signed number 01010011 (+83) and 11000101 (-59).

Step 1

 $11000101 \longrightarrow 00111011$

Step 3

0 1001100100001 (+4897)

1 0110011011111 (-4897)

Step 2 1010011 $\times 0111011$ 1010011

+ 1010011 11111001

+ 0000000 011111001

+ 1010011

1110010001

+ 1010011

100011000001

+ 1010011

1001100100001

 $+ \frac{0000000}{1001100100001}$

Multiplicand

Multiplier

1st partial product

2nd partial product

Sum of 1st and 2nd

3rd partial product

Sum

4th partial product

Sum

5th partial product

Sum

6th partial product

Sum

7th partial product

Final product

Division - accomplished using subtraction in computers:

- 1. Initialize the quotient to zero.
- 2. Subtract the divisor from the dividend or previous partial remainder. If the partial remainder is:
 - positive, add 1 to the quotient and repeat.
 - zero, add 1 to the quotient and finish.
 - negative, finish.
- 3. Determine the sign of the quotient by checking the signs of the dividend and divisor.

 dividend

divisor

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Division	01100100	Dividend
Divide 01100100 (+100)	+ 11100111	2's complement of divisor
by 00011001 (+25)	01001011	Positive 1st partial remainder
	01001011	1st partial remainder
The result is	+ 11100111	2's complement of divisor
	00110010	Positive 2nd partial remainder
00000100 (+4)		
	00110010	2nd partial remainder
	+ 11100111	2's complement of divisor
	00011001	Positive 3rd partial remainder
	00011001	3rd partial remainder
	+ 11100111	2's complement of divisor
	00000000	Zero remainder